Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

# 2SK2603

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain–source ON resistance  $: RDS (ON) = 3.0 \Omega (typ.)$ • High forward transfer admittance  $: |Y_{fs}| = 2.6 S (typ.)$ • Low leakage current  $: IDSS = 100 \mu A (max) (VDS = 640 V)$ • Enhancement mode  $: V_{th} = 2.0 \sim 4.0 V (VDS = 10 V, ID = 1 mA)$ 

#### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	800	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	800	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	ΙD	3	Α	
	Pulse (Note 1)	I <sub>DP</sub>	9	A	
Drain power dissipation	n (Tc = 25°C)	$P_{D}$	100	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	300	mJ	
Avalanche current		I <sub>AR</sub>	3	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	10.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

1. GATE
2. DRAIN (HEAT SINK)
3. SOURCE

1. O. 20 AB

JEDEC TO-220AB

JETA SC-46

TOSHIBA 2-10P1B

Weight: 2.0 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.25	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 60.0 mH,  $I_{AR} = 6 \text{ A}$ ,  $R_G = 25 \Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



### **Electrical Characteristics (Ta = 25°C)**

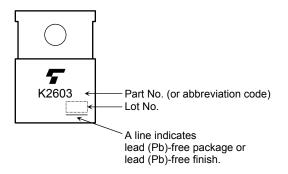
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bro	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	800	_	_	V
Gate threshold v	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A,	_	3.0	3.6	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 1.5 A	0.65	2.6	_	S
Input capacitano	e	C <sub>iss</sub>		_	750	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	10	_	pF
Output capacitance		Coss	]	_	70	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 1.5 \text{ A} \\ V_{GS} = 1.5 \text{ A} \\ V_{OUT} = 1.5 \text{ A} \\ V_{OUT} = 1.33 \Omega$ $V_{DD} = 200 \text{ V}$	_	15	_	
	Turn-on time	t <sub>on</sub>		_	55	_	20
	Fall time	t <sub>f</sub>		_	30	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	_	110	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	25		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		13		nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>			12	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

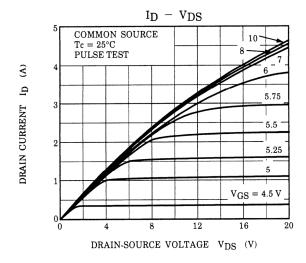
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	3	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	9	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 3 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 3 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 100 A / μs		900	_	ns
Reverse recovery charge	Q <sub>rr</sub>	1DR - 3 A, VGS - 0 V, αιDR / αι - 100 A / μS		6	_	μC

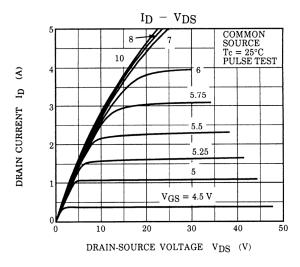
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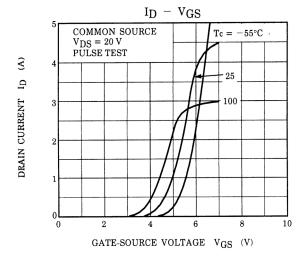
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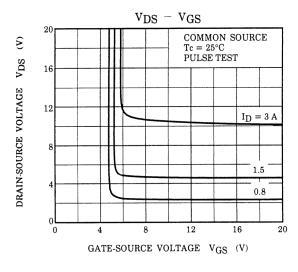


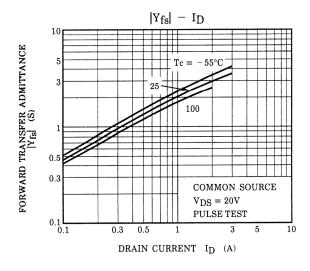
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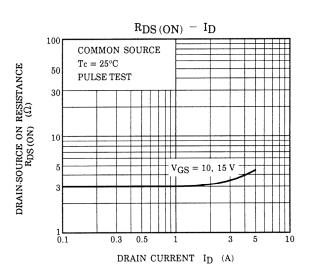


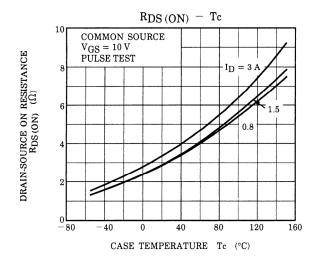


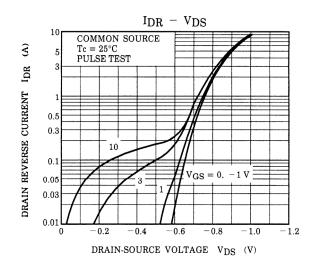


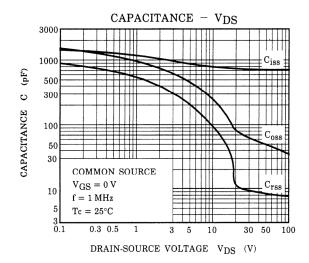


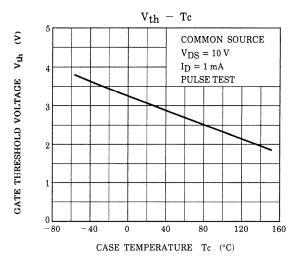


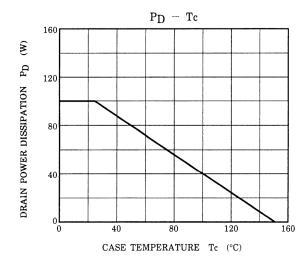


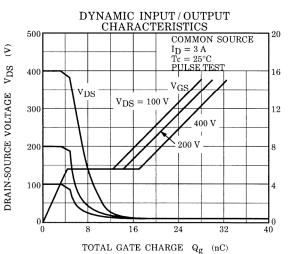




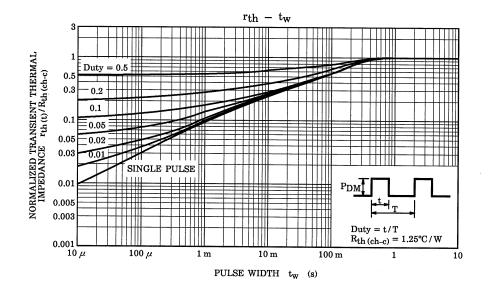


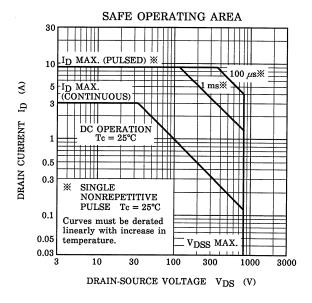


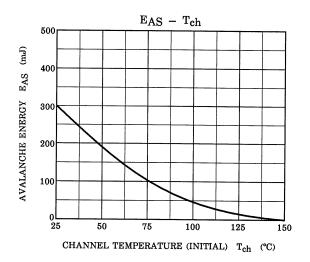


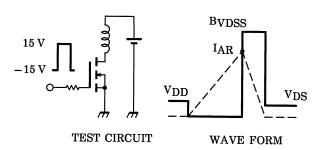


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$$\begin{aligned} RG &= 25~\Omega \\ V_{DD} &= 90~V,~L = 60~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right) \end{aligned}$$

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