International TOR Rectifier

HFA16PB120PbF

Ultrafast, Soft Recovery Diode

HEXFRED™

Features

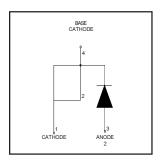
- Ultrafast Recovery
- Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- · Specified at Operating Conditions
- Lead-Free

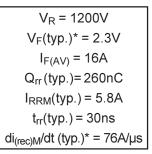
Benefits

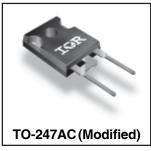
- · Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- · Higher Frequency Operation
- · Reduced Snubbing
- · Reduced Parts Count

Description

International Rectifier's HFA16PB120 is a state of the art ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 volts and 16 amps continuous current, the HFA16PB120 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA16PB120 is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.







Absolute Maximum Ratings

	Parameter	Max	Units
V _R	Cathode-to-Anode Voltage	1200	V
I _F @ T _C = 25°C	Continuous Forward Current		
I _F @ T _C = 100°C	Continuous Forward Current	16	A
I _{FSM}	Single Pulse Forward Current	190	^
I _{FRM}	Maximum Repetitive Forward Current	64	
P _D @ T _C = 25°C	Maximum Power Dissipation	151	w
P _D @ T _C = 100°C	Maximum Power Dissipation	60	
TJ	Operating Junction and	55.4	
T _{STG}	Storage Temperature Range	-55 to +150	0℃

^{* 125°}C



Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions		
V_{BR}	Cathode Anode Breakdown Voltage	1200			V	$I_R = 100 \mu A$		
V _{FM}	Max Forward Voltage		2.5	3.0	V	I _F = 16A		
			3.2	3.93		I _F = 32A	See Fig. 1	
			2.3	2.7		I _F = 16A, T _J = 125°C		
I _{RM}	Max Reverse Leakage Current		0.75	20	μΑ	V _R = V _R Rated	See Fig. 2	
			375	2000		$T_J = 125$ °C, $V_R = 0.8 \times V_R$ Rated		
C _T	Junction Capacitance		27	40	pF	V _R = 200V	See Fig. 3	
L _S	Series Inductance		8.0		nH	Measured lead to lead 5mm from package body		

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

-	-	-		•		-	•
	Parameter	Min	Тур	Max	Units	Test Conditions	
t _{rr}	Reverse Recovery Time		30			$I_F = 1.0A$, $di_f/dt = 200A/\mu s$, $V_R = 30$	
t _{rr1}	See Fig. 5, 10		90	135	ns	T _J = 25°C	
t _{rr2}			164	245		T _J = 125°C	$I_{F} = 16A$
I _{RRM1}	Peak Recovery Current		5.8	10	Α	T _J = 25°C	
I _{RRM2}	See Fig. 6		8.3	15	_ ^	$T_J = 125$ °C	$V_{R} = 200V$
Q _{rr1}	Reverse Recovery Charge		260	675	nC	T _J = 25°C	
Q _{rr2}	See Fig. 7		680	1838	IIC	T _J = 125°C	$di_f/dt = 200A/\mu s$
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current		120		A/µs	T _J = 25°C	
di _{(rec)M} /dt2	During t _b See Fig. 8		76		Λίμο	T _J = 125°C	

Thermal - Mechanical Characteristics

	Parameter	Min	Тур	Max	Units
T _{lead} ①	Lead Temperature			300	°C
R _{thJC}	Thermal Resistance, Junction to Case			0.83	
R _{thJA} ②	Thermal Resistance, Junction to Ambient			80	K/W
R _{thCS} ③	Thermal Resistance, Case to Heat Sink		0.50		
Wt	Weight		2.0		g
			0.07		(oz)
	Mounting Torque	6.0		12	Kg-cm
	Mounting Forque	5.0		10	lbf•in

① 0.063 in. from Case (1.6mm) for 10 sec

② Typical Socket Mount

³ Mounting Surface, Flat, Smooth and Greased

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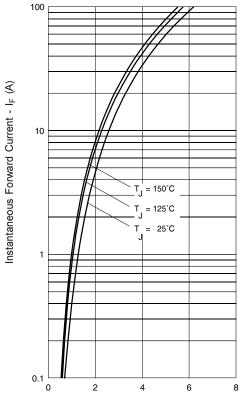


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

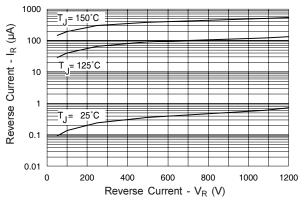


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

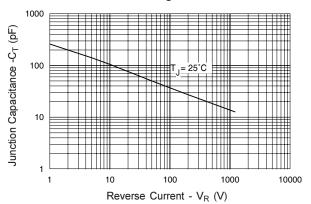
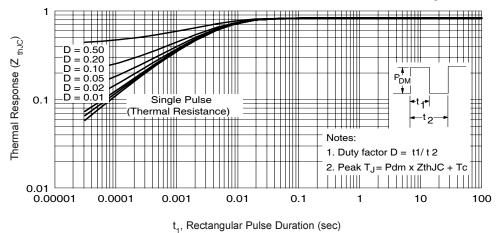


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



 $\textbf{Fig. 4} - \text{Maximum Thermal Impedance Z_{thjc} Characteristics}$

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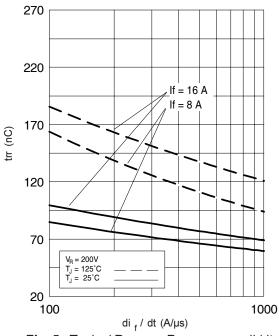


Fig. 5 - Typical Reverse Recovery vs. di_f/dt, (per Leg)

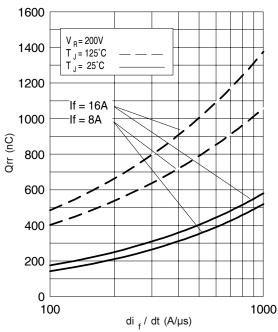


Fig. 7 - Typical Stored Charge vs. di_f/dt, (per Leg)

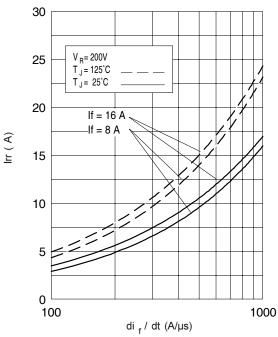


Fig. 6 - Typical Recovery Current vs. di_f/dt, (per Leg)

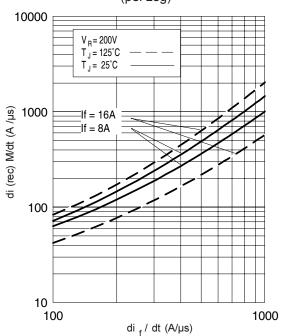


Fig. 8 - Typical di_{(rec)M}/dt vs. di_f/dt, (per Leg)

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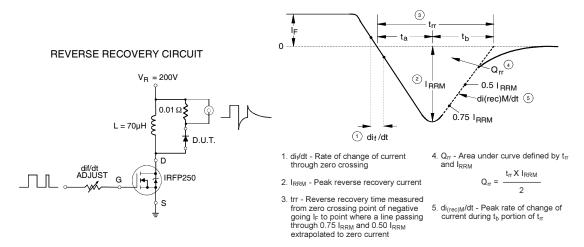
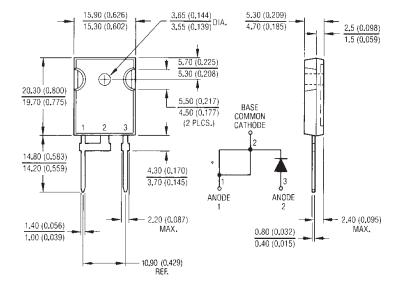


Fig. 9 - Reverse Recovery Parameter Test Circuit

Fig. 10 - Reverse Recovery Waveform and Definitions

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Conforms to JEDEC Outline TO-247AC (Modified) Dimensions in millimeters and inches

Note: Marking "P" indicates Lead-Free

Data and specifications subject to change without notice.



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