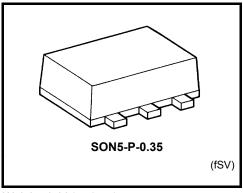
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH126FS

Bus Buffer with 3-STATE Output

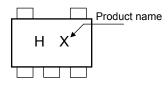
Features

- High speed: t_{pd} = 3.8 ns (typ.) at V_{CC} = 5 V, 15pF
- Low power dissipation: I_{CC} = 2 μA (max) at Ta = 25°C
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Wide operating voltage range: V_{CC} (opr.) = 2~5.5 V
- 5.5-V tolerant input

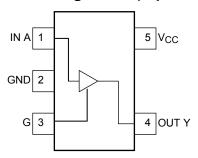


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



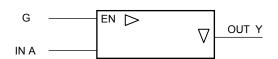
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~7	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	lik	-20	mA
Output diode current	I _{OK}	±20	mA
DC output current	I _{OUT}	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P_{D}	50	mW
Storage temperature	T _{stg}	-65~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

IEC Logic Symbol



Truth Table

G	Α	Υ
L	Х	Z
Н	L	L
Н	Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	$0\sim100~(V_{CC}=3.3~V\pm0.3~V)$	ns/V	
input rise and rail time	ui/uv	$0 \sim 20 \; (V_{CC} = 5 \; V \pm 0.5 \; V)$	113/ V	

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			1	Γa = 25°0	0	Ta = -40~85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level				2.0	1.5	_	_	1.5	_	
input voltage	V _{IH}		_	3.0~5.5	V _{CC} × 0.7	١	_	V _{CC} × 0.7		V
Low-level				2.0	_		0.5	_	0.5	
input voltage	V_{IL}		_	3.0~5.5	_	١	V _{CC} × 0.3	_	V _{CC} × 0.3	V
				2.0	1.9	2.0	_	1.9	_	V
High lovel		V _{IN} = V _{IH}	$I_{OH} = -50 \mu A$	3.0	2.9	3.0	_	2.9	_	
High-level output voltage	V _{OH}	VIN = VIH		4.5	4.4	4.5	_	4.4	_	
			I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
		V _{OL} V _{IN} = V _{IH} or V _{IL}	Ι _{ΟL} = 50 μΑ	2.0	_	0	0.1	_	0.1	V
Low-level VOL output voltage				3.0	_	0	0.1	_	0.1	
	V_{OL}			4.5	_	0	0.1	_	0.1	
			$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44	
			$I_{OL} = 8 \text{ mA}$	4.5	_		0.36	_	0.44	
3-state output off-state current	loz	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		5.5	_	_	±0.25	_	±2.5	μΑ
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5		_	±0.1		±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} (or GND	5.5	_	_	2.0	_	20	μА

3 2007-11-01

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Symbol Symbol	Symbol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
			3.3 ± 0.3	15	_	5.6	8.0	1.0	9.5	
Propagation delay	t_{pLH}		3.3 ± 0.3	50	_	8.1	11.5	1.0	13.0	ns
time	t_{pHL}	_	E 0 + 0 E	15	_	3.8	5.5	1.0	6.5	115
			5.0 ± 0.5	50	_	5.3	7.5	1.0	8.5	
	t _p zL		3.3 ± 0.3	15	_	5.4	8.0	1.0	9.5	
3-state output				50	_	7.9	11.5	1.0	13.0	
enable time		5.0 ± 0.5	15	_	3.6	5.1	1.0	6.0	ns	
			5.0 ± 0.5	50	_	5.1	7.1	1.0	8.0	
3-state output	t _{pLZ}		3.3 ± 0.3	50	_	9.5	13.2	1.0	15.0	no
disable time	t_{pHZ}	_	5.0 ± 0.5	50	_	6.1	8.8	1.0	10.0	ns
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Output capacitance	C _{OUT}		_			6			_	pF
Power dissipation capacitance	C _{PD}			(Note)	_	15	_	_		pF

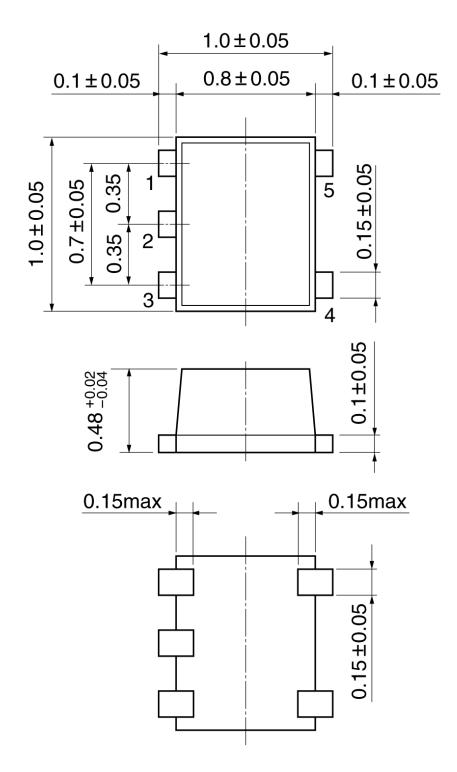
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

4

Average operating current can be obtained by the equation:

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions



Weight: 0.001 g (typ.)

5 2007-11-01

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in his document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patents or other rights of TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS
 compatibility. Please use these products in this document in compliance with all applicable laws and regulations
 that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses
 occurring as a result of noncompliance with applicable laws and regulations.