TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

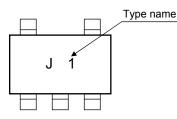
TC7SZ00F,TC7SZ00FU

2 Input NAND Gate

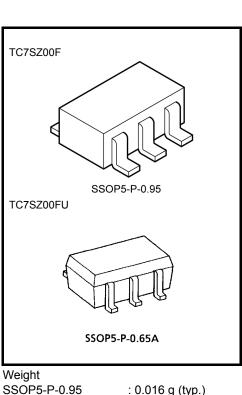
Features

- High output drive: ±24 mA (min) at V_{CC} = 3 V
- Super high speed operation: t_{pd} = 2.4 ns (typ.)
 - at V_{CC} = 5 V, 50 pF
- Operation voltage range: V_{CC (opr)} = 1.8~5.5 V
- 5.5-V tolerant inputs
- 5.5-V power down protection outputs
- · Matches the performance of TC74LCX series when operated at 3.3-V Vcc

Marking



Absolute Maximum Ratings (Ta = 25°C)



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SSOP5-P-0.95	: 0.016 g (typ.)
SSOP5-P-0.65A	: 0.006 g (typ.)

Pin Assignment (top view)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	Vout	-0.5~6	V
Input diode current	lік	-20	mA
Output diode current	I _{OK}	-20	mA
DC output current	IOUT	±50	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10s)	TL	260	°C

IN B 5 V_{CC} 1 IN A 2 4 OUT Y GND 3

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Logic Diagram



Truth Table

Inp	out	Output
А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.8~5.5	V
		1.5~5.5 (Note 1)	v
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~5.5 (Note 2)	V
		0~V _{CC} (Note 3)	v
Operating temperature	T _{opr}	-40~85	°C
	dt/dv	0~20 (V_{CC} = 1.8 V, 2.5 V \pm 0.2 V)	ns/V
Input rise and fall time		0~10 (V_{CC} = 3.3 V \pm 0.3 V)	
		0~5 (V _{CC} = 5.5 V \pm 0.5 V)	

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or Low state

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		Condition		Ta = 25°C			Ta = -40~85°C		Unit	
		V _{CC} (V)		Min	Тур.	Max	Min.	Max.	Unit	
High-level	VIH			1.8	V _{CC} × 0.88	_	_	V _{CC} × 0.88	_	
input voltage	VН		_	2.3~5.5	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	V
Low-level	Ma			1.8	_	_	V _{CC} × 0.12	_	V _{CC} × 0.12	v
input voltage	VIL		—	2.3~5.5	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
				1.8	1.7	1.8	—	1.7	—	
			I _{OH} = –100 μA	2.3	2.2	2.3	_	2.2	_	-
			ΙΟΗ = - ΙΟΟ μΑ	3.0	2.9	3.0	_	2.9	_	
High-level	V _{ОН}	$V_{IN} = V_{IH}$		4.5	4.4	4.5	_	4.4	_	
output voltage	VOH	or VIL	I _{OH} = -8 mA	2.3	1.9	2.15	—	1.9	_	
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
			I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3		
			I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8		V
	M		I _{OL} = 100 μΑ	1.8	_	0	0.1	_	0.1	
				2.3	_	0	0.1	_	0.1	
				3.0		0	0.1	_	0.1	
Low-level				4.5		0	0.1	_	0.1	
output voltage	V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			$I_{OL} = 16 \text{ mA}$ $I_{OL} = 24 \text{ mA}$ $I_{OL} = 32 \text{ mA}$	3.0	_	0.15	0.4	—	0.4	
				3.0	—	0.22	0.55	_	0.55	
				4.5	_	0.22	0.55	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5	—		±1	_	±10	μA
Power off leakage current	IOFF	V_{IN} or $V_{OUT} = 5.5 V$		0.0	_		1	_	10	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		5.5	—	_	2	_	20	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symb		Test Condition	_	Ta = 25°C		Ta = -4	Unit		
Characteristics	Characteristics Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min.	Max.	Unit
			1.8	2.0	4.5	9.5	2.0	10.0	
		C _L = 15 pF,	2.5 ± 0.2	0.8	3.0	6.5	0.8	7.0	
Propagation delay time	t _{pLH}	$R_L = 1 M\Omega$	3.3 ± 0.3	0.5	2.4	4.5	0.5	4.7	ns
	t _{pHL}		5.0 ± 0.5	0.5	2.0	3.9	0.5	4.1	115
		C _L = 50 pF,	3.3 ± 0.3	1.5	2.9	5.0	1.5	5.2	
		$R_L = 500 \ \Omega$	5.0 ± 0.5	0.8	2.4	4.3	0.8	4.5	
Input capacitance	C _{IN}	—	0~5.5	_	4	_		_	pF
Dower dissinction expectance	C==	(Note 4)	3.3	_	24			_	۳E
Power dissipation capacitance	C _{PD}	(Note 4)	5.5	_	30			_	pF

Note 4: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

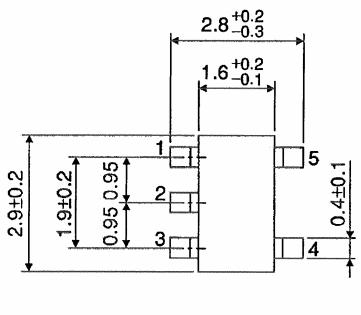
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

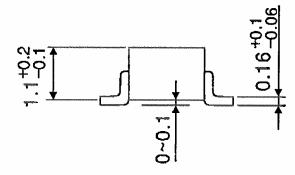
<u>TOSHIBA</u>

Package Dimensions

SSOP5-P-0.95

Unit : mm

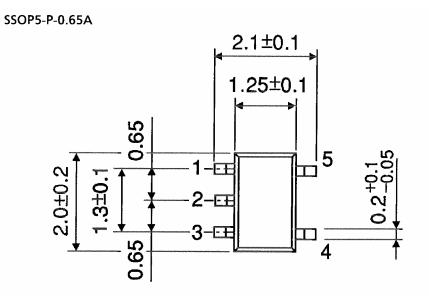




Weight: 0.016 g (typ.)

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Package Dimensions



0.9±0.1

Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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