

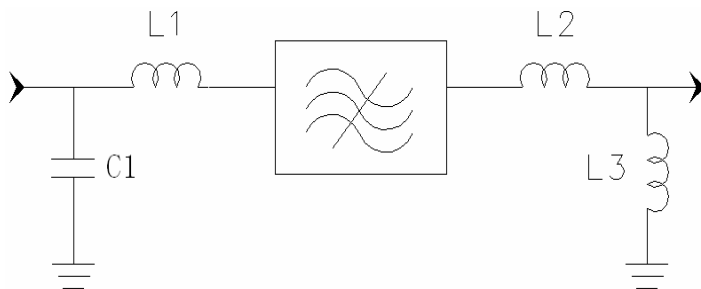
### Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	138.585	138.625	138.665
Insertion Loss	dB	-	9.5	11
1 dB Bandwidth	MHz	0.5	0.53	-
3 dB Bandwidth	MHz	-	0.86	-
40 dB Bandwidth	MHz	-	1.98	2.10
Passband Variation	dB	-	0.3	0.6
Absolute Delay	usec	-	1.88	-
Phase Linearity	deg	-	1.8	-
Group Delay Variation	nsec	-	80	-
Ultimate Rejection				
$f_0 \pm 2.5\text{MHz}$	dB	37	41	-
$f_0 \pm 20\text{MHz}$	dB	40	45	-
Substrate Material			Quartz	
Ambient Temperature	°C		25	
Package Size			SF-26	

#### Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

### Matching Configuration




L1=82nH L2=56nH

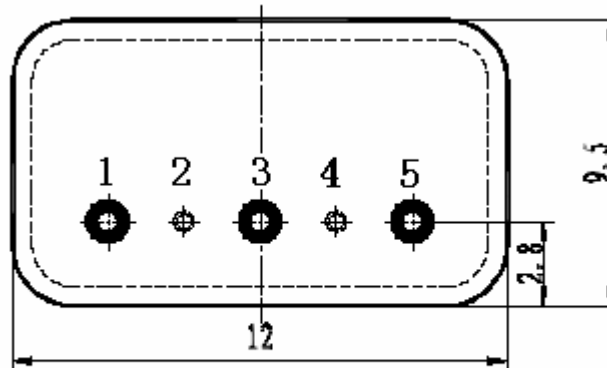
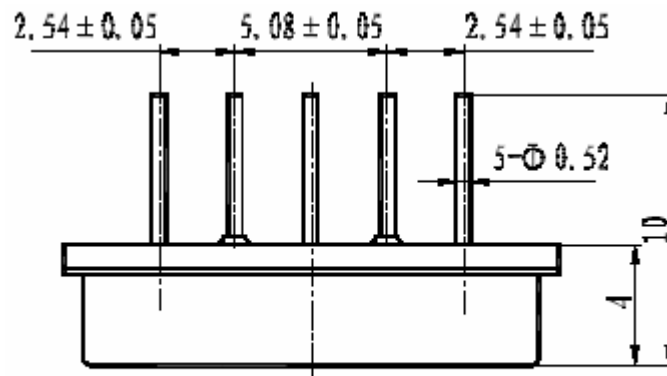
L3=33nH C1=47pF

**Source/Load Impedance=50 ohm**

Notes - Component values may change depending on board layout.

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*Package Dimension*



Pin 1:input  
pin 5:Output

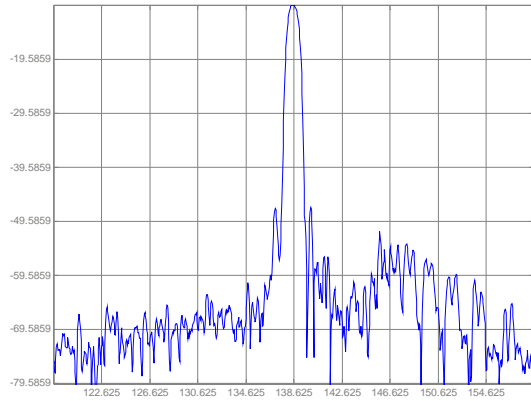


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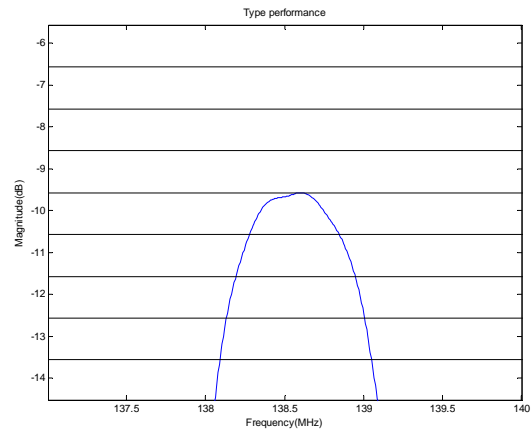
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Typical Performance

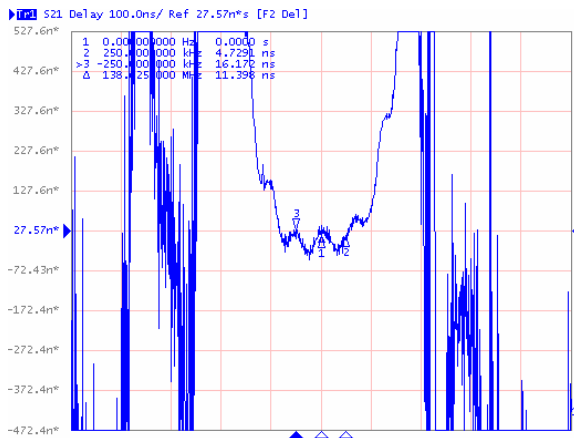
Frequency Respond



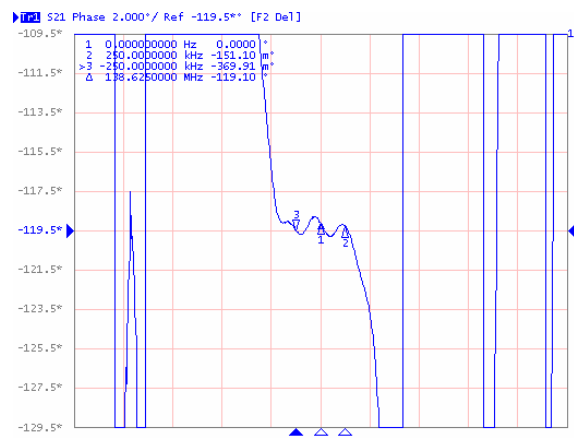
Passband Respond



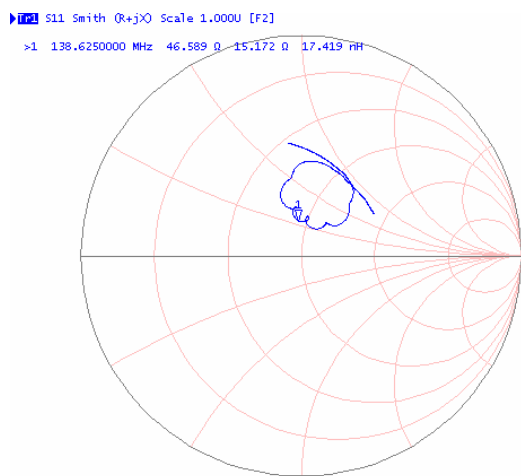
Group Delay Variation( $f_0 \pm 250\text{kHz}$ )



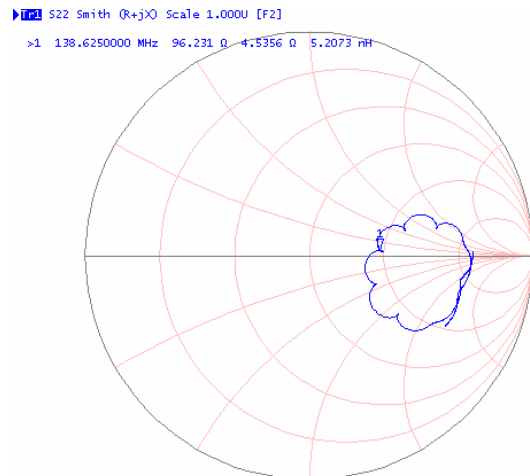
Phase Linearity( $f_0 \pm 250\text{kHz}$ )



Simth Chart S11



Simth Chart S22



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