

Silicon NPN Power Transistors

2SC2830

DESCRIPTION

- With TO-3 package
- High voltage ,high speed
- Wide area of safe operation

APPLICATIONS

- For switching regulator applications

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

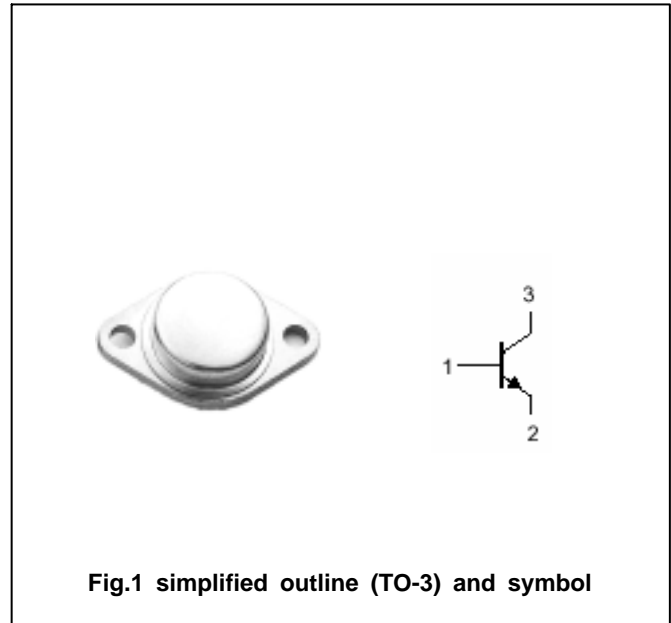


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings($T_a=25$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	500	V
V_{CEO}	Collector-emitter voltage	Open base	400	V
V_{EBO}	Emitter-base voltage	Open collector	7	V
I_C	Collector current		20	A
P_T	Total power dissipation	$T_{mb}=25$	200	W
T_j	Junction temperature		200	
T_{stg}	Storage temperature		-65~200	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j-mb}$	Thermal resistance from junction to mounting base	1.0	/W

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.1A ; I _B =0	400			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =12A; I _B =2.4A			1.2	V
V _{BEsat}	Base-emitter saturation voltage	I _C =12A; I _B =2.4A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =500V; I _E =0			0.1	mA
I _{CEO}	Collector cut-off current	V _{CE} =400V; I _B =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =7V; I _C =0			0.1	mA
h _{FE-1}	DC current gain	I _C =2.4A ; V _{CE} =5V	15		50	
h _{FE-2}	DC current gain	I _C =12A ; V _{CE} =5V	10			

PACKAGE OUTLINE

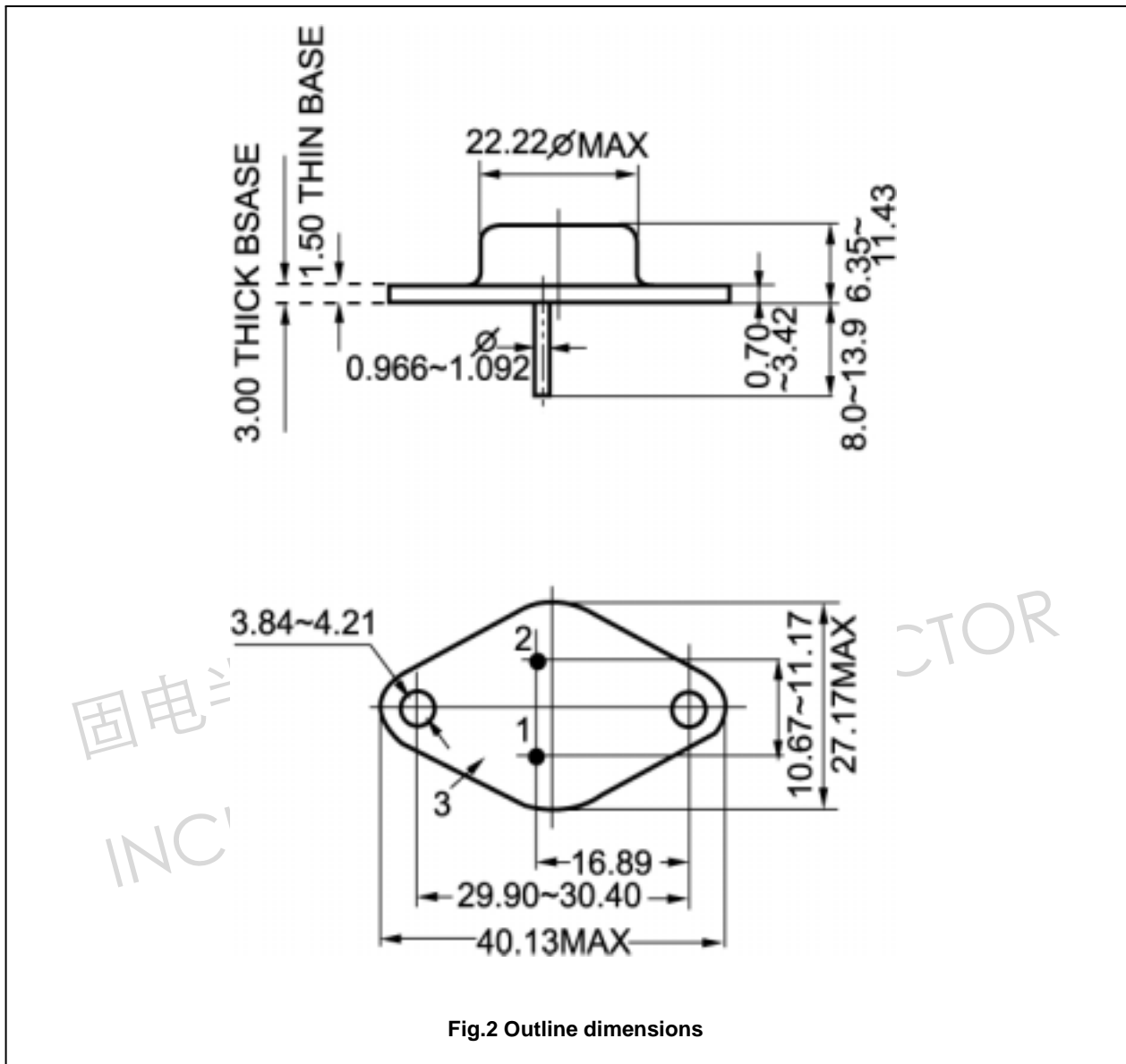


Fig.2 Outline dimensions