HSM126S

Silicon Schottky Barrier Diode for System Protection

REJ03G0174-0400Z
(Previous: ADE-208-111C)
Rev.4.00
Jan.28.2004

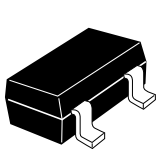
Features

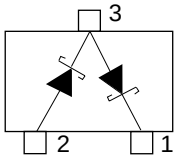
- HSM126S which is connected in series configuration enable to protect electric systems from miss-operation against external + and – surge.
- Low V_F and low leakage current.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSM126S	S14	MPAK

Pin Arrangement





(Top View)

1. Cathode 2
2. Anode 1
3. Cathode 1
   Anode 2

Absolute Maximum Ratings *3

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	20	V
Average forward current	I_O *1	200	mA
Non-Repetitive peak forward surge current	I_{FSM} *2	2	A
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Notes: 1. Sine wave, Two device total
2. 50 Hz half sine wave 1 pulse
3. Per one device

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_R	—	—	2.0	μA	V_R = 5 V
Forward voltage	V_F	—	—	0.35	V	I_F = 10 mA
Capacitance	C	—	40	—	pF	V_R = 0 V, f = 1 MHz

Note: 1. Per one device

Main Characteristic

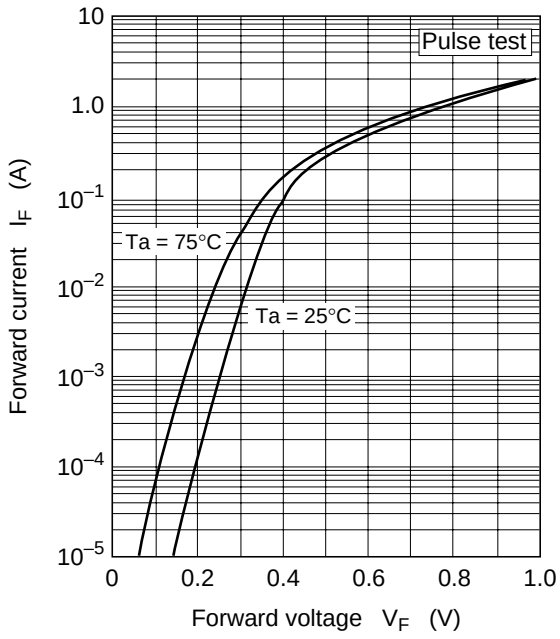


Fig.1 Forward current vs. Forward voltage

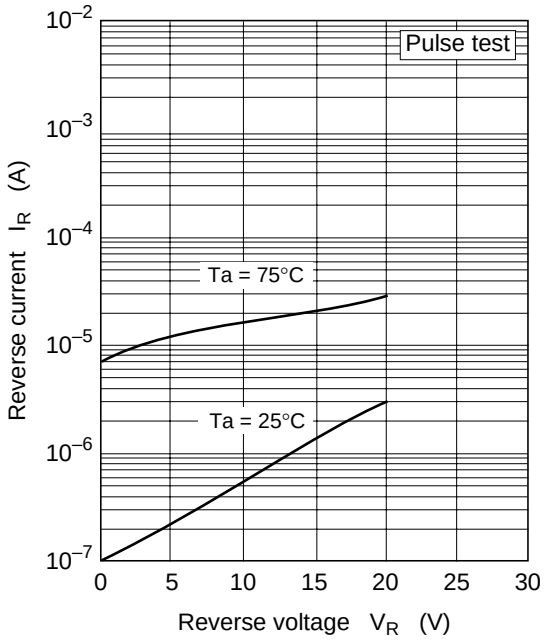


Fig.2 Reverse current vs. Reverse voltage

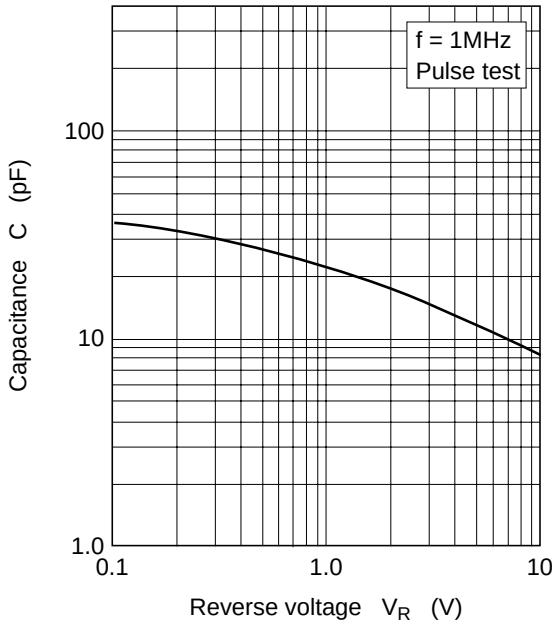
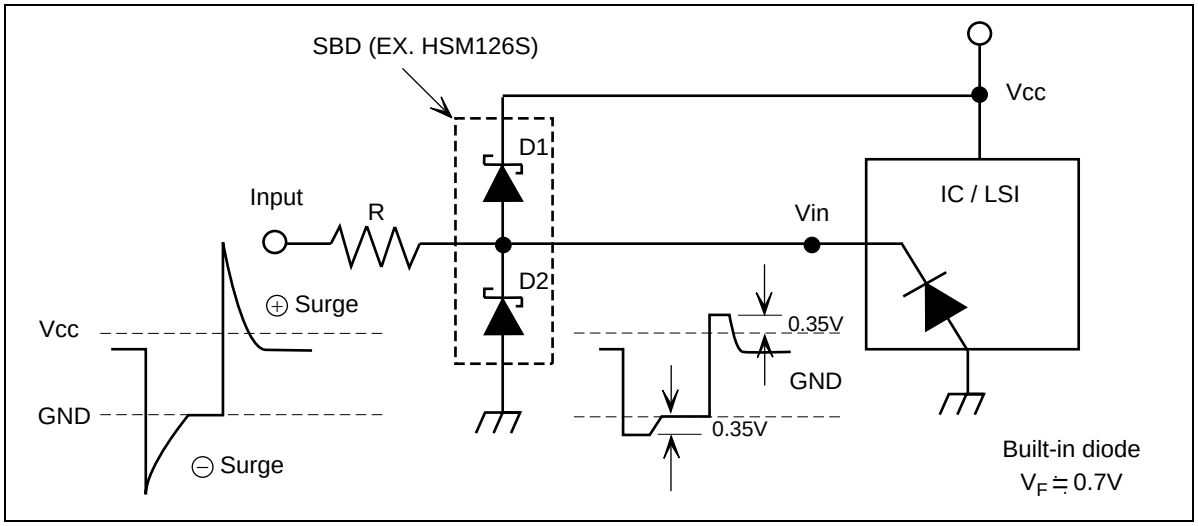


Fig.3 Capacitance vs. Reverse voltage

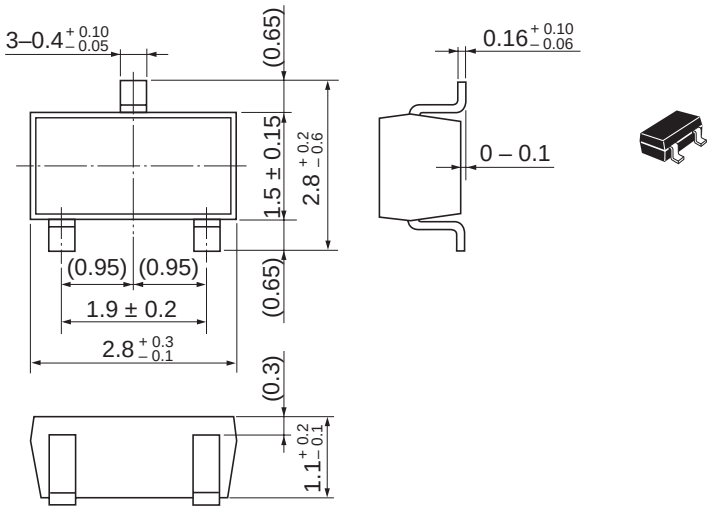
Example of application circuite



Package Dimensions

As of January, 2003

Unit: mm



Package Code	MPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.011 g

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