

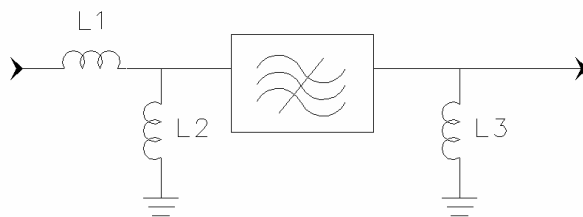
Specifications

| Parameter | Unit | Minimum | Typical | Maximum |
|---------------------|------|---|---------|---------|
| Center Frequency | MHz | 159.92 | 160 | 160.08 |
| Insertion Loss | dB | | 26.5 | 28 |
| 3dB Bandwidth | MHz | 2.8 | 2.809 | |
| 40 dB Bandwidth | MHz | | 3.28 | 3.31 |
| 50 dB Bandwidth | MHz | | 3.475 | 3.5 |
| Passband Variation | dB | | 0.5 | 1 |
| Absolute Delay | usec | | 4.86 | 4.89 |
| Ultimate Rejection | dB | 50 | 55 | |
| Substrate Material | | Quartz | | |
| Ambient Temperature | °C | 25 | | |
| Package Size | | DIP3512 (35.0x12.8x4.7mm ³) | | |

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=27nH L2=27nH

L3=18nH

Source/Load Impedance=50 ohm

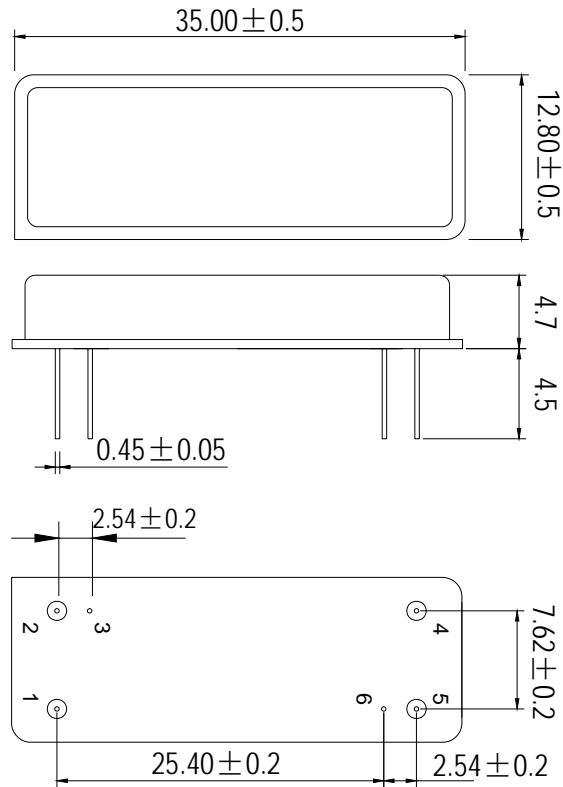
Notes - Component values may change depending on board layout.



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| | | | |
|-------------|----------|----------|--|
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Package Dimension

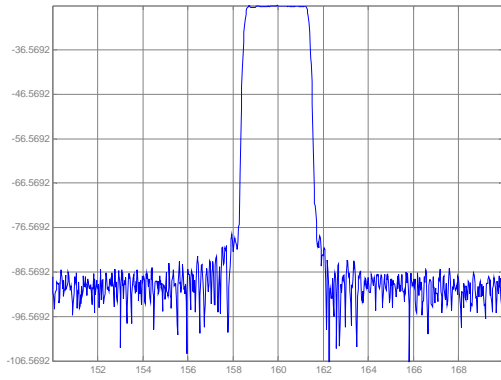


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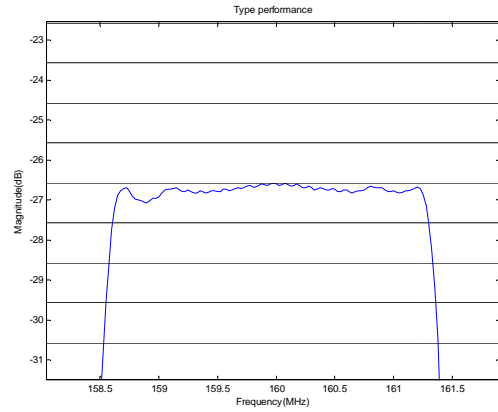
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Typical Performance

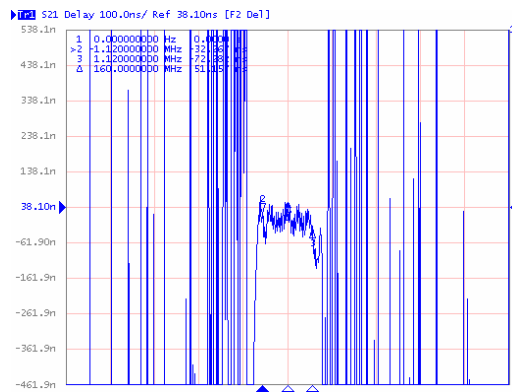
Frequency Respond



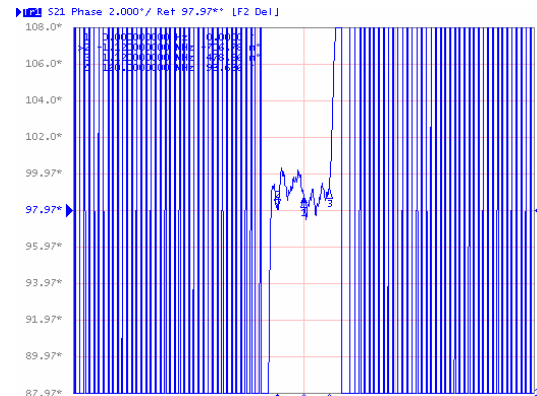
Passband Respond



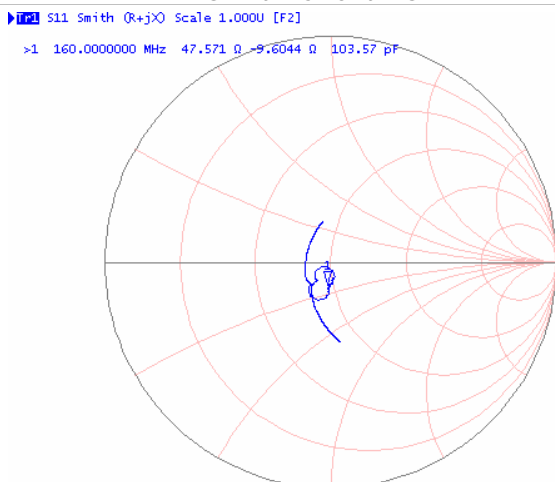
Group Delay Variation($f_0 \pm 1.12\text{MHz}$)



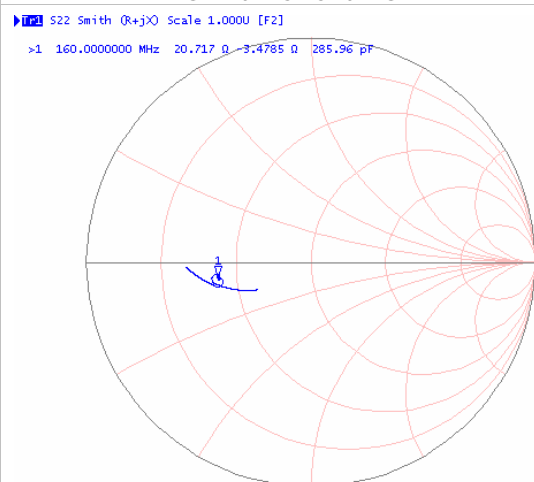
Phase Linearity($f_0 \pm 1.12\text{MHz}$)



Simth Chart S11



Simth Chart S22



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