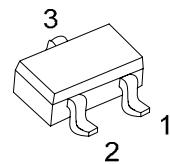
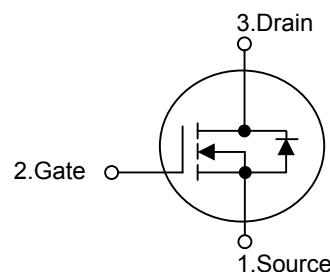


UT2302**Power MOSFET****N-CHANNEL
ENHANCEMENT MODE****■ DESCRIPTION**

The UT2302 is N-channel Power MOSFET, designed with high density cell, with fast switching speed, ultra low on-resistance ,excellent thermal and electrical capabilities . Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters .



SOT-23

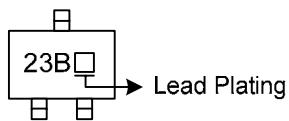
■ SYMBOL

*Pb-free plating product number: UT2302L

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Normal	Lead Free Plating		1	2	3	
UT2302-AE3-R	UT2302L-AE3-R	SOT-23	S	G	D	Tape Reel

UT2302L-AE3-R	(1)Packing Type (2)Package Type (3)Lead Plating	(1) R: Tape Reel (2) AE3: SOT-23 (3) L: Lead Free Plating, Blank: Pb/Sn
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■ MARKING

■ ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V_{DSS}	20	V	
Gate-Source Voltage	V_{GSS}	± 8	V	
Continuous Drain Current	I_D	2.4	A	
Pulsed Drain Current	I_{DM}	10	A	
Power Dissipation	$T_a=25^\circ C$	P_D	1.25	W
	$T_a=70^\circ C$		0.8	W
Junction Temperature	T_J	+150		
Storage Temperature	T_{STG}	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Junction to Ambient (Note 3)	θ_{JA}			100	/W

■ ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ C$, unless otherwise specified)

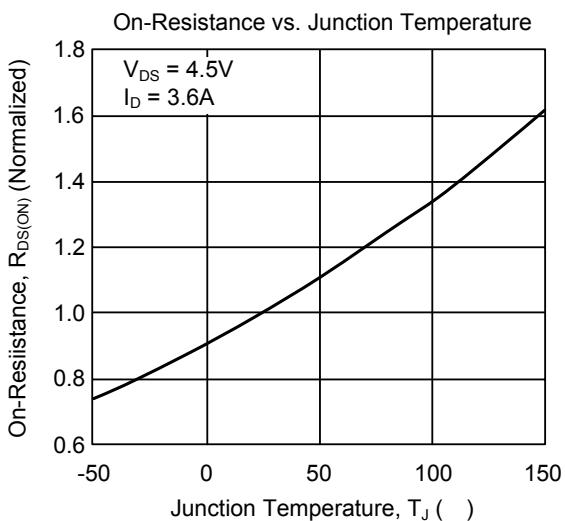
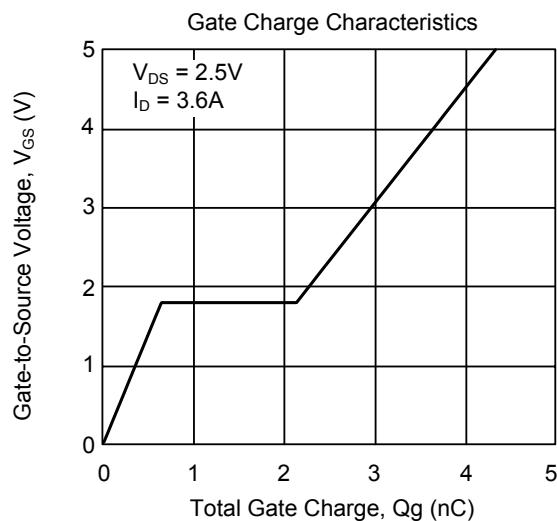
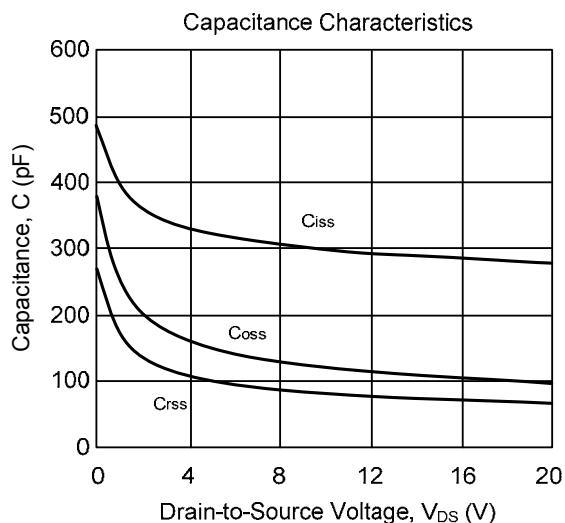
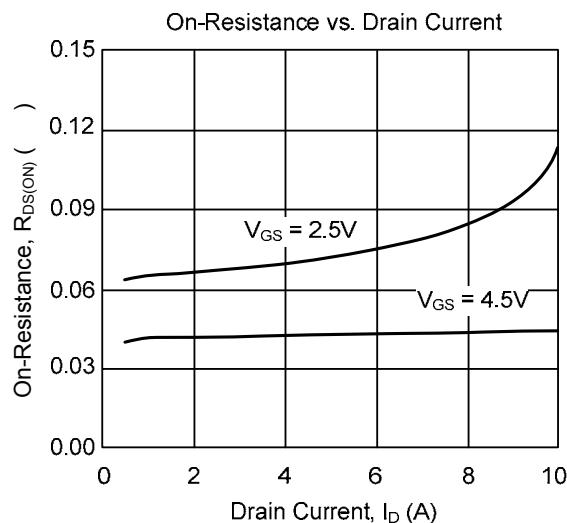
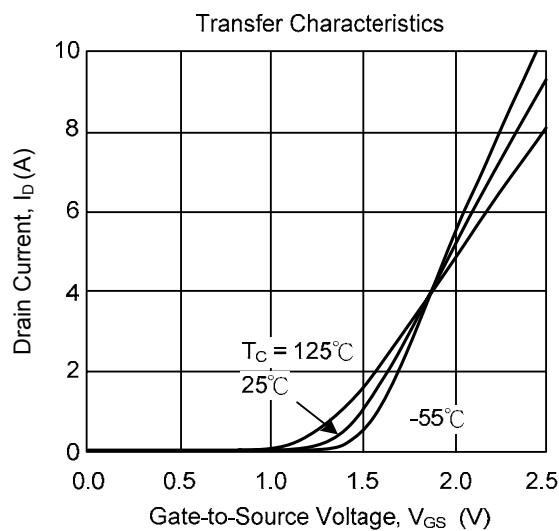
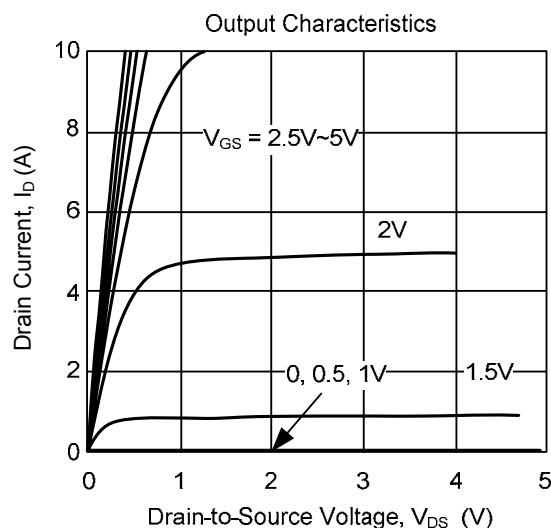
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0 V, I_D = 250 \mu A$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20 V, V_{GS} = 0 V$			1.0	μA
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA
ON CHARACTERISTICS						
Gate-Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.45			V
Drain-Source On-Resistance (Note 2)	$R_{DS(ON)}$	$V_{GS} = 4.5 V, I_D = 3.6 A$		50	65	Ω
		$V_{GS} = 2.5 V, I_D = 3.1 A$		75	95	Ω
On State Drain Current (Note 2)	$I_{D(ON)}$	$V_{DS} \geq 5 V, V_{GS} = 4.5 V$	6			A
DYNAMIC PARAMETERS						
Input Capacitance	C_{iss}	$V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz$		450		pF
Output Capacitance	C_{oss}			70		pF
Reverse Transfer Capacitance	C_{rss}			43		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD} = 10 V, R_L = 10 \Omega, I_D = 1 A, V_{GEN} = 4.5 V, R_G = 6 \Omega$		7	15	ns
Turn-ON Rise Time	t_R			55	80	ns
Turn-OFF Delay Time	$t_{D(OFF)}$			16	60	ns
Turn-OFF Fall-Time	t_F			10	25	ns
Total Gate Charge	Q_G	$V_{DS} = 10 V, V_{GS} = 4.5 V, I_D = 3.6 A$		5.2	10	nC
Gate-Source Charge	Q_{GS}			0.65		nC
Gate-Drain Charge	Q_{GD}			1.5		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0 V, I_S = 1.0 A$		0.76	1.2	V
Maximum Continuous Drain-Source Diode Forward Current	I_S				1.6	A

Note: 1. Pulse width limited by $T_{J(MAX)}$

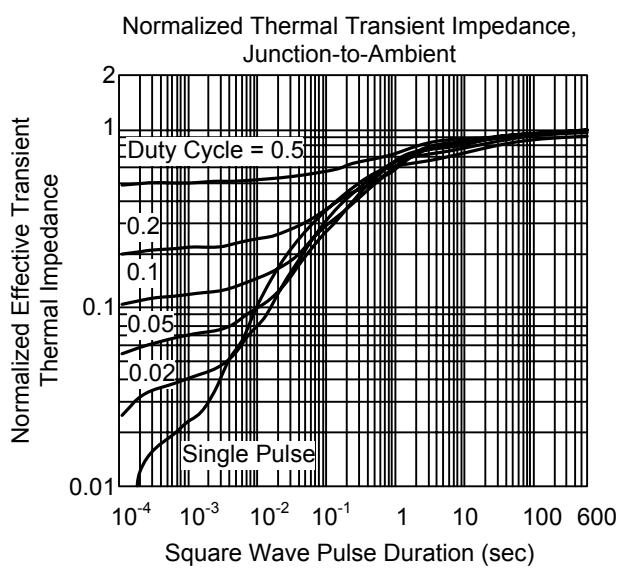
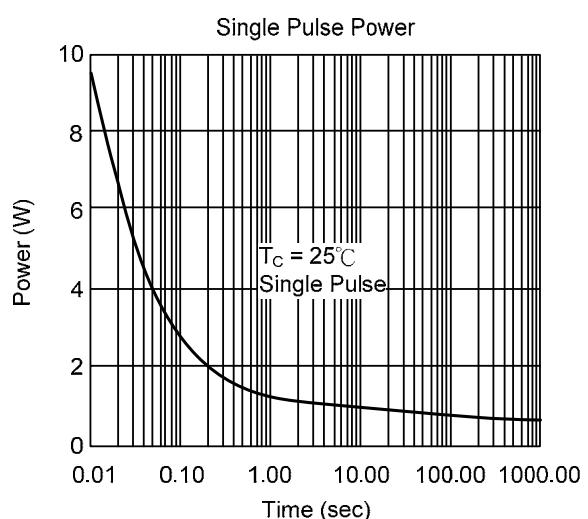
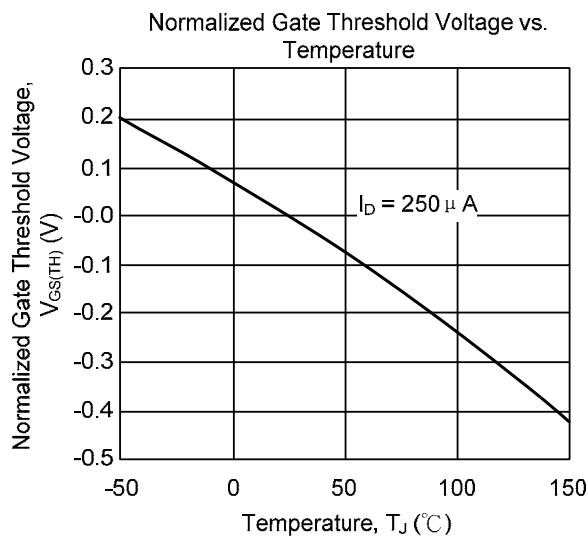
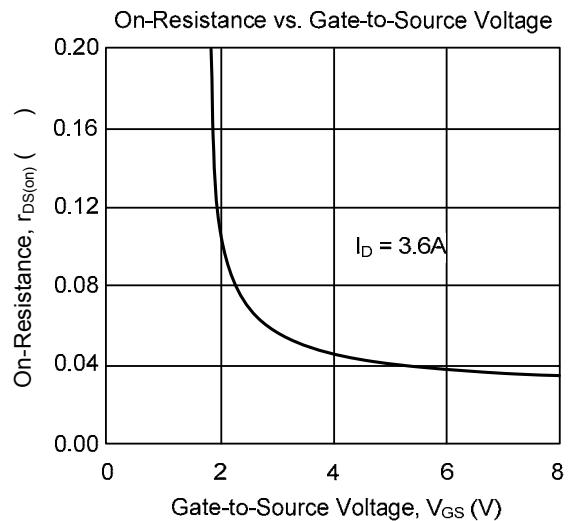
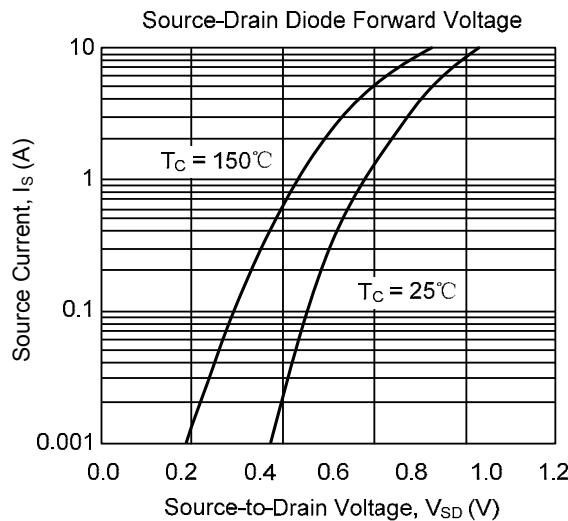
2. Pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$.

3. Surface mounted on FR4 board t = 5 sec.

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)



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