

M62221L/FP

3.0 V Fixed Output Voltage DC/DC Converter

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General Description

The M62221 is a general purpose DC/DC converter which provides a 3.0 V fixed output voltage.

It is possible to simplify the peripheral circuit and to design compact and low cost sets because this IC, housed in a small 5 or 8-pin package includes necessary peripheral components.

Especially this is most suitable for CD-ROM, Disk Drive sets and PDA as a converter from 5 V to 3.0 V.

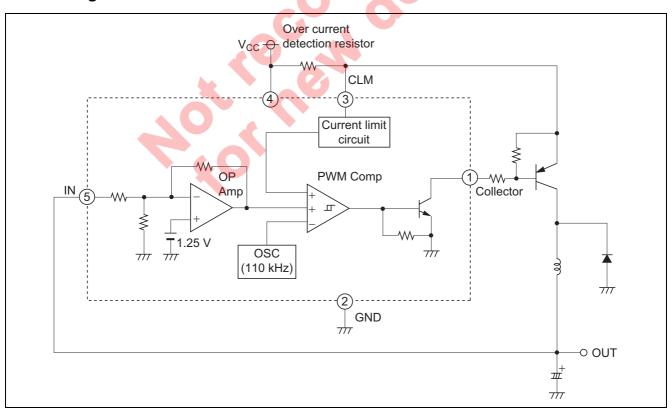
Features

- Wide operation supply voltage range.....4 to 15 V
- Low power consumption......900 µA (max.)
- Built-in oscillator without external components (110 kHz typ.)
- Built-in over current protection circuit
- Small size 5-pin SIP and 8-pin SOP packages

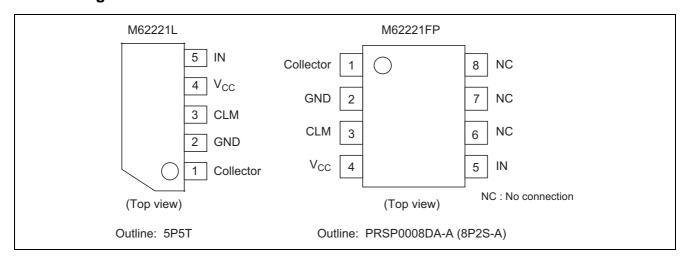
Applications

CD-ROM, PDA, general purpose electric products

Block Diagram



Pin Arrangement





Absolute Maximum Ratings

(Ta = 25°C, unless otherwise noted)

Item	Symbol	Ratings	Unit	Conditions	
Supply voltage	V _{CC}	16	V		
Output current	Io	100	mA		
Power dissipation	Pd	450	mW	Ta = 25°C	5-pin SIP
		440	mW		8-pin SOP
Thermal derating	Κθ	4.5	mW/°C	Ta > 25°C	5-pin SIP
		4.4	mW/°C		8-pin SOP
Operating ambient temperature	Topr	−20 to +85	°C		
Storage temperature	Tstg	-40 to +125	°C		

Electrical Characteristics

(Ta = 25°C, V_{CC} = 5 V, unless otherwise noted)

			Limits				
Block	Item	Symbol	Min	Тур	Max	Unit	Test Conditions
All block	Supply voltage	Vcc	4.0		15	V	
	Supply current	Icc	_	660	900	μΑ	Without load
Error	Output voltage	Vo	2.85	3.00	3.15	V	
Amp.	REF line regulation	Vreg-L		5	15	mV	V _{CC} = 4 to 12 V
	IN input current	lin	_	100	300	μΑ	
Oscillator	Oscillator frequency	fosc	65	110	155	kHz	
	Maximum on duty	T _{DUTY}		90	_	%	
CLM	Current limit voltage	V _{THCLM}	120	150	180	mV	V _{CC} – CLM
Output	Output leakage current	IcL	-1	P	1	μΑ	$V_{CC} = 12 \text{ V}, V_{C} = 12 \text{ V}$
	Output saturation voltage	Vsat		0.4	0.7	٧	I _O = 100 mA

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Application Circuit (3.0 V Output DC/DC Converter)

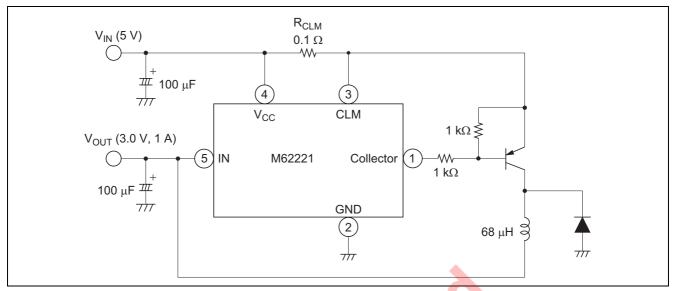


Figure 1 Example of the M62221L/FP Application Circuit

• Current limit detection:

When the voltage drop between pin 3 and pin 4 becomes more than 150 mV, the current limit detection circuit begins operating. The peak switch current "Ipk" is limited to 150 mV/R_{CLM}. In the example of application (Figure 1), the current is limited to 1.5 A.

The Expression of Circuit Constants

Constants	Expressions
T _{ON} T _{OFF}	$\frac{V_O + V_F}{V_{IN} - V_{CE} (sat) - V_O}$
(T _{ON} + T _{OFF}) _{MAX}	$\frac{1}{f_{OSC}} f_{OSC}: 110 \text{ kHz} (V_{CC} = 5 \text{ V})$
T _{OFF} (MIN)	$(T_{ON} + T_{OFF}) / (1 + \frac{T_{ON}}{T_{OFF}})$
T _{ON (MAX)}	$\frac{1}{f_{OSC}} - T_{OFF}$
L (MIN)	$\frac{(V_{\text{IN}} - V_{\text{CE (sat)}} - V_{\text{O}}) \times \text{Ton (MAX)}}{\Delta I_{\text{O}}}$
lpk	$I_{O} + \frac{1}{2} \Delta I_{O}$
R _{CLM}	$\frac{0.15}{\text{lpk}} \Delta V_{\text{CLM}}$: 150 mV (V _{CC} = 5 V)

Note: V_F: Forward voltage drop of an external diode.

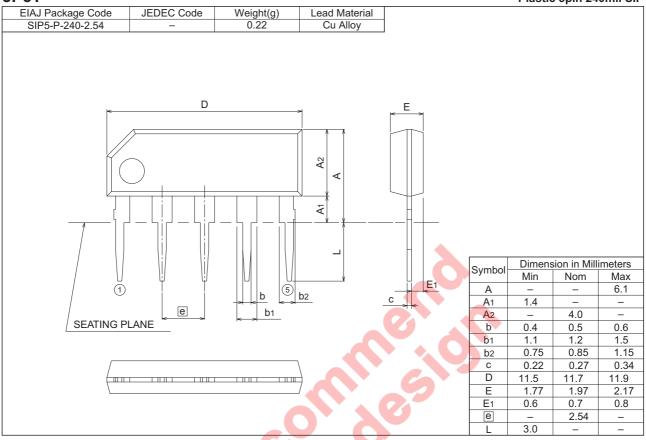
Vsat: Output saturation voltage of an external switching transistor.

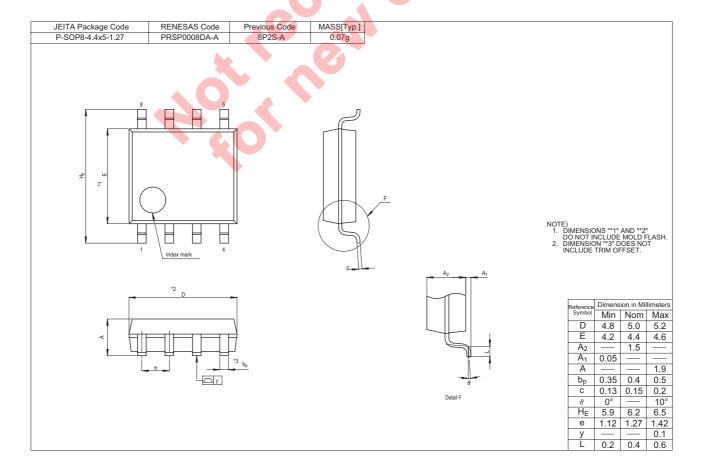
 ΔI_0 : It should be set between 1/3 and 1/5 of maximum output current.

An external transistor, diode and inductor should have a peak current capability of greater than "Ipk".

Package Dimensions

5P5T Plastic 5pin 240mil SIP





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