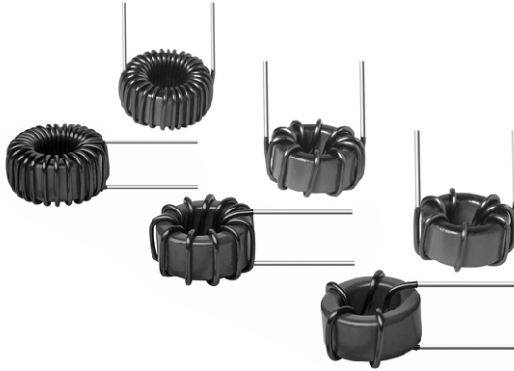


## Toroid, High Current, High Temperature



### FEATURES

- Printed circuit mounting
- Toroid design reduces EMI
- Vertical or horizontal mounting to optimize P.C. board layout
- High temperature rating of 200 °C - no aging
- 100 % lead (Pb)-free and RoHS compliant


**RoHS**  
COMPLIANT

### APPLICATIONS

- Switching power supplies
- EMI/RFI filtering
- Output chokes

### STANDARD ELECTRICAL SPECIFICATIONS in inches [millimeters]

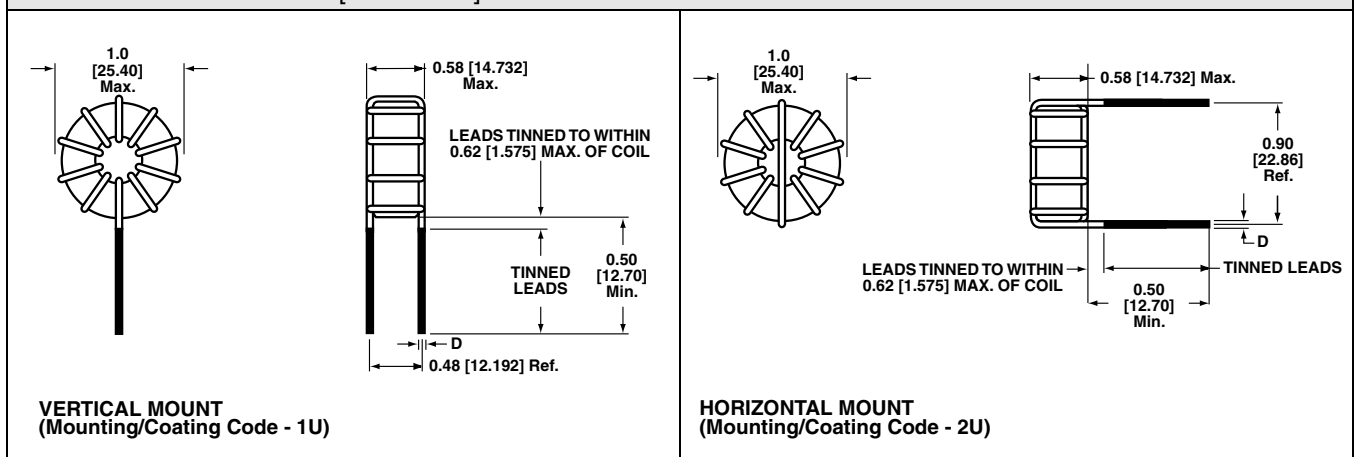
INDUCTANCE ( $\mu$ H) $L_0$	TOLERANCE	DCR (VERTICAL MOUNT)		DCR (HORIZONTAL MOUNT)		RATED CURRENT <sup>(1)</sup> VERTICAL MOUNT (AMPS)	RATED CURRENT <sup>(1)</sup> HORIZONTAL MOUNT (AMPS)	SATURATION CURRENT <sup>(2)</sup> (AMPS)	LEAD DIAMETER D
		$\Omega$ TYP.	$\Omega$ MAX.	$\Omega$ TYP.	$\Omega$ MAX.				
0.47	20 %	0.0016	0.0024	0.0022	0.003	36	30	50	0.053 [1.346]
1.2	20 %	0.0028	0.0032	0.0032	0.0035	28	24	33	0.053 [1.346]
2.2	20 %	0.0036	0.0042	0.0042	0.0048	23	22	22	0.053 [1.346]
3.9	20 %	0.0045	0.0058	0.005	0.006	21	19.5	18	0.053 [1.346]
4.7	20 %	0.005	0.0064	0.0055	0.007	19	18.5	15	0.053 [1.346]
6.8	20 %	0.006	0.0074	0.0065	0.0078	18	17	14	0.053 [1.346]
10	20 %	0.0075	0.011	0.0084	0.012	15.8	15.5	10	0.053 [1.346]
22	20 %	0.015	0.019	0.016	0.02	10.8	10.5	7	0.042 [1.067]
39	20 %	0.02	0.025	0.022	0.028	9.2	9.1	5	0.042 [1.067]
100	20 %	0.05	0.069	0.054	0.075	5.5	5.5	3.0	0.034 [0.864]
470	20 %	0.17	0.29	0.175	0.3	2.8	2.8	1.5	0.027 [0.686]

**Note**

1. DC current that will cause an approx.  $\Delta T$  of 50 °C
2. DC current that will cause  $L_0$  to drop approx. 20 %

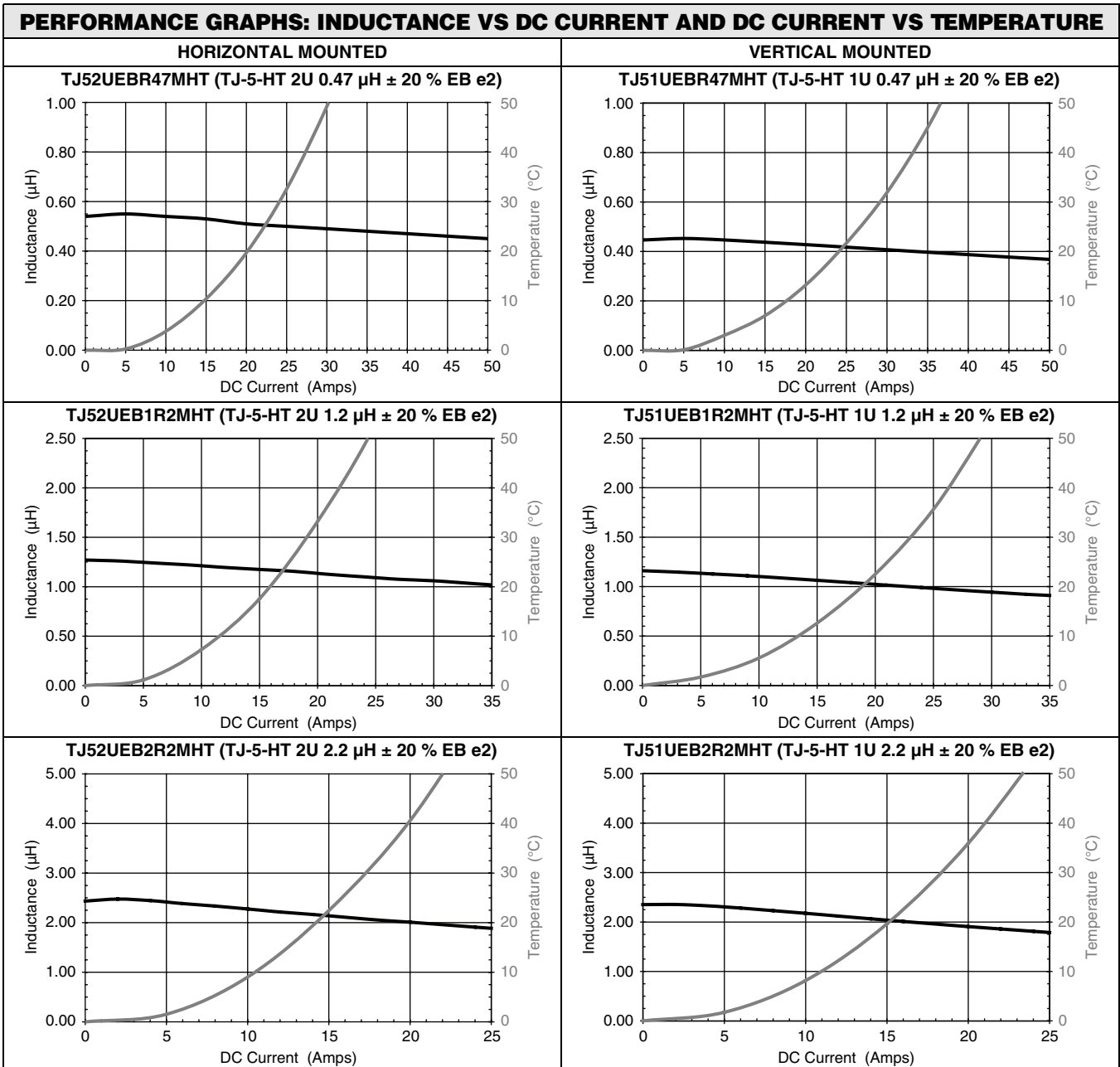
Operating Temperature (ambient +  $\Delta T$ ): - 55 °C to + 200 °C  
 Inductance tested at 0.25  $V_{RMS}$ , 1 kHz  
 DCR tested at 25 °C  $\pm$  5 °C  
 All material rated at 200 °C

### DIMENSIONS in inches [millimeters]

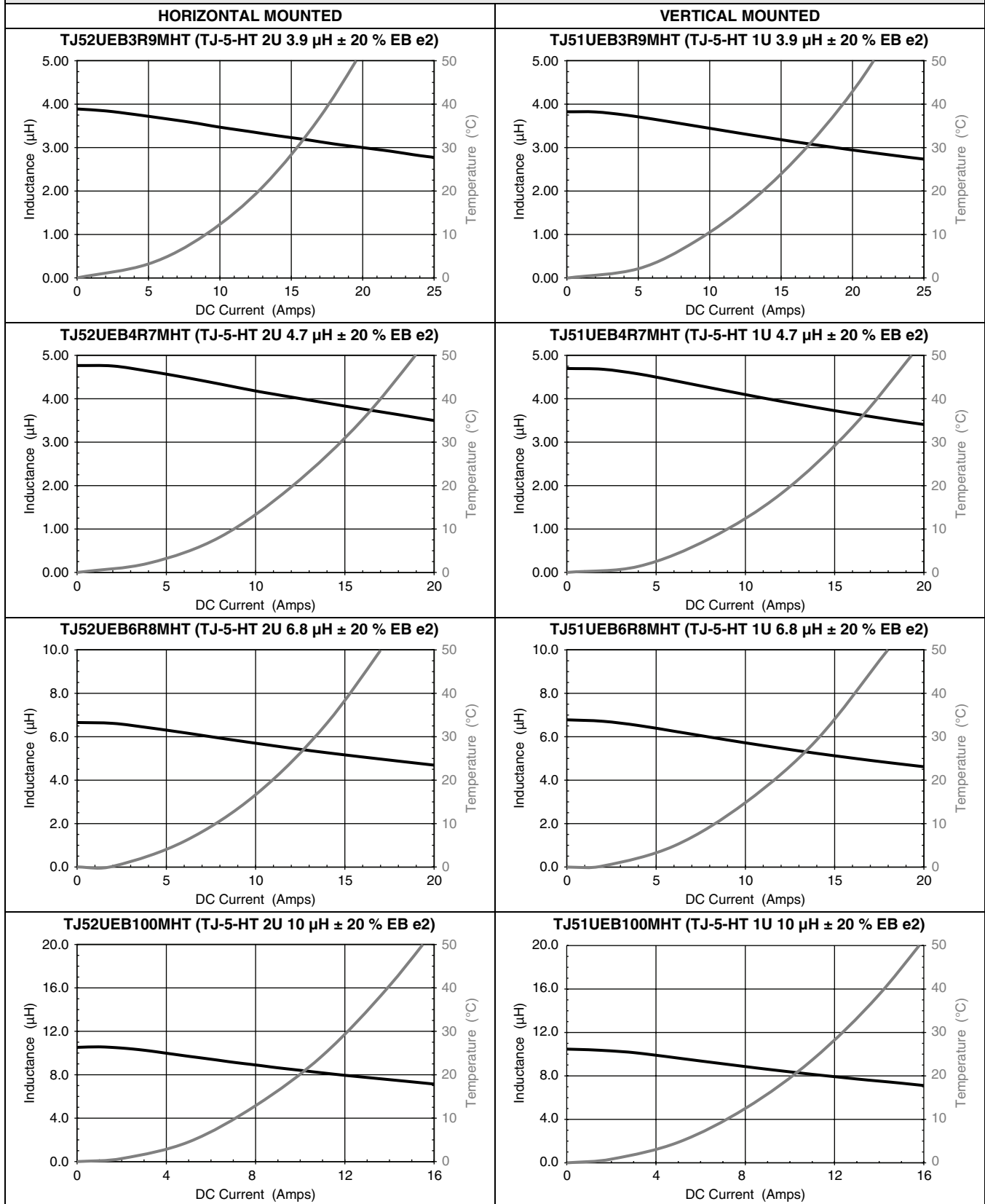




DESCRIPTION					
TJ5-HT MODEL	1U MOUNTING/COATING CODE	10 $\mu$ H INDUCTANCE VALUE	$\pm$ 20 % INDUCTANCE TOLERANCE	EB PACKAGE CODE	e2 JEDEC LEAD (Pb)-FREE STANDARD
GLOBAL PART NUMBER					
<b>T</b> MODEL	<b>J</b> MOUNTING/COATING CODE	<b>5</b> MOUNTING/COATING CODE	<b>1</b> MOUNTING/COATING CODE	<b>U</b> MOUNTING/COATING CODE	<b>E</b> PACKAGE CODE
			<b>B</b> PACKAGE CODE	<b>1</b> INDUCTANCE VALUE	<b>0</b> INDUCTANCE VALUE
				<b>0</b> INDUCTANCE VALUE	<b>M</b> INDUCTANCE TOLERANCE
					<b>H</b> SERIES
					<b>T</b> SERIES

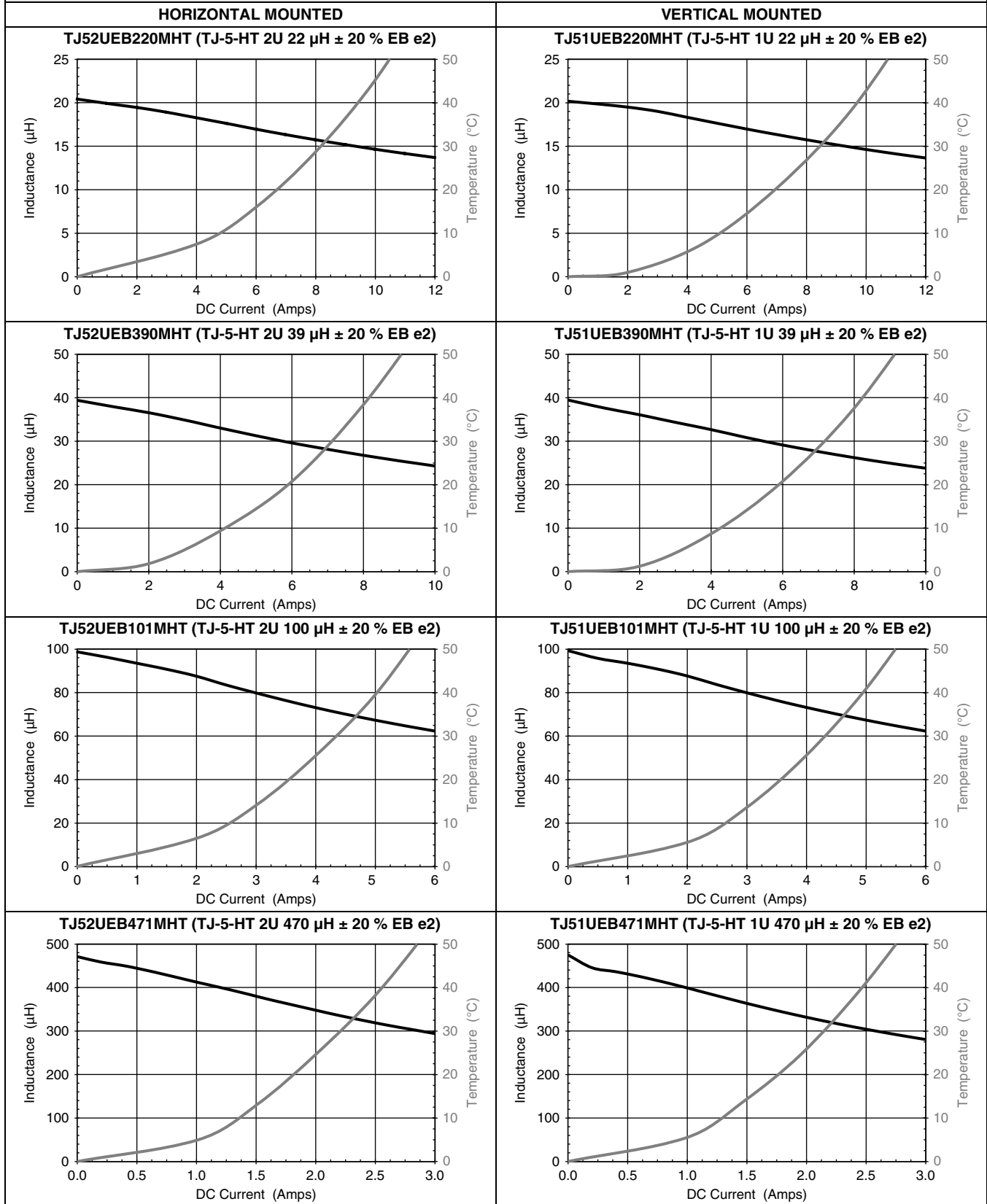


**PERFORMANCE GRAPHS: INDUCTANCE VS DC CURRENT AND DC CURRENT VS TEMPERATURE**





**PERFORMANCE GRAPHS: INDUCTANCE VS DC CURRENT AND DC CURRENT VS TEMPERATURE**





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