



SamHop Microelectronics Corp.

STS4622

Jun, 06 2006

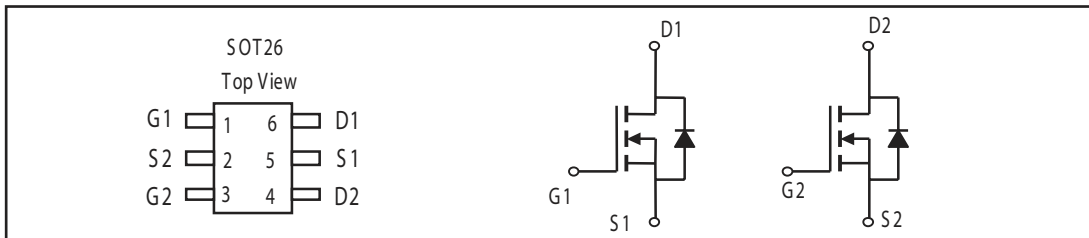
Dual N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

V _{DSS}	I _D	R _{DS(ON)} (mΩ) Max
40V	3A	65 @ V _{GS} = 10V 85 @ V _{GS} = 4.5V

FEATURES

- Super high dense cell design for low R_{DS(ON)}.
- Rugged and reliable.
- SOT-26 package.



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 20	V
Drain Current-Continuous @ T _J =25°C -Pulsed ^b	I _D	3	A
	I _{DM}	12	A
Drain-Source Diode Forward Current	I _S	1.25	A
Maximum Power Dissipation ^a	P _D	1.25	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R _{thJA}	100	°C/W
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ELECTRICAL CHARACTERISTICS (T_A =25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	40			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =32V, V _{GS} =0V			1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} =± 20V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS ^b						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D = 250uA	1	1.8	3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =3A		53	65	m-ohm
		V _{GS} = 4.5V, I _D =2A		66	85	m-ohm
On-State Drain Current	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 4.5V	10			A
Forward Transconductance	g _{FS}	V _{DS} = 5V, I _D =3A		7		S
DYNAMIC CHARACTERISTICS ^c						
Input Capacitance	C _{ISS}	V _{DS} =20V, V _{GS} = 0V f =1.0MHz		330		pF
Output Capacitance	C _{OSS}			50		pF
Reverse Transfer Capacitance	C _{RSS}			28		pF
SWITCHING CHARACTERISTICS ^c						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = 20V, I _D = 1A, V _{GS} = 10V, R _L = 20 ohm R _{GEN} = 6 ohm		7.9		ns
Rise Time	t _r			4.6		ns
Turn-Off Delay Time	t _{D(OFF)}			17		ns
Fall Time	t _f			9.3		ns
Total Gate Charge	Q _g	V _{DS} =20V, I _D = 3A, V _{GS} =10V		6.7		nC
Gate-Source Charge	Q _{gs}			0.9		nC
Gate-Drain Charge	Q _{gd}			1.6		nC

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ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS ^b						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = 1.25A$		0.82	1.2	V

Notes

- a.Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.
- b.Pulse Test:Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
- c.Guaranteed by design, not subject to production testing.

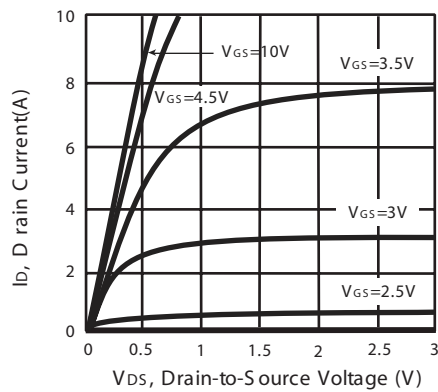


Figure 1. Output Characteristics

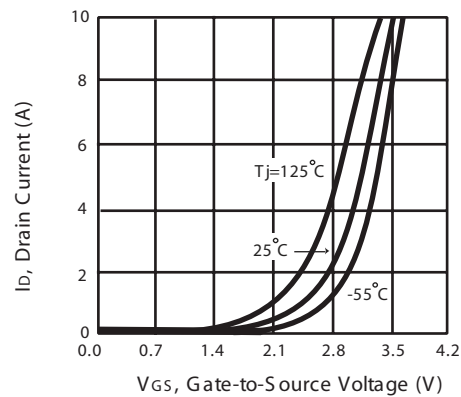


Figure 2. Transfer Characteristics

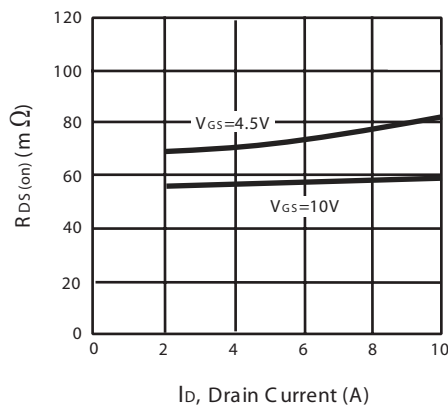


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

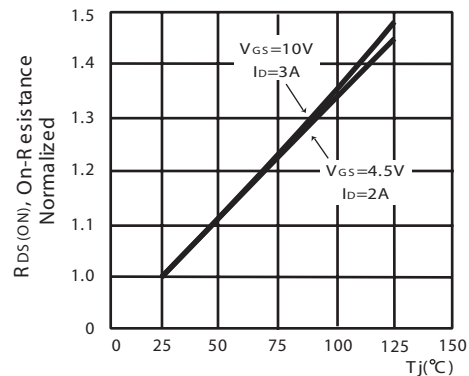


Figure 4. On-Resistance Variation with Temperature

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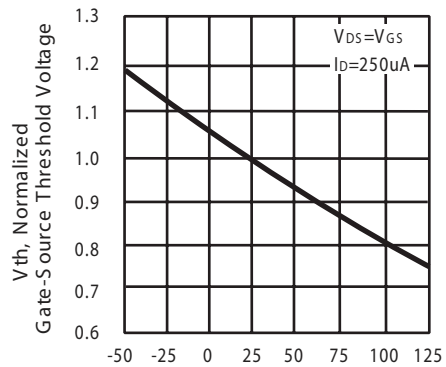


Figure 5. Gate Threshold Variation with Temperature

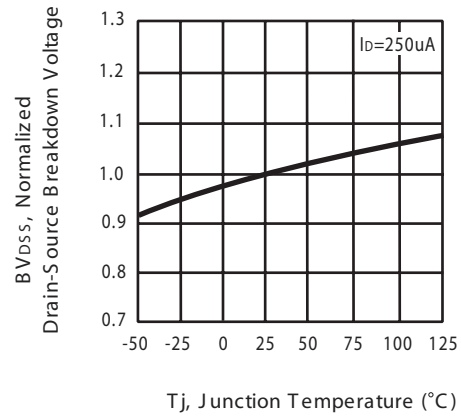


Figure 6. Breakdown Voltage Variation with Temperature

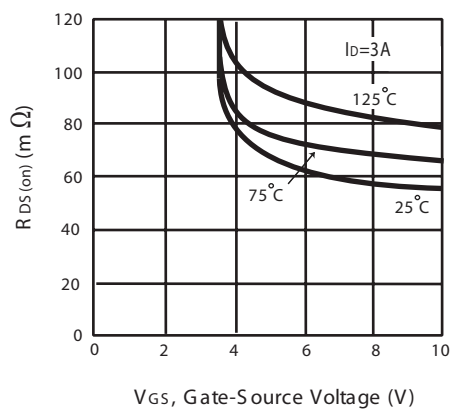


Figure 7. On-Resistance vs. Gate-Source Voltage

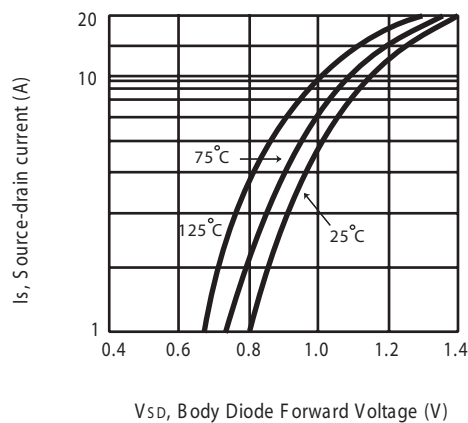


Figure 8. Body Diode Forward Voltage Variation with Source Current

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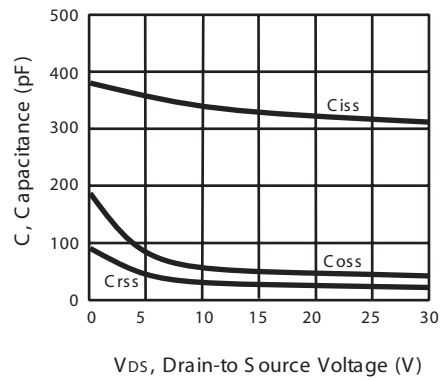


Figure 9. Capacitance

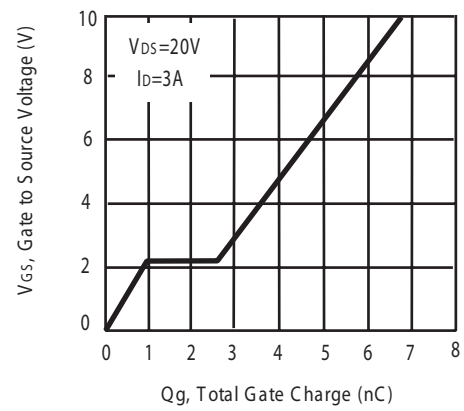


Figure 10. Gate Charge

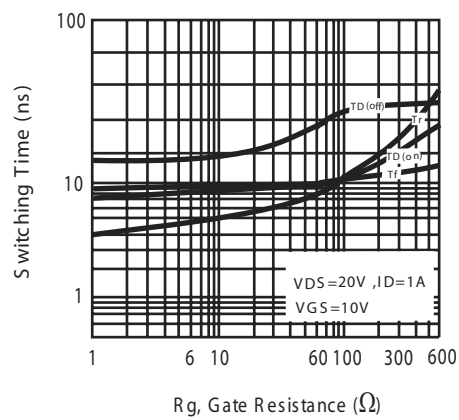


Figure 11. switching characteristics

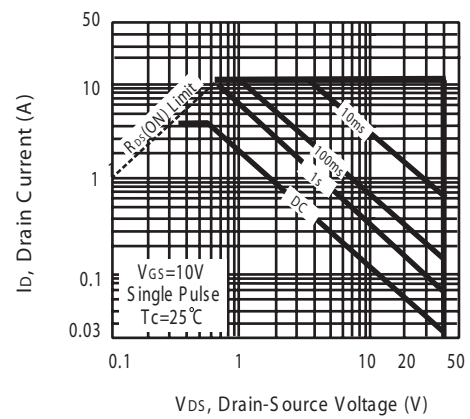


Figure 12. Maximum Safe Operating Area

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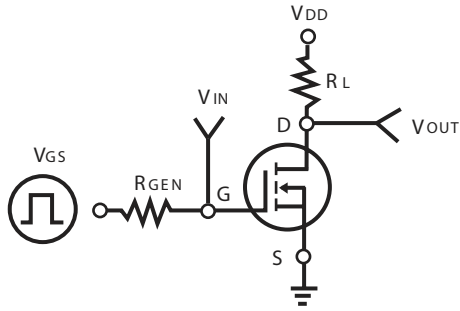


Figure 13. S switching Test Circuit

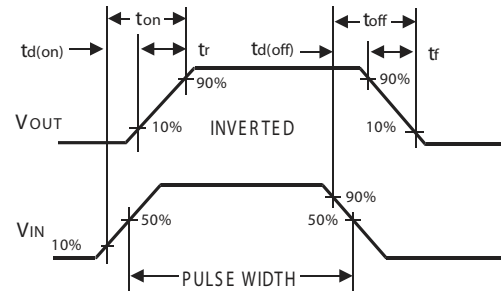
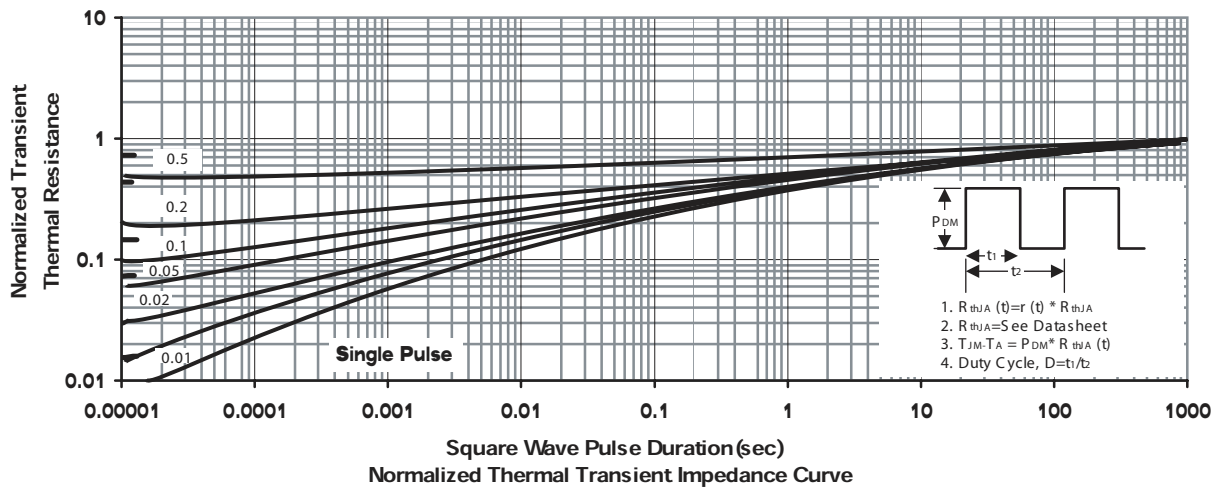


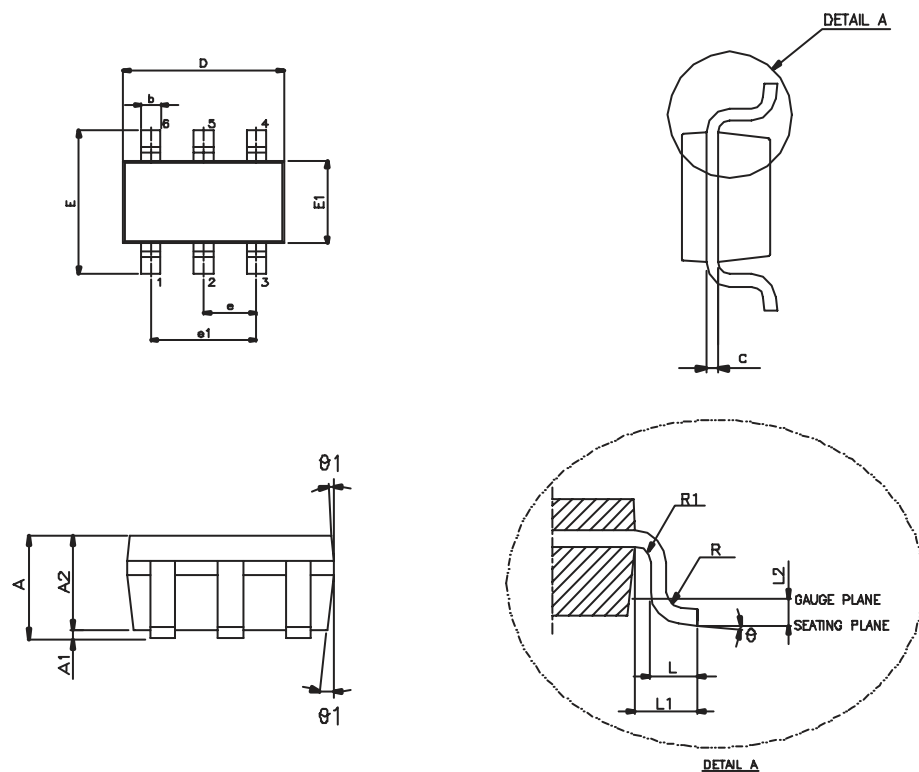
Figure 14. S switching Waveforms



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PACKAGE OUTLINE DIMENSIONS

SOT26

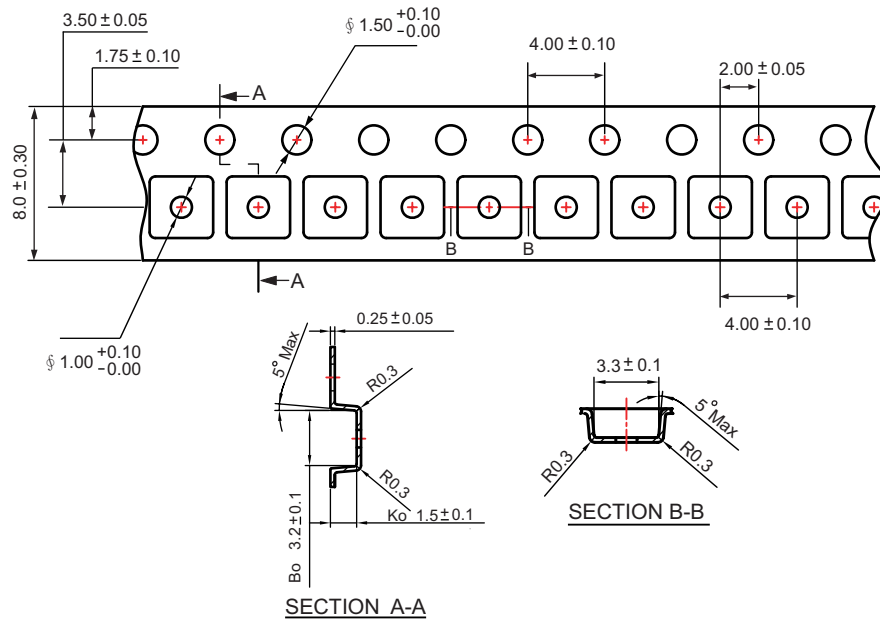


SYMBOL	MIN.	NOM.	MAX.
A	—	—	1.45
A1	—	—	0.15
A2	0.90	1.15	1.30
b	0.30	—	0.50
c	0.08	—	0.22
D	2.90 BSC.		
E	2.80 BSC.		
E1	1.60 BSC.		
e	0.95 BSC		
e1	1.90 BSC.		
L	0.30	0.45	0.60
L1	0.60 REF.		
L2	0.25 BSC.		
R	0.10	—	—
R1	0.10	—	0.25
θ	0°	4°	8°
θ1	5°	10°	15°

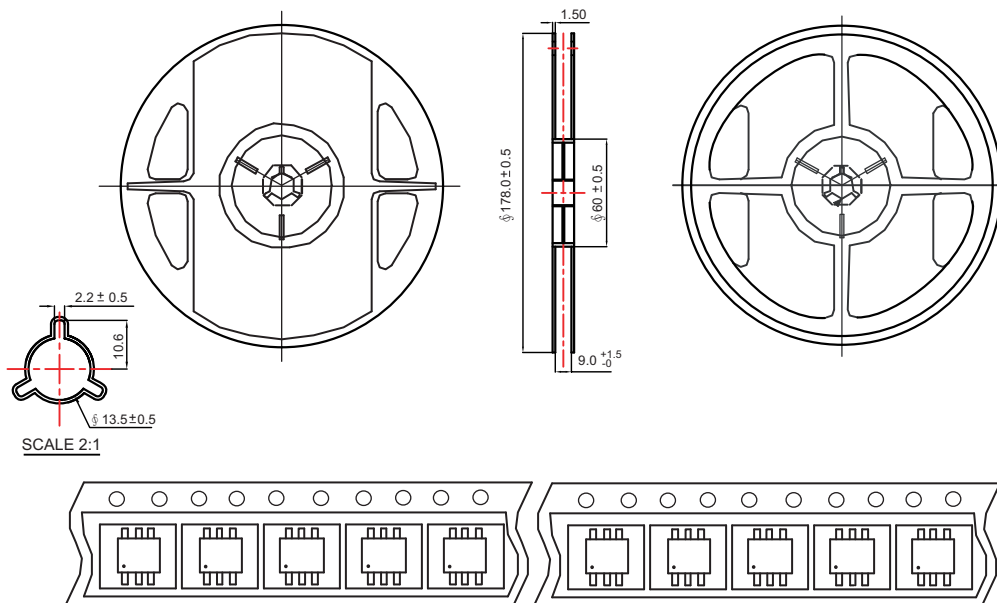
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SOT 26 Tape and Reel Data

SOT 26 Carrier Tape



SOT 26 Reel



SOT 26