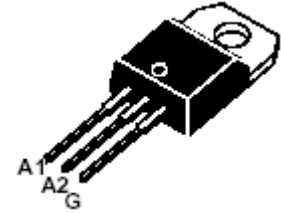


# BTA04

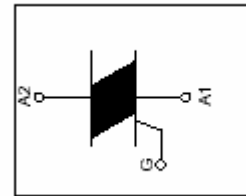
## ◆ Features

- With TO-220AB Insulated package
- Suitable for general purpose applications where gate high sensitivity is required ,such as phase control and static switching applications



## ◆ QUICK REFERENCE DATA

| SYMBOL      | PARAMETER                            | VALUE     | UNIT |
|-------------|--------------------------------------|-----------|------|
| $V_{DRM}$   | Repetitive peak off-state voltage    | 600       | V    |
| $V_{RRM}$   | Repetitive peak off-state voltage    | 600       | V    |
| $I_{T(AV)}$ | Average on-state current             | 4         | A    |
| $I_{TSM}$   | Non-repetitive peak on-state current | 40        | A    |
| $T_{stg}$   | Storage temperature                  | -45 ~ 150 |      |
| $T_j$       | Operating junction temperature       | 110       |      |



## ◆ ELECTRICAL CHARACTERISTICS ( $T_j = 25^\circ\text{C}$ , unless otherwise specified)

| SYMBOL    | PARAMETER                         | CONDITIONS                            | MIN | MAX | UNIT |
|-----------|-----------------------------------|---------------------------------------|-----|-----|------|
| $V_{DRM}$ | Repetitive peak off-state voltage | $I_D=0.1\text{mA}$                    | 600 |     | V    |
| $V_{RRM}$ | Repetitive peak reverse voltage   | $I_D=0.5\text{mA}$                    | 600 |     | V    |
| $I_{GT}$  | Gate trigger current              | $V_D=12\text{V}; R_L=100$ T2+ G+      |     | 6   | mA   |
|           |                                   | T2+ G-                                |     | 10  |      |
|           |                                   | T2- G-                                |     | 10  |      |
|           |                                   | T2- G+                                |     | 35  |      |
| $V_T$     | On-state voltage                  | $I_T=8\text{A}$                       |     | 1.7 | V    |
| $I_H$     | Holding current                   | $I_T=0.1\text{A}; I_{GT}=20\text{mA}$ |     | 15  | mA   |
| $V_{GT}$  | Gate trigger voltage              | $V_D=12\text{V}; R_L=100$ T2+ G+      |     | 1.5 | V    |
|           |                                   | T2+ G-                                |     | 1.5 |      |
|           |                                   | T2- G-                                |     | 1.5 |      |
|           |                                   | T2- G+                                |     | 1.8 |      |