

HIGH POWER PLANAR RESISTORS, 5W to 100W

TP SERIES CERAMIC SUBSTRATE TPS SERIES STEEL SUBSTRATE





☐ Space saving flame retardant design

☐ High power density

☐ Thin profile, and lightweight

☐ Low Inductance: 50nh typ at 1MHz

 \square Wide resistance range: 0.5Ω to $1M\Omega$

☐ Excellent transient withstanding capabilities

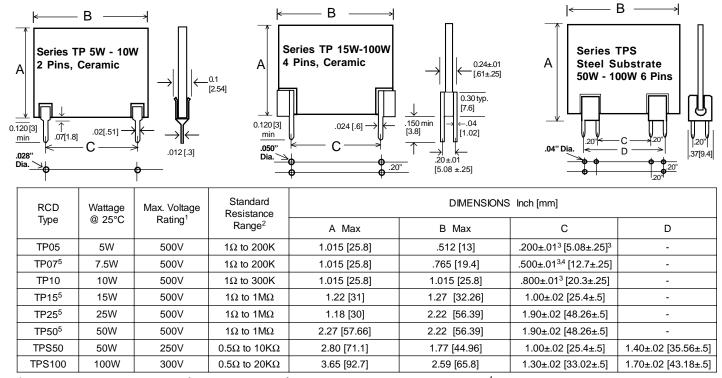
☐ Standard tolerances: as tight as ±0.5%

OPTIONS

☐ Special marking, extended value range, custom sizes and shapes, burn-in and military screening, insulated leads, quick-disconnect terminals, multiple resistor circuits, etc. Consult factory for assistance.

SPACE SAVING THICK FILM PLANAR RESISTORS!

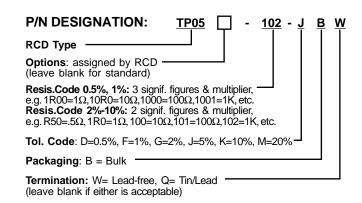
Series TP/TPS offer high power density in a lightweight design. Designed for switch mode power supplies, snubber circuits, heaters, in-rush limiters, dynamic braking, and other power applications, the planar construction offers significant PCB real estate savings over conventional resistors, especially in forced air applications. The unique design enables very low inductance and excellent surge capabilities. Series TP utilize alumina substrates for excellent heat dissipation. Series TPS utilize stainless steel substrates for even greater dissipation and durability. A special hi-temp/hi-voltage dielectric insulates the resistor and terminals from the metal substrate, enabling greater voltage and insulation compared to porcelain enameled models.



¹ Maximum Operating Voltage is DC or AC peak ² Expanded range available ³ Available with 0.300" [7.62mm] pin spacing, specify opt.78 ⁴ Available with 0.200" [5.08mm] pin spacing, specify opt.80

TYPICAL PERFORMANCE CHARACTERISTICS

Load Life (1000 hours)	±2%
Moisture Resistance	±1%
Temperature Cycling	±1%
Overload (not to exceed max voltage)	5x rated W, 200mS
Dielectric Strength	500VDC (750V avail)
Resistance to Solder Heat (260°C, 5 sec)	±0.5%
Temperature Coefficient (ppm/°C)	100ppm°C (≥2%=200ppm)
Operating Temperature	TP = -55°C to 170°C TPS = -55°C to 275°C
Power Derating	TP = 1%/°C > 70°C TPS = 0.4%/°C > 25°C



⁵ Preliminary information