BFU725F/N1

NPN wideband silicon germanium RF transistor

Rev. 01 — 13 July 2009 Product data sheet

1. Product profile

1.1 General description

NPN silicon germanium microwave transistor for high speed, low noise applications in a plastic, 4-pin dual-emitter SOT343F package.

CAUTION

This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

1.2 Features

- Low noise high gain microwave transistor
- Noise figure (NF) = 0.7 dB at 5.8 GHz
- High maximum stable gain 27 dB at 1.8 GHz
- 110 GHz f_T silicon germanium technology

1.3 Applications

- 2nd LNA stage and mixer stage in DBS LNB's
- Satellite radio
- Low noise amplifiers for microwave communications systems
- WLAN and CDMA applications
- Analog/digital cordless applications
- Ka band oscillators (DRO's)

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-	10	V
V_{CEO}	collector-emitter voltage	open base		-	-	2.8	V
V_{EBO}	emitter-base voltage	open collector		-	-	0.55	V
I_C	collector current			-	25	40	mA
P_{tot}	total power dissipation	$T_{sp} \leq 90$ °C	[1]	-	-	136	mW
h_{FE}	DC current gain	I_C = 10 mA; V_{CE} = 2 V; T_j = 25 °C		160	280	400	

Table 1. Quick reference data *...continued*

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
C_{CBS}	collector-base capacitance	V_{CB} = 2 V; f = 1 MHz		-	70	-	fF
f_T	transition frequency	I_C = 25 mA; V_{CE} = 2 V; f = 2 GHz; T_{amb} = 25 °C		-	55	-	GHz
$G_{p(max)}$	maximum power gain	I_C = 25 mA; V_{CE} = 2 V; f = 5.8 GHz; T_{amb} = 25 °C	[2]	-	18	-	dB
NF	noise figure	I_C = 5 mA; V_{CE} = 2 V; f = 5.8 GHz; $\Gamma_S = \Gamma_{opt}$; T_{amb} = 25 °C		-	0.7	-	dB

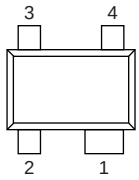
[1] T_{sp} is the temperature at the solder point of the emitter lead.

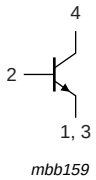
[2] $G_{p(max)}$ is the maximum power gain, if K > 1. If K < 1 then $G_{p(max)}$ = Maximum Stable Gain (MSG).

2. Pinning information

Table 2. Discrete pinning

Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	base		
3	emitter		
4	collector		





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3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BFU725F/N1	-	plastic surface-mounted flat pack package; reverse pinning; 4 leads	SOT343F

4. Marking

Table 4. Marking

Type number	Marking	Description
BFU725F/N1	B7*	* = p : made in Hong Kong
		* = t : made in Malaysia
		* = W : made in China

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	10	V
V_{CEO}	collector-emitter voltage	open base		-	2.8	V
V_{EBO}	emitter-base voltage	open collector		-	0.55	V
I_C	collector current			-	40	mA
P_{tot}	total power dissipation	$T_{sp} \leq 90$ °C	[1]	-	136	mW
T_{stg}	storage temperature			–65	+150	°C
T_j	junction temperature			-	150	°C

[1] T_{sp} is the temperature at the solder point of the emitter lead.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		440	K/W

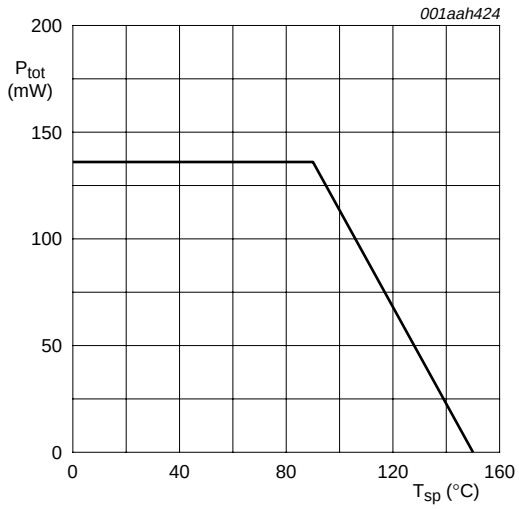


Fig 1. Power derating curve

7. Characteristics

Table 7. Characteristics

T_j = 25 °C unless otherwise specified.

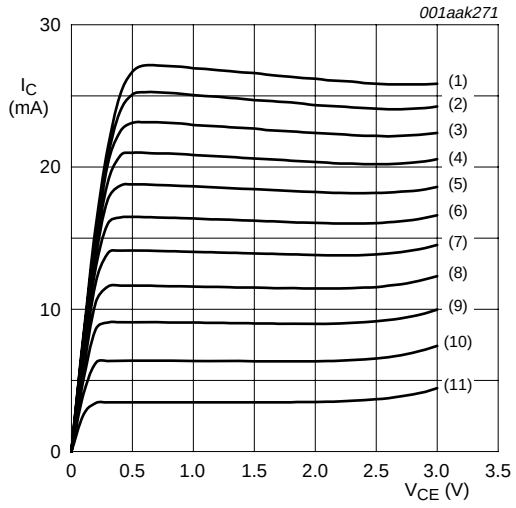
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	I_C = 2.5 µA; I_E = 0 mA	10	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	I_C = 1 mA; I_B = 0 mA	2.8	-	-	V
I_C	collector current		-	25	40	mA
I_{CBO}	collector-base cut-off current	I_E = 0 mA; V_{CB} = 4.5 V	-	-	100	nA
h_{FE}	DC current gain	I_C = 10 mA; V_{CE} = 2 V	160	280	400	
C_{CES}	collector-emitter capacitance	V_{CB} = 2 V; f = 1 MHz	-	268	-	fF
C_{EBS}	emitter-base capacitance	V_{EB} = 0.5 V; f = 1 MHz	-	400	-	fF
C_{CBS}	collector-base capacitance	V_{CB} = 2 V; f = 1 MHz	-	70	-	fF
f_T	transition frequency	I_C = 25 mA; V_{CE} = 2 V; f = 2 GHz; T_{amb} = 25 °C	-	55	-	GHz
$G_{p(max)}$	maximum power gain	I_C = 25 mA; V_{CE} = 2 V; T_{amb} = 25 °C [1]				
		f = 1.5 GHz	-	28	-	dB
		f = 1.8 GHz	-	27	-	dB
		f = 2.4 GHz	-	25.5	-	dB
		f = 5.8 GHz	-	18	-	dB
		f = 12 GHz	-	13	-	dB
$\|s_{21}\|^2$	insertion power gain	I_C = 25 mA; V_{CE} = 2 V; T_{amb} = 25 °C				
		f = 1.5 GHz	-	26.7	-	dB
		f = 1.8 GHz	-	25.4	-	dB
		f = 2.4 GHz	-	23	-	dB
		f = 5.8 GHz	-	16	-	dB
		f = 12 GHz	-	9.3	-	dB
NF	noise figure	I_C = 5 mA; V_{CE} = 2 V; $\Gamma_S = \Gamma_{opt}$; T_{amb} = 25 °C				
		f = 1.5 GHz	-	0.42	-	dB
		f = 1.8 GHz	-	0.43	-	dB
		f = 2.4 GHz	-	0.47	-	dB
		f = 5.8 GHz	-	0.7	-	dB
		f = 12 GHz	-	1.1	-	dB
G_{ass}	associated gain	I_C = 5 mA; V_{CE} = 2 V; $\Gamma_S = \Gamma_{opt}$; T_{amb} = 25 °C				
		f = 1.5 GHz	-	24	-	dB
		f = 1.8 GHz	-	22	-	dB
		f = 2.4 GHz	-	20	-	dB
		f = 5.8 GHz	-	13.5	-	dB
		f = 12 GHz	-	10	-	dB

Table 7. Characteristics *...continued*

T_j = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$P_{L(1dB)}$	output power at 1 dB gain compression	I_C = 25 mA; V_{CE} = 2 V; Z_S = Z_L = 50 Ω; T_{amb} = 25 °C				
		f = 1.5 GHz	-	8.5	-	dBm
		f = 1.8 GHz	-	9	-	dBm
		f = 2.4 GHz	-	8.5	-	dBm
		f = 5.8 GHz	-	8	-	dBm
IP3	third-order intercept point	I_C = 25 mA; V_{CE} = 2 V; Z_S = Z_L = 50 Ω; T_{amb} = 25 °C; $f_2 = f_1 + 1$ MHz				
		f_1 = 1.5 GHz	-	17	-	dBm
		f_1 = 1.8 GHz	-	17	-	dBm
		f_1 = 2.4 GHz	-	17	-	dBm
		f_1 = 5.8 GHz	-	19	-	dBm

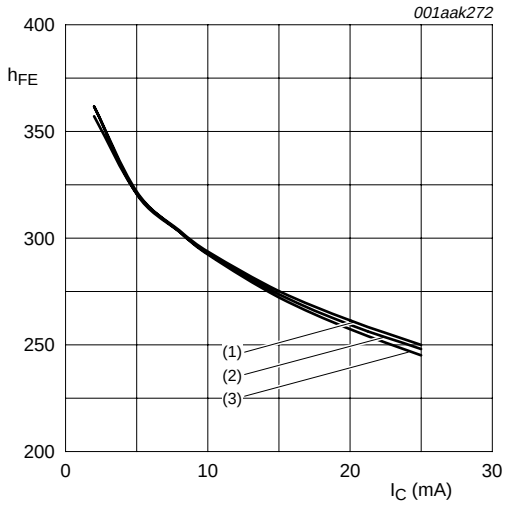
[1] $G_{p(max)}$ is the maximum power gain, if K > 1. If K < 1 then $G_{p(max)}$ = MSG.



T_{amb} = 25 °C.

(1) I_B = 110 µA
(2) I_B = 100 µA
(3) I_B = 90 µA
(4) I_B = 80 µA
(5) I_B = 70 µA
(6) I_B = 60 µA
(7) I_B = 50 µA
(8) I_B = 40 µA
(9) I_B = 30 µA
(10) I_B = 20 µA
(11) I_B = 10 µA

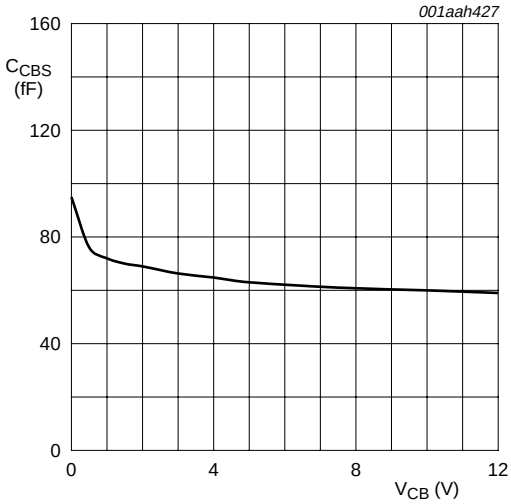
Fig 2. Collector current as a function of collector-emitter voltage; typical values



T_{amb} = 25 °C.

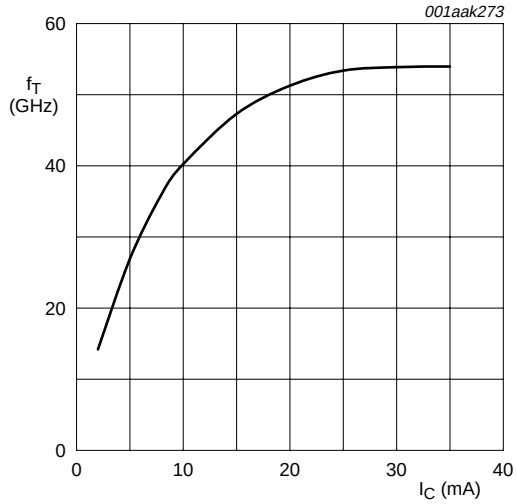
(1) V_{CE} = 1 V
(2) V_{CE} = 1.5 V
(3) V_{CE} = 2 V

Fig 3. DC current gain a function of collector current; typical values



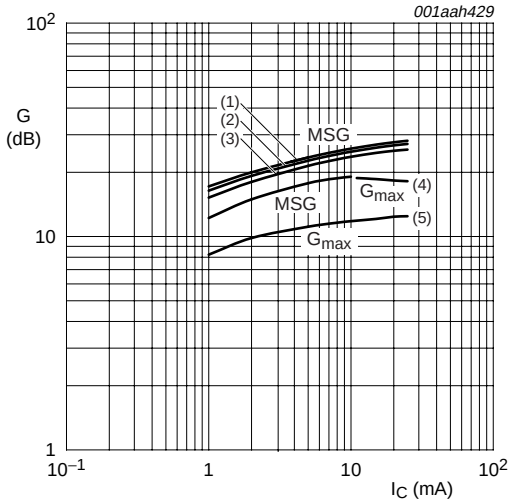
f = 1 MHz, T_{amb} = 25 °C.

Fig 4. Collector-base capacitance as a function of collector-base voltage; typical values



V_{CE} = 2 V; f = 2 GHz; T_{amb} = 25 °C.

Fig 5. Transition frequency as a function of collector current; typical values



V_{CE} = 2 V; T_{amb} = 25 °C.

(1) f = 1.5 GHz

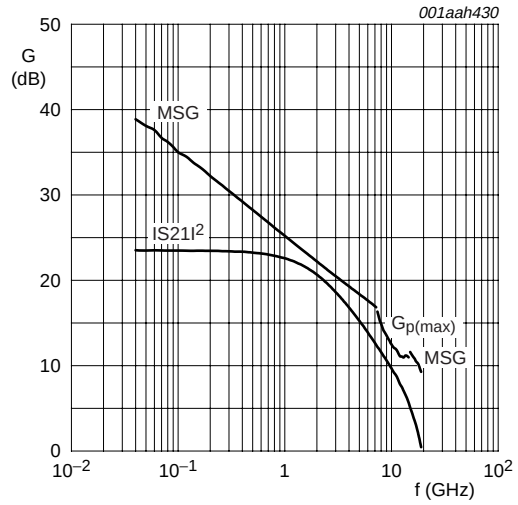
(2) f = 1.8 GHz

(3) f = 2.4 GHz

(4) f = 5.8 GHz

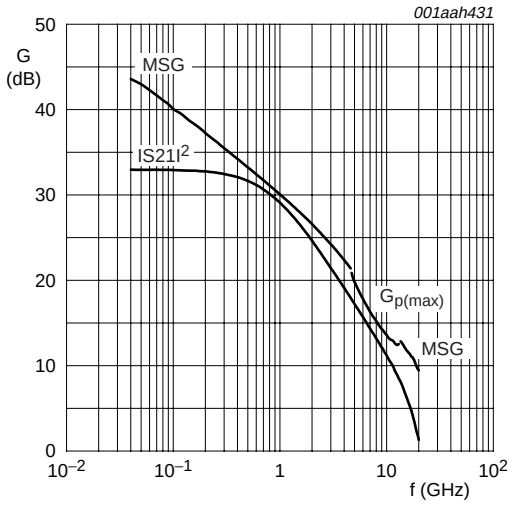
(5) f = 12 GHz

Fig 6. Gain as a function of collector current; typical value



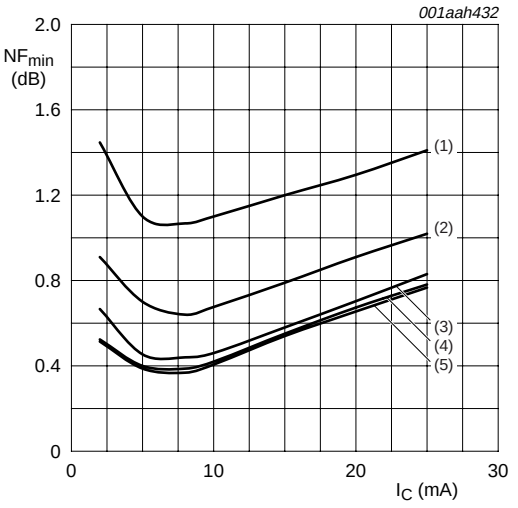
V_{CE} = 2 V; I_C = 5 mA; T_{amb} = 25 °C.

Fig 7. Gain as a function of frequency; typical values



V_{CE} = 2 V; I_C = 25 mA; T_{amb} = 25 °C.

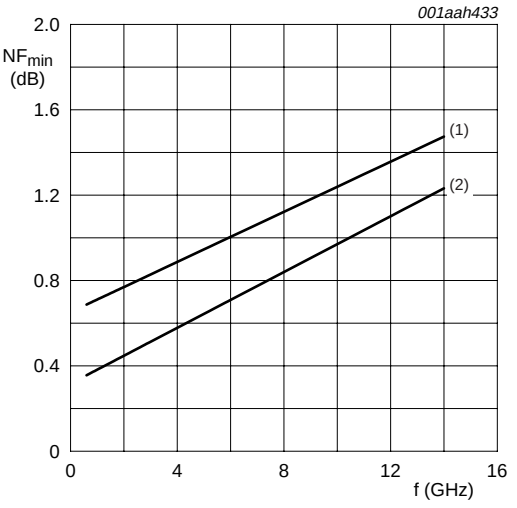
Fig 8. Gain as a function of frequency; typical values



V_{CE} = 2 V; T_{amb} = 25 °C.

(1) f = 12 GHz
(2) f = 5.8 GHz
(3) f = 2.4 GHz
(4) f = 1.8 GHz
(5) f = 1.5 GHz

Fig 9. Minimum noise figure as a function of collector current; typical values



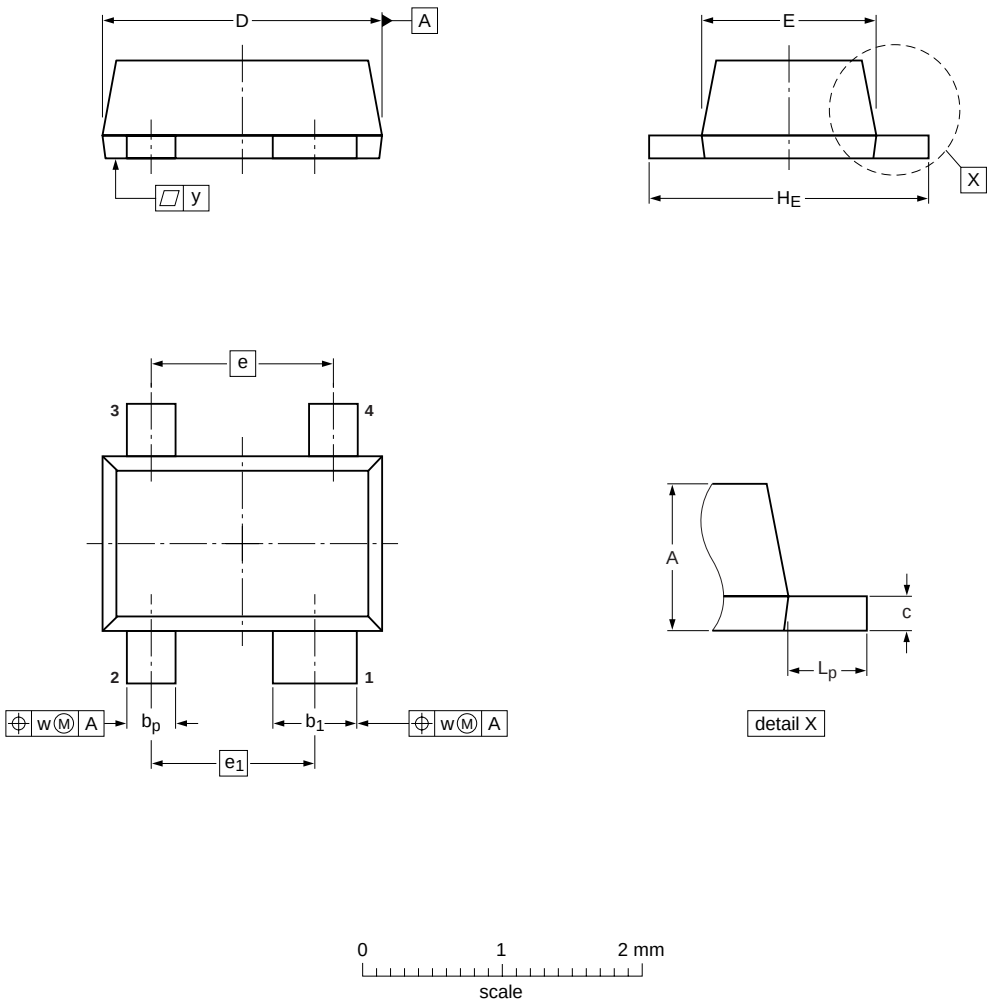
V_{CE} = 2 V; T_{amb} = 25 °C.

(1) I_C = 25 mA
(2) I_C = 5 mA

Fig 10. Minimum noise figure as a function of frequency; typical values

8. Package outline

Plastic surface-mounted flat pack package; reverse pinning; 4 leads **SOT343F**



DIMENSIONS (mm are the original dimensions)

UNIT	A max	b_p	b_1	c	D	E	e	e_1	H_E	L_p	w	y
mm	0.75 0.65	0.4 0.3	0.7 0.5	0.25 0.10	2.2 1.8	1.35 1.15	1.3	1.15	2.2 2.0	0.48 0.38	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT343F						~~05-07-12~~ 06-03-16

Fig 11. Package outline SOT343F

9. Abbreviations

Table 8. Abbreviations

Acronym	Description
CDMA	Code Division Multiple Access
DBS	Direct Broadcast Satellite
DC	Direct Current
DRO	Dielectric Resonator Oscillator
LNA	Low Noise Amplifier
LNB	Low Noise Block
Ka	Kurtz above
NPN	Negative-Positive-Negative
RF	Radio Frequency
WLAN	Wireless Local Area Network

10. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFU725F_N1_1	20090713	Product data sheet	-	-

11. Legal information

11.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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12. Contact information

For more information, please visit: **http://www.nxp.com**

For sales office addresses, please send an email to: **salesaddresses@nxp.com**

13. Contents

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