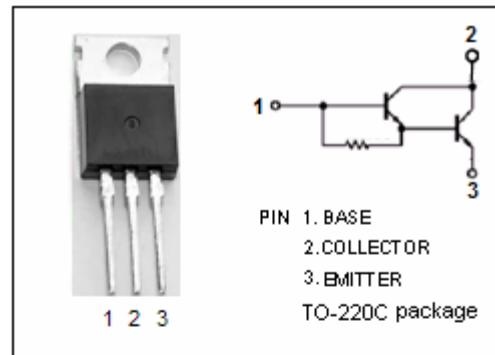


isc Silicon NPN Power Transistors

KSD5018

DESCRIPTION

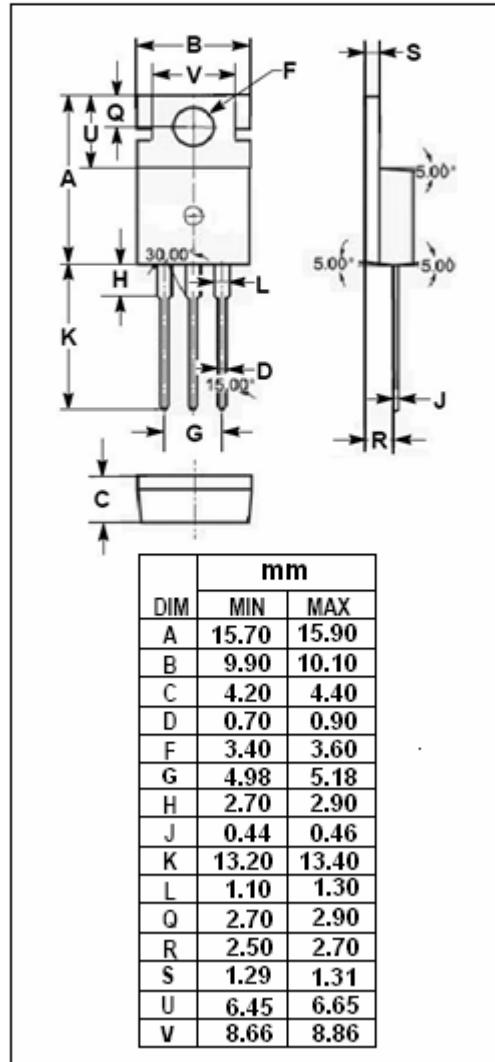
- High Breakdown Voltage-
: $V_{(BR)CEO} = 275V$ (Min)
- Built-in Resistor Between Base and Emitter
- Wide Area of Safe Operation

**APPLICATIONS**

- Designed for motor drive and general purpose applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	600	V
V_{CEO}	Collector-Emitter Voltage	275	V
V_{EBO}	Emitter-Base Voltage	10	V
I_c	Collector Current-Continuous	4	A
I_{CM}	Collector Current-Peak	6	A
I_B	Base Current-Continuous	0.5	A
P_c	Collector Power Dissipation @ $T_c=25^\circ C$	40	W
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-55~150	°C



isc Silicon NPN Power Transistors**KSD5018****ELECTRICAL CHARACTERISTICS**T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CER}	Collector-Emitter Voltage	I _C = 1mA; R _{BE} = 330 Ω	600			V
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = 1.5A; I _{B1} = 0.05A; Clamped	275			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 5mA			1.5	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 20mA			1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 2A; I _B = 5mA			2.0	V
I _{CES}	Collector Cutoff Current	V _{CE} = 500V			1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 10V; I _C = 0			1	mA
h _{FE-1}	DC Current Gain	I _C = 2A ; V _{CE} = 2V	1000			
h _{FE-2}	DC Current Gain	I _C = 4A ; V _{CE} = 2V	200			