

Silicon NPN Power Transistors

2SC3540

DESCRIPTION

With TO-220Fa package

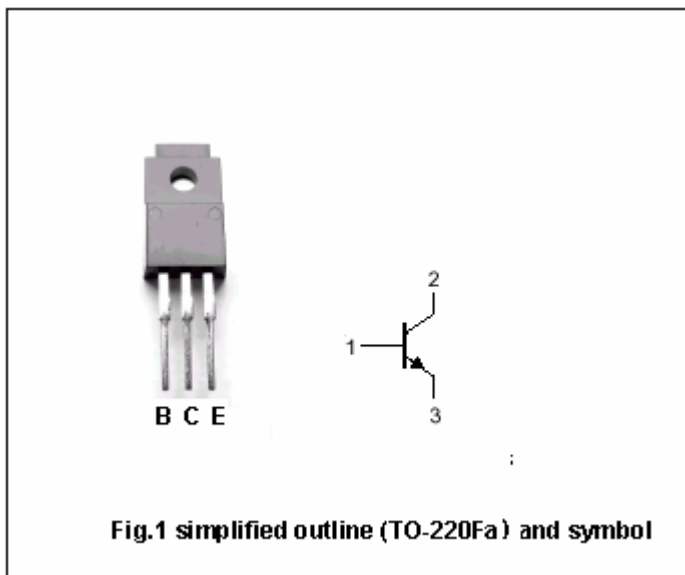
- Low collector saturation voltage
- High speed switching time
- Complement to type 2SA1388

APPLICATIONS

- High current switching applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	100	V
V _{CEO}	Collector-emitter voltage	Open base	80	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current (DC)		5	A
I _{CM}	Collector current-peak		8	A
I _B	Base current (DC)		1	A
P _C	Collector power dissipation	T _a =25°C	2	W
		T _C =25°C	25	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

Silicon NPN Power Transistors

2SC3540

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =10mA ; I _B =0	80			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =3A ; I _B =0.15A		0.2	0.4	V
V _{BEsat}	Base-emitter saturation voltage	I _C =3A ; I _B =0.15A		0.9	1.2	V
I _{CBO}	Collector cut-off current	V _{CB} =100V; I _E =0			1	μA
I _{EBO}	Emitter cut-off current	V _{EB} =7V; I _C =0			1	μA
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =1V	70		240	
h _{FE-2}	DC current gain	I _C =3A ; V _{CE} =1V	40			
f _T	Transition frequency	I _C =1A ; V _{CE} =4V		120		MHz
C _{ob}	Collector output capacitance	I _E =0 ; V _{CE} =10V; f=1MHz		80		pF

Switching times

t _{on}	Turn-on time	I _{B1} =-I _{B2} =0.15A V _{CC} ≈30V; R _L =10Ω		0.2		μs
t _{stg}	Storage time			1.0		μs
t _f	Fall time			0.1		μs

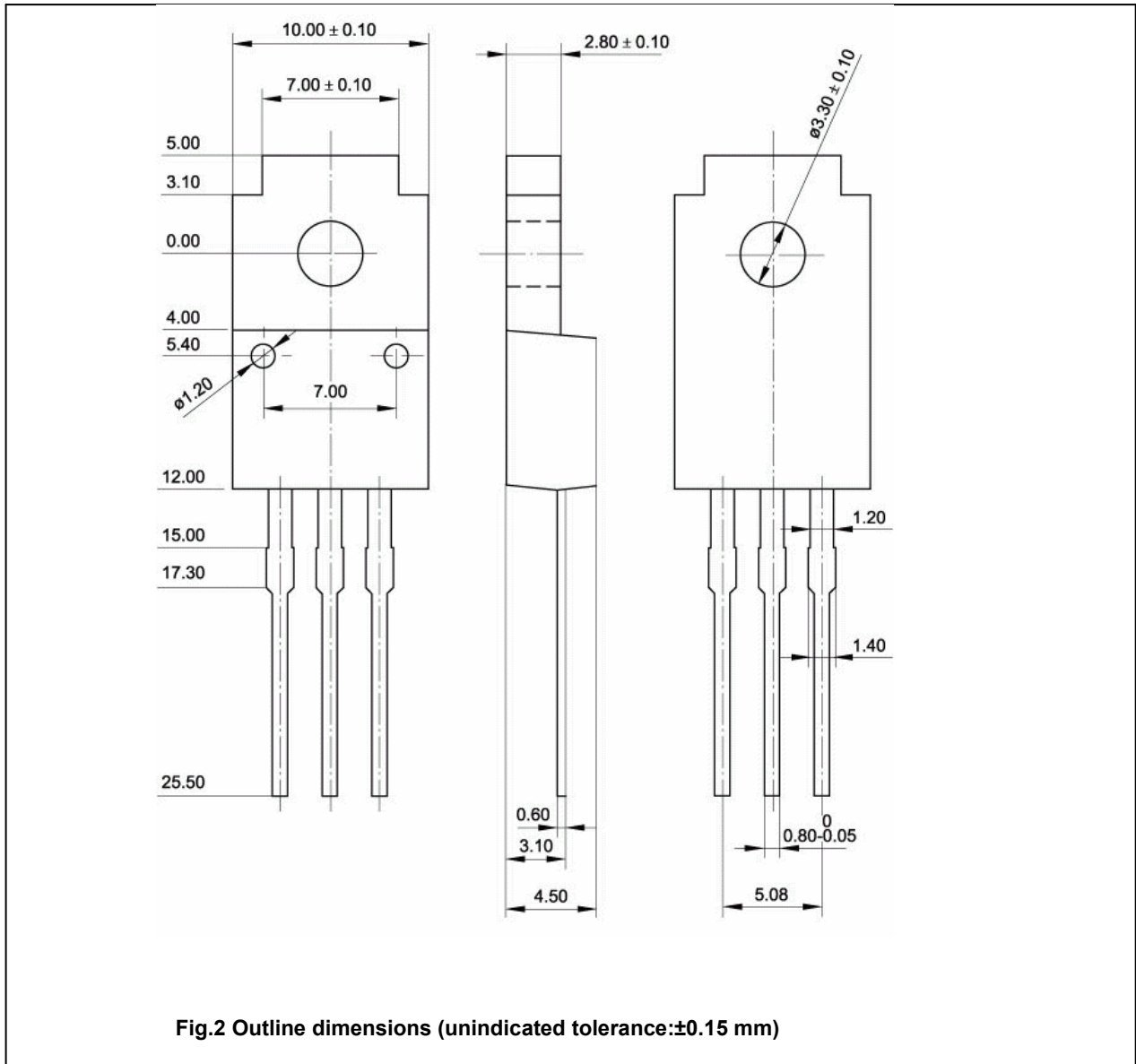
◆ h_{FE-1} Classifications

O	Y
70-140	120-240

Silicon NPN Power Transistors

2SC3540

PACKAGE OUTLINE



Silicon NPN Power Transistors

2SC3540

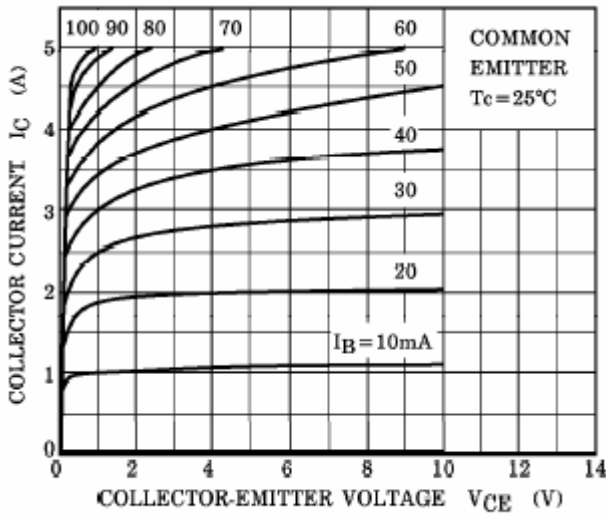


Fig.3 Static Characteristic

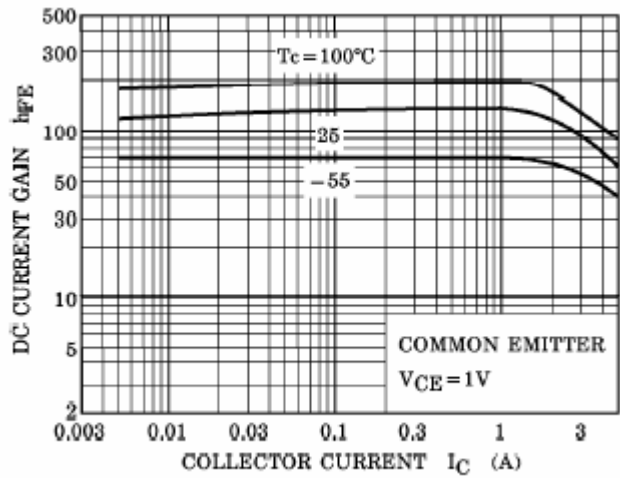


Fig.4 DC current Gain

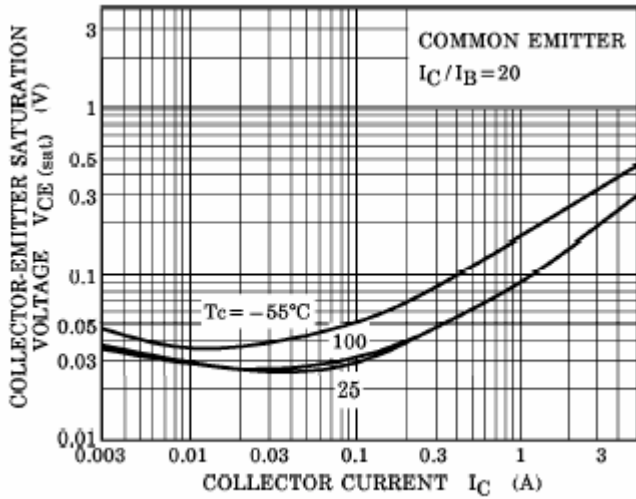


Fig.5 Collector-Emitter Saturation Voltage

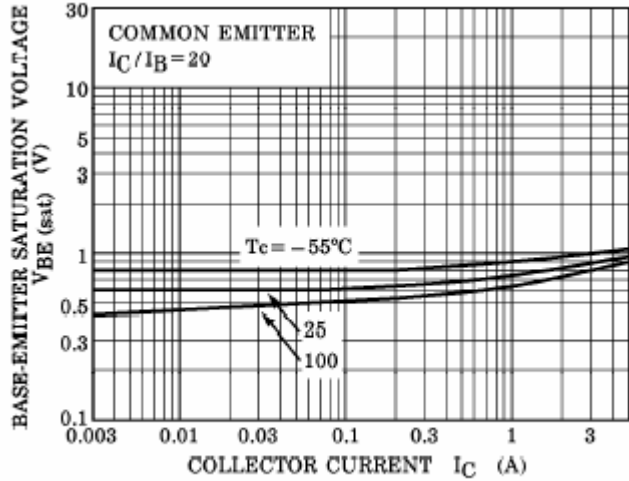


Fig.6 Base-Emitter Saturation Voltage

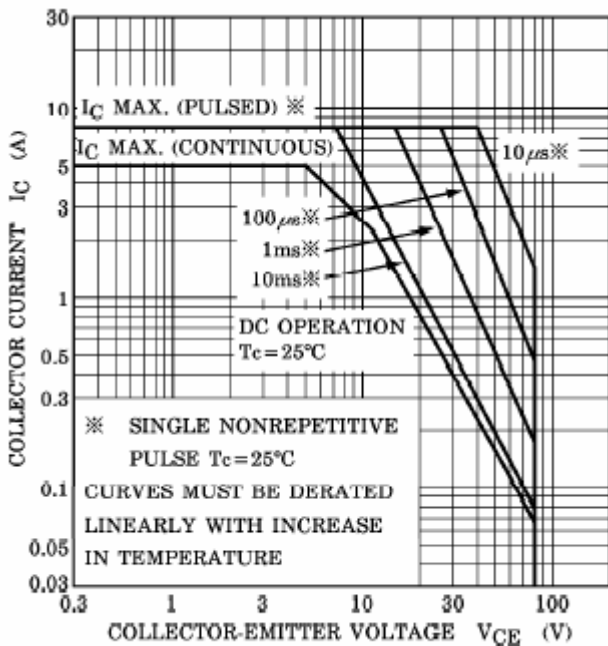


Fig.7 Safe Operating Area