

# IN5400 thru IN5408

## PLASTIC SILICON RECTIFIER

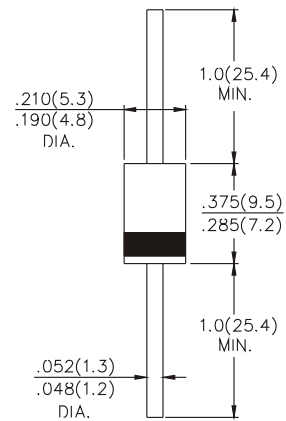


**CHENG-YI  
ELECTRONIC**



VOLTAGE RANGE 50 TO 1000 Volts  
CURRENT 3.0 Amperes

### DO-201AD



Dimensions in inches and (millimeters)

#### FEATURE

- Low forward voltage
- High current capability
- Low leakage current
- High surge capability
- Low cost

#### MECHANICAL DATA

- Case: Mold plastic use UL 94V-0 recognized flame retardant epoxy
- Terminals: Axial leads, solderable per MIL-STD-202, method 208
- Polarity: Color band denotes cathode
- Mounting Position: Any

#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.  
Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

	IN5400	IN5401	IN5402	IN5403	IN5404	IN5405	IN5406	IN5407	IN5408	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	300	400	500	600	800	1000	V
Maximum RMS Voltage	35	70	140	210	280	350	420	560	700	V
Maximum DC Blocking Voltage to $T_A = 150^\circ\text{C}$	50	100	200	300	400	500	600	800	1000	V
Maximum Average Forward Rectified Current .5", (12.5mm) Lead Length at $T_A = 75^\circ\text{C}$	3.0									A
Peak Forward Surge Current 8.3 ms single half sine-wave	150									A
Maximum Forward Voltage at 3.0A Peak	1.2									V
Maximum Reverse Current, $T_A = 25^\circ\text{C}$ at Rated DC Blocking Voltage $T_A = 55^\circ\text{C}$	10									$\mu\text{A}$
	500									$\mu\text{A}$
Maximum Full Load Reverse Current, Full Cycle Average, .5", (12.5mm) Lead Length $T_A = 105^\circ\text{C}$	500									$\mu\text{A}$
Typical Junction Capacitance (Note 1)	50									pF
Storage Temperature Range $T_A$	-65 to +175									$^\circ\text{C}$
Operating Temperature Range $T_J$	-65 to +170									$^\circ\text{C}$

Notes : 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 Vbc  
\* JEDEC Registered Value.

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### RATING AND CHARACTERISTICS CURVES IN5400 THRU IN5408

Fig. 1 - TYPICAL FORWARD CHARACTERISTICS

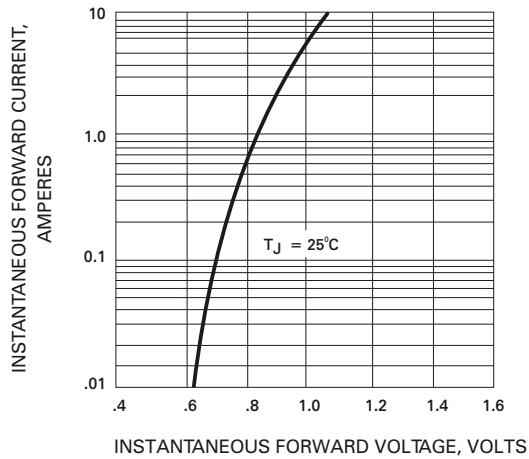


Fig. 2 - PEAK FORWARD SURGE CURRENT

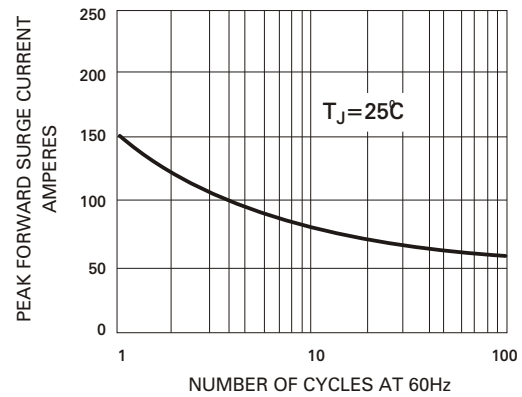


Fig. 3 - FORWARD CURRENT DERATING CURVE

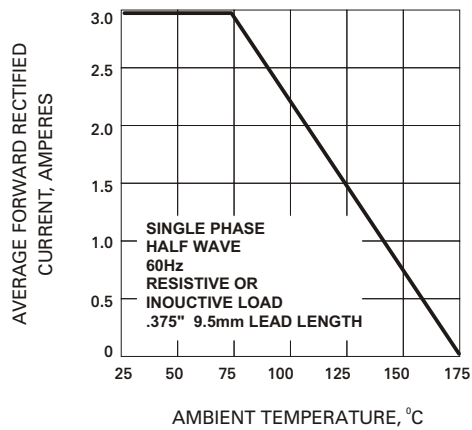


Fig. 4 - TYPICAL JUNCTION CAPACITANCE

