TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOS IV)

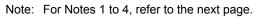
TPC8025

Lithium-Ion Battery Applications Portable Equipment Applications Notebook PC Applications

- Small footprint due to a small and thin package
- Low drain-source ON-resistance: R_{DS} (ON) = 7.5 m Ω (typ.)
- High forward transfer admittance: $|\,Y_{\rm fs}\,|$ = 26 S (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 30 \ V)$
- Enhancement mode: V_{th} = 1.3 to 2.5 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	30	V
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$)	V _{DGR}	30	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	۱ _D	11	А
Drain current	Pulse (Note 1)	I _{DP}	44	~
Drain power dissipati	on (t = 10 s) (Note 2a)	PD	1.9	W
Drain power dissipati	on (t = 10 s) (Note 2b)	PD	1.0	W
Single pulse avalanche energy (Note 3)		E _{AS}	31	mJ
Avalanche current		I _{AR}	11	А
Repetitive avalanche (energy Note 2a) (Note 4)	E _{AR}	0.053	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	–55 to 150	°C



Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

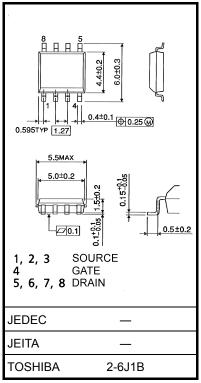
temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.)

are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

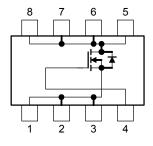
This transistor is an electrostatic-sensitive device. Handle with care.





Weight: 0.08 g (typ.)

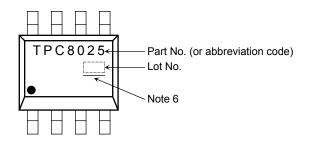
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 10 \ s)$ (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

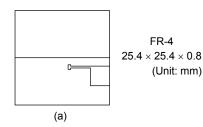
Marking (Note 5)

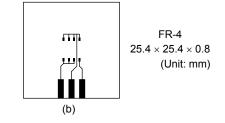


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





- Note 3: $V_{DD}=-24$ V, $T_{ch}=25^{\circ}C$ (initial), L=0.2 mH, $I_{AR}=11$ A
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5: on the lower left of the marking indicates Pin 1.
 - * Weekly code: (Three digits)



Week of manufacture _(01 for first week of year, continuing up to 52 or 53) -Year of manufacture (The last digit of the calendar year)

Note 6: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

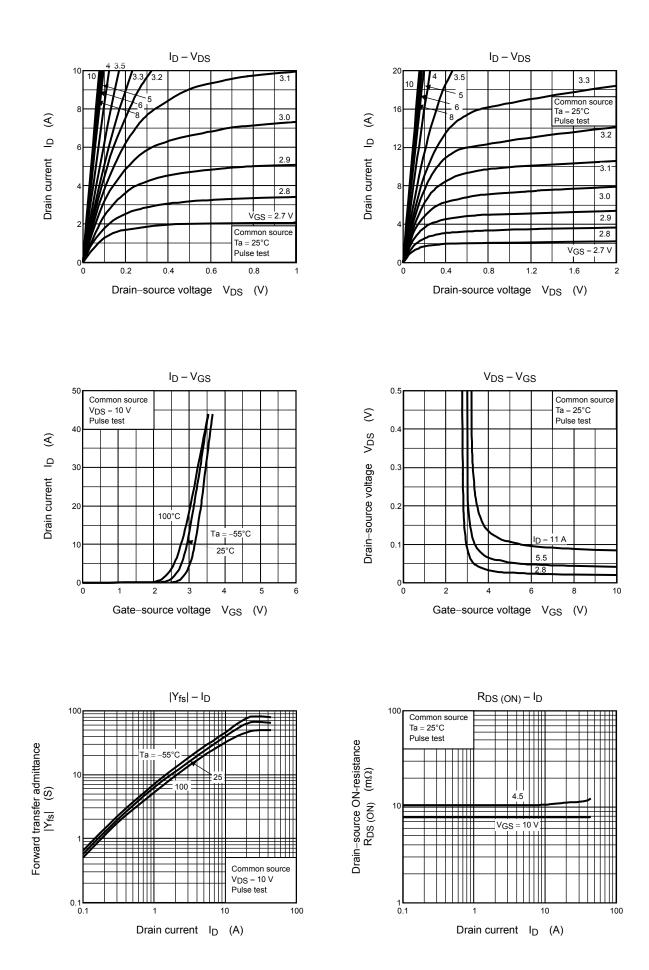
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

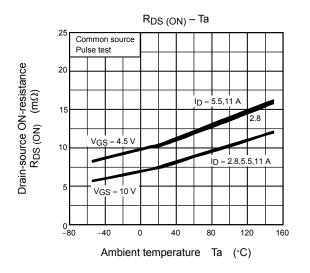
Electrical Characteristics (Ta = 25°C)

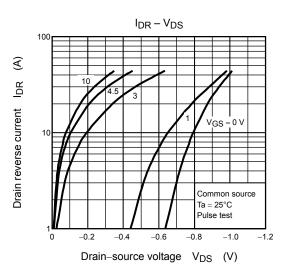
Cha	Characteristics		Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	_	—	±100	nA
Drain cut-OFF cu	ırrent	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
	Puput capacitance Putput capacitance Rise time Turn-ON time	V (BR) DSS	$I_{D} = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
Drain-source bre	akuown voltage	V (BR) DSX	$I_D = 10$ mA, $V_{GS} = -20$ V	10	±100 - 10 30 10	v	
Gate threshold ve	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	1.3	_	2.5	V
			$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 5.5 \text{ A}$	_	10.5	14.5	
Drain-source ON	-Tesisiance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5.5 \text{ A}$	_	7.5	9	mΩ
Forward transfer admittance		Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 5.5 \text{ A}$	13	26		S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	1270		pF
Reverse transfer capacitance		C _{rss}			240		
Output capacitance		C _{oss}			380		
	Rise time	tr	10 V 🗖 🖢 = 5 5 A	_	12	_	- ns
Switching time	Turn-ON time	t _{on}	$V_{GS} \stackrel{10 \text{V}}{}_{0 \text{V}} \stackrel{I_{D} = 5.5 \text{A}}{\underset{O \text{V}}{}_{O \text{V}}}$		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
Switching time	Fall time	t _f	4.7 Ω 4.7 Ω 4.7 Ω	_	9	_	
	Turn-OFF time	t _{off}	$\frac{d\vec{z}}{V_{DD}\approx 15~V}$ Duty \leq 1%, $t_{W}=$ 10 μs	_	35	_	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 11 A		26	_	nC
Gate-source charge 1		Q _{gs1}			3.8		
Gate-drain ("miller") charge		Q _{gd}			8		

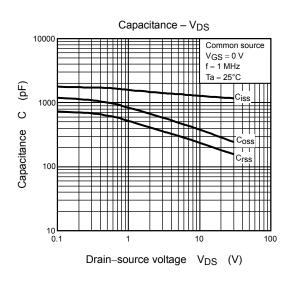
Source-Drain Ratings and Characteristics (Ta = 25°C)

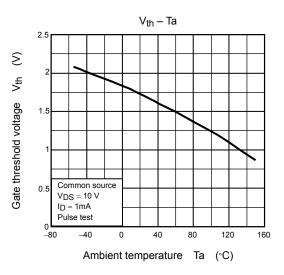
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	_	_	44	А
Forward voltage (diode)			V _{DSF}	$I_{DR} = 11 \text{ A}, V_{GS} = 0 \text{ V}$	_		-1.2	V

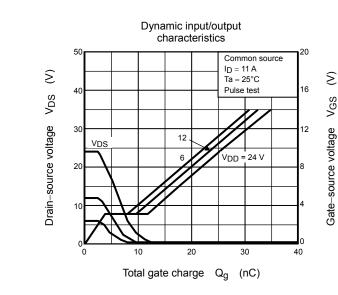


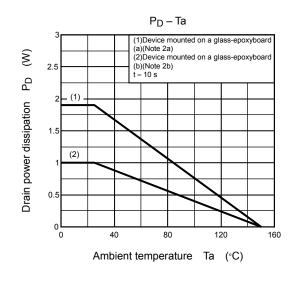


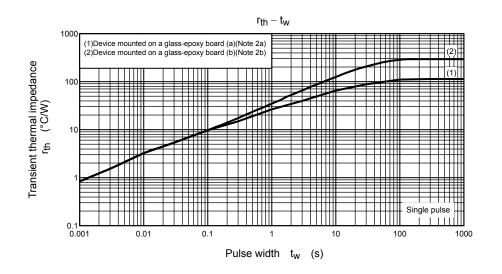


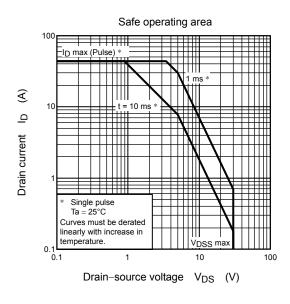












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