Elektronische Bauelemente

S5JLM2931

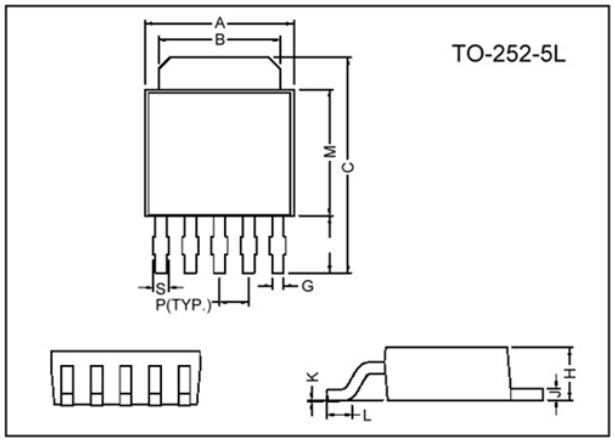
100mA CMOS

Low Dropout Adjustable Voltage Regulator

RoHS Compliant Product

Description

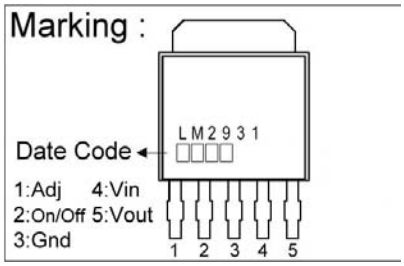
The S5JLM2931 positive voltage regulator features a very low quiescent current of 1mA or less when supplying 10mA loads. This unique characteristic and the extremely low input-output differential required for proper regulation (0.2V for output current of 10mA) make the S5JLM2931 the ideal regulator for standbe power system. Applications include memory standby circuits, COMS and other low power processor power supplies as well as systems demanding as much as 100mA of output current. Designed originally for automotive applications, the S5JLM2931 and all regulated circuitry are protected from reverse battery installations or 2 battery jumps. During line transients, such as a load dump (60V) when the input voltage to the regulator can momentarily exceed the specified max. operating voltage, the regulator will automatically shut down to protect both internal circuits and the load. The S5JLM2931 cannot be harmed by temporary mirror-image insertion. Familiar regulator features such as short circuit and thermal overload protection are also provided.



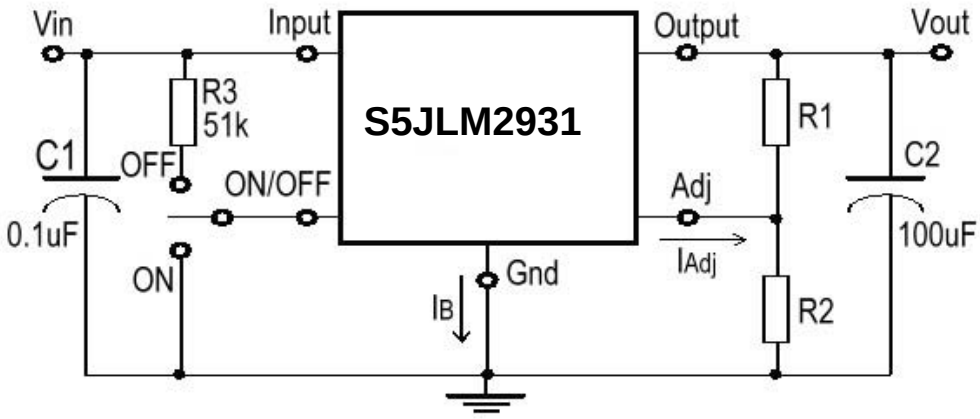
REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.35	6.73	G	0.45	0.60
B	5.21	5.46	H	2.20	2.40
C	9.40	10.20	J	0.46	0.58
D	2.40	3.00	K	0	0.15
P	1.27 REF.		L	0.90	1.50
S	0.50	0.80	M	5.40	5.59

Features

* 60V Load Dump Protection
* Internal Current Limiting With Thermal Shutdown
* Input-to-Output Voltage Differential Of Less Then 0.6V at 100mA
* Temporary Mirror-Image Protection
* -50V Reverse Transient Protection



Typical Applications



$V_{OUT}=V_{REF}*(1+R2/R1)+I_{ADJ}*R2$

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Input Voltage	V_I	-15~40	V
Input Voltage t 100ms	$V_{I(T)}$	-50~60	V
Output Current	I_O	100	mA
Storage Temperature	T_{STG}	-60~+150	°C
Junction Temperature	Tj	-40~+125	°C
Resistance Junction-Ambient	$R\theta_{JA}$	102	°C/W

Electrical Characteristics

V_{IN}=14V, V_{OUT}=3V, I_O=10mA, T_J=25℃, Ci=0.1uF, Co=100uF, R1=27k (unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reference voltage	V_{REF}	I_O=10mA	1.14	1.20	12.6	V
		I_O≤100mA, -40℃≤ T_J ≤125℃ Measure from V_{OUT} to Adj Pin	1.08		1.32	V
Output voltage	V_{OUT}		3		24	V
Line regulation	Reg_{Line}	V_{OUT}+0.6V≤ V_{IN} ≤ 26V	-	-	1.5	mV/V
Load regulation	Reg_{Load}	5mA≤ I_O ≤ 100mA	-	-	1.0	%
Consumption current	I_B	I_O=10mA	-	-	1.0	mA
		I_O=100mA	-	6	-	
		Output is "off" ($V_{th(OI)}$=2.5V)	-	-	1.0	
Adjustment current	I_{Adj}		-	0.2	-	uA
Dropout voltage	V_{ds}	I_O=10mA I_O=100mA	-	-	0.2 0.6	V
Output Impedance	Z_O	ΔIO=1mA, f=10Hz~100kHz	-	40	-	mΩ/V
Noise voltage on output	Vn	f=10Hz~100kHz	-	140	-	mV/V
Pulse-smoothing ratio	RR	f=120Hz	0.1	-	-	%/V
Temporary unstability output voltage	S		-	0.4	-	%/1000hr
Maximum input voltage threshold	$V_{th(OV)}$		26	-	40	V
Output voltage at negative input voltage	$-V_O$	V_{IN}=-15V	-0.3	-	-	V
Voltage threshold of disconnection output	$V_{th(OI)}$	Output is "ON"	-	-	1.9	V
		Output is "OFF"	2.5	-	-	
Disconnection output current	$I_{th(OI)}$	$V_{th(OI)}$=2.5V	-	-	50	uA

Definition of Terms

Dropout Voltage: The input-output voltage differential at which the circuit ceases to regulate against further reduction in input voltage. Measured when the output voltage has dropped 100mV from the normal voltage obtained at 14V input, dropout voltage is dependent upon load current and junction temperature.

Input Voltage: The DC voltage applied to the input terminals with respect to ground.

Input-Output Differential: The Voltage difference between the unregulated input voltage and the regulated output voltage for which the regulator will operate.

Line Regulation: The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by usung pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation: The change in output voltage for a change in load current at constant chip temperature.

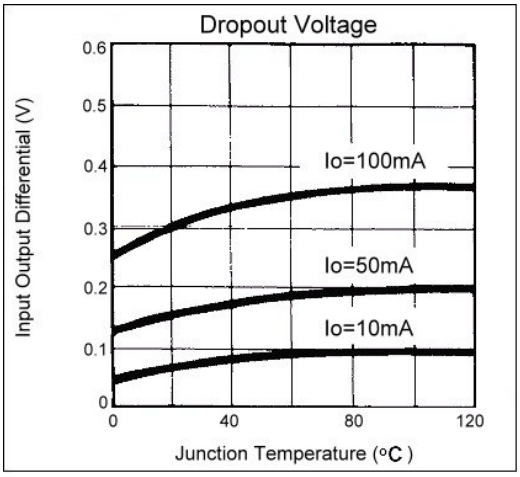
Long Term Stability: Output voltage stability under accelerated life-test conditions after 1000 hours with max. rate voltage and junction temperature.

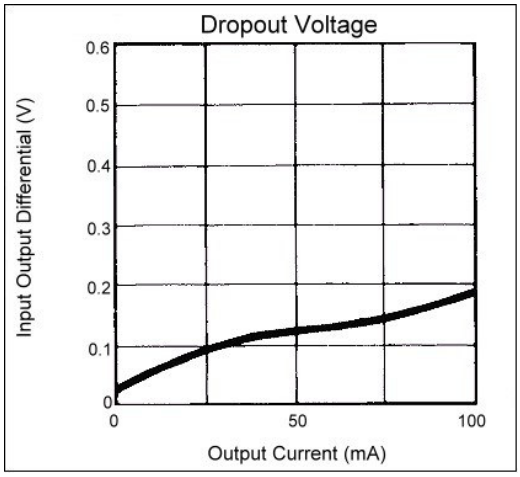
Output Noise Voltage: The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

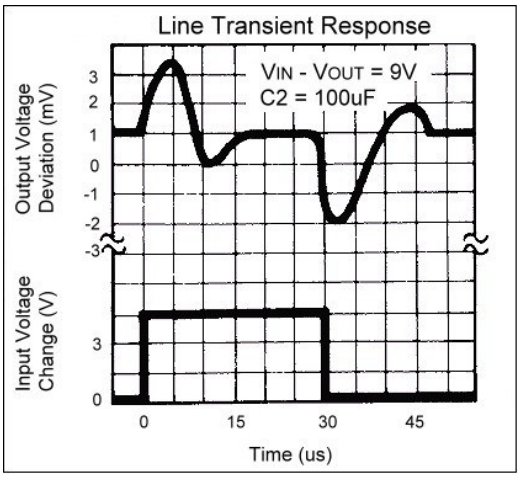
Quiescent Current: That part of the positive input current that does not contribute to the positive load current. The regulator ground lead current.

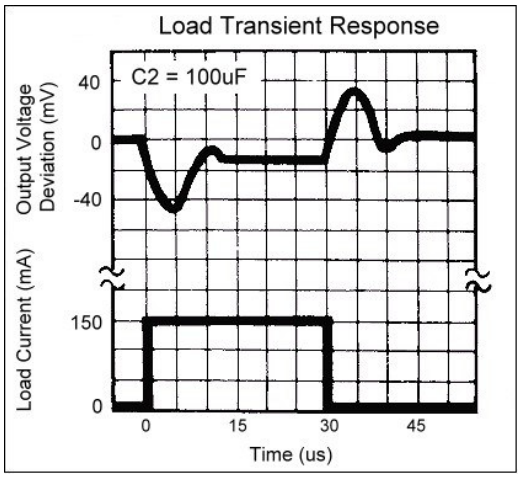
Ripple Rejection: The ratio of the peak-of-peak input ripple voltage to the peak-of-peak output ripple voltage at a specified frequency.

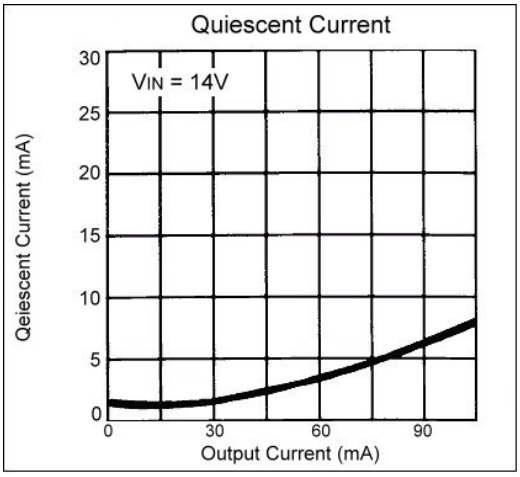
Typical Performance Characteristics

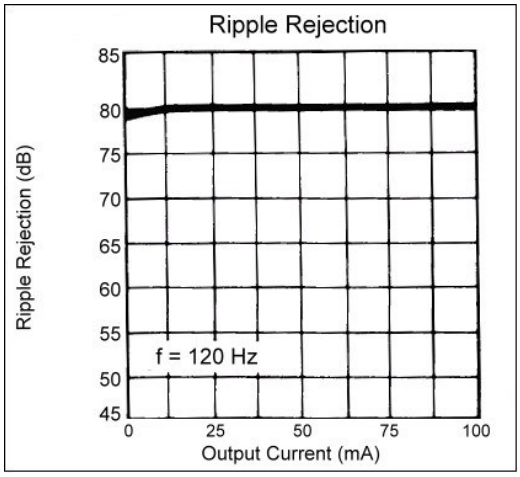


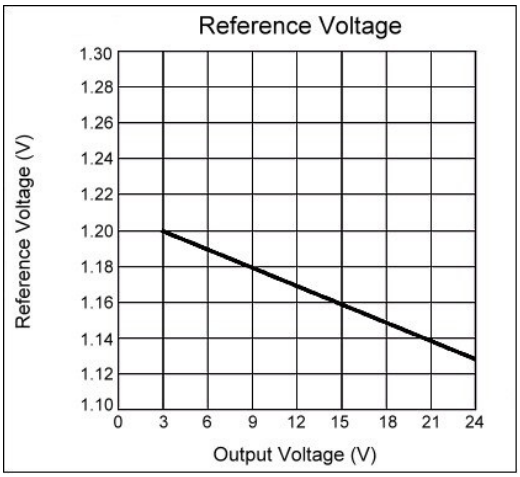


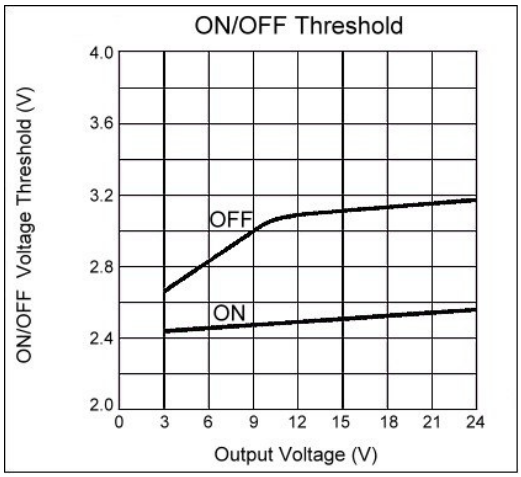









Reference Voltage
Reference Voltage (V)
1.30
1.28
1.26
1.24
1.22
1.20
1.18
1.16
1.14
1.12
1.10
0 3 6 9 12 15 18 21 24
Output Voltage (V)


ON/OFF Threshold
ON/OFF Voltage Threshold (V)
4.0
3.6
3.2
2.8
2.4
2.0
OFF
ON
0 3 6 9 12 15 18 21 24
Output Voltage (V)