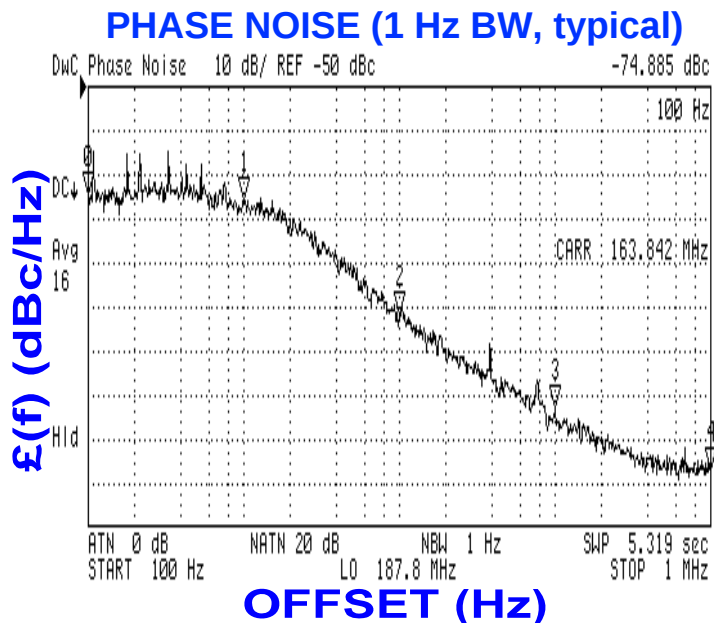


FEATURES

- Frequency Range: 163.84 - 163.84 MHz
- Step Size: 80 KHz
- cPLL - Style Package

APPLICATIONS

- Telecommunications
- Satellite
- Telemetry



PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	163.84 - 163.84	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-102	dBc/Hz
Harmonic Suppression (2nd, typ.)	-10	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	80	KHz
Charge Pump Output Current	1250	μA
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	cPLL	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	3	Vdc
Supply Current (Icc, typ.)	21	mA

All specifications are typical unless otherwise noted and subject to change without notice.

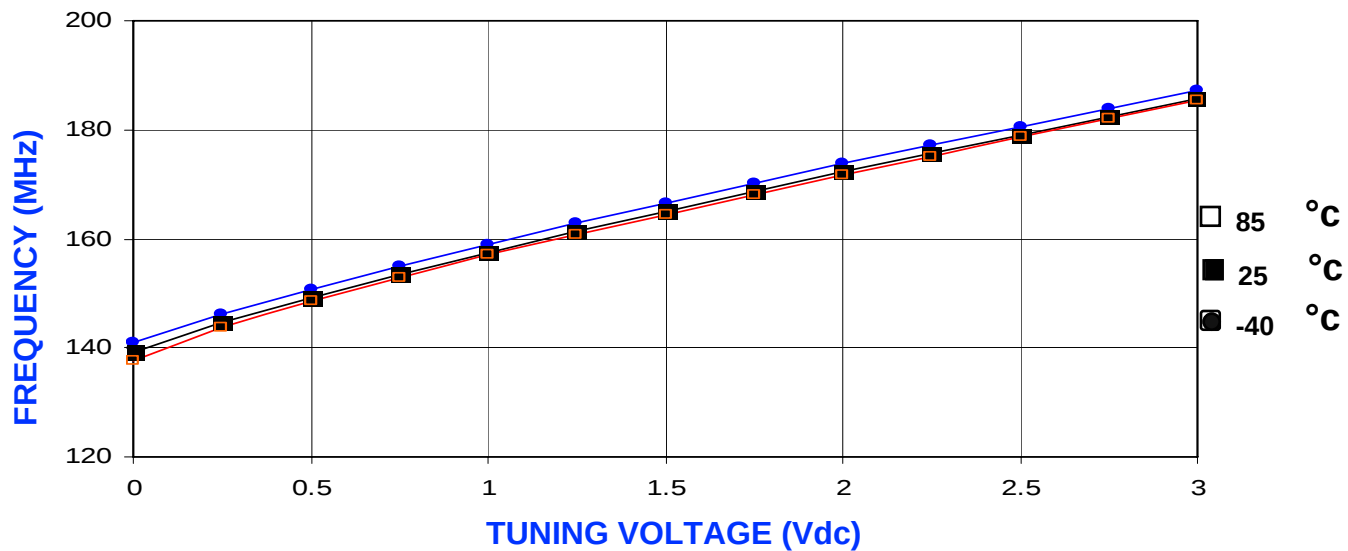
APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

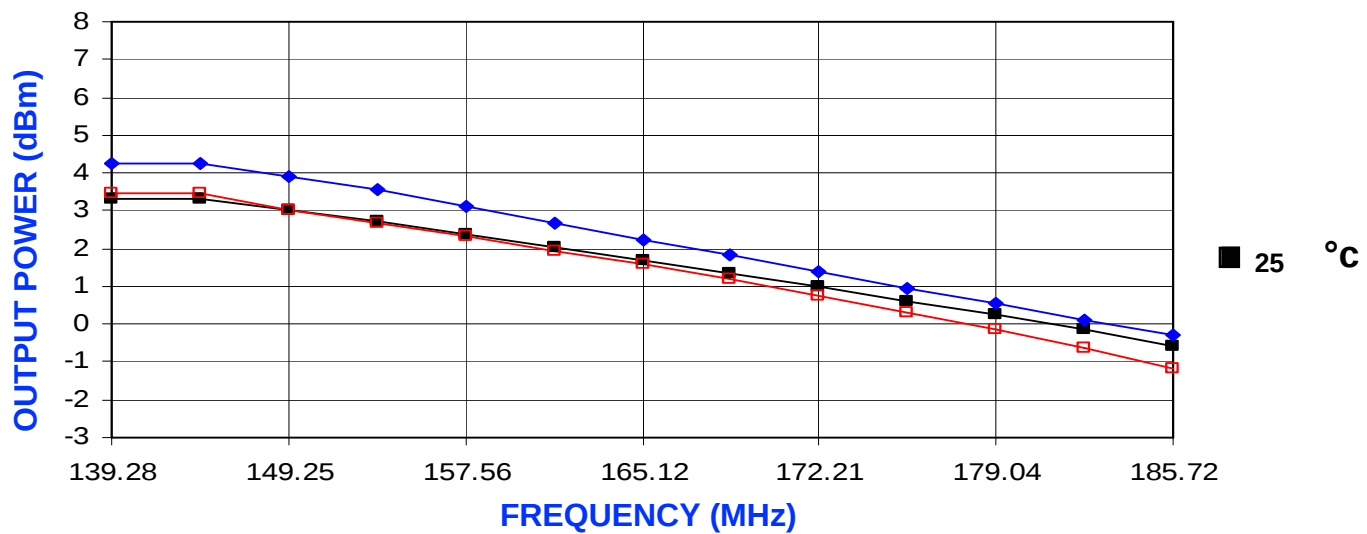
NOTES:

Reference Oscillator Signal: 5 MHz < f_{osc} < 100 MHz
 Frequency Synthesizer: Analog Devices - ADF4001

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

