

AWT6132R

415 MHz CDMA 3.4V/29.5dBm

Linear Power Amplifier Module

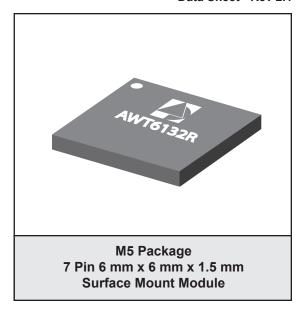
Data Sheet - Rev 2.1

FEATURES

- InGaP HBT Technology
- · High Efficiency 35 % CDMA
- Low Leakage Current (<5 μA)
- · SMT Module Package
- 50 Ω Input and Output Matching
- · Shut Down and Mode Control
- CDMA 2000 1XRTT Compliant
- RoHS Compliant Package, 250 °C MSL-3

APPLICATIONS

 CDMA/EVDO 415 MHz Wireless Handsets and Data Devices



PRODUCT DESCRIPTION

The AWT6132R is a high power, high efficiency amplifier module for CDMA wireless handset applications. The device is manufactured on an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability and ruggedness. Alow power mode is available to reduce

power drain on the system battery. The 6 mm x 6 mm x 1.5 mm laminate package is self contained, incorporating 50 Ω input and output matching networks optimized for output power, linearity, and efficiency.

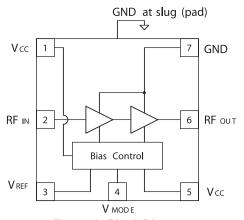


Figure 1: Block Diagram

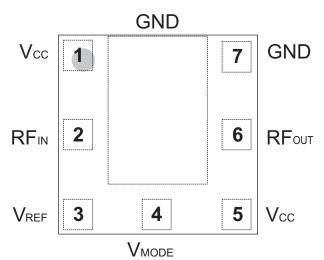


Figure 2: Pinout (X-ray Top View)

Table 1: Pin Description

PIN	NAME	DESCRIPTION		
1	Vcc	Supply Voltage		
2	RFℕ	RF Input Signal		
3	V _{REF}	Reference Voltage		
4	V _{MODE}	Mode Control		
5	Vcc	Supply Voltage		
6	RFout	RF Output		
7	GND	Ground		

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

PARAMETER	MIN	MAX	UNIT
Supply Voltage (Vcc)	0	+5	V
Mode Control Voltage (VMODE)	0	+3.4	V
Reference Voltage (VREF)	0	+3.4	V
RF Input Power (P _N)	1	+10	dBm
Storage Temperature (Tstg)	-40	+150	°C

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Table 3: Operating Ranges

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Operating Frequency (f)	410	-	420	MHz	
Supply Voltage (Vcc)	+3.2	+3.4	+4.2	V	
Reference Voltage (V _{REF})	+2.80 0	+2.90	+3.1 +0.5	V	PA"on" PA"shut down"
Mode Control Voltage (V _{MODE})	+2.5 0	+2.90	+3.1 +0.5	٧	Low Bias Mode High Bias Mode
RF Output Power (Pout)	+29.0 (1)	+29.5	-	dBm	CDMA, Vcc =+3.4V
Case Temperature (Tc)	-30	-	+90	°C	

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Notes:

(1) For operation at Vcc = +3.2 V, Pout is derated by 0.5 dB.



Table 4: Electrical Specifications – CDMA Operation (T_c = +25 °C, V_{cc} = +3.4 V, V_{REF} = +2.90 V, 50 Ω system)

(1025 0, 4005.4 v, 48EF2.50 v, 50 22 System)						
PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS	
Gain	26 25	28 27	31 29	dB	POUT = +29.5 dBm, VMODE = 0 V POUT = +16 dBm, VMODE = +2.90 V	
Gain Variation	-	0.4	1.0	dB		
Adjacent Channel Power at ±885 kHz offset ⁽¹⁾ Primary Channel BW = 1.23 MHZ Adjacent Channel BW = 30 kHz		-52 -57	-46.5 -47	dB	POUT = +29.5 dBm, VMODE = 0 V POUT = +16 dBm, VMODE = +2.90 V	
Adjacent Channel Power at ±1.98 MHz offset ⁽¹⁾ Primary Channel BW = 1.23 MHZ Adjacent Channel BW = 30 kHz	1 1	-62 -67	-59 -59	dB	Pout = +29.5 dBm, VMODE = 0 V Pout = +16 dBm, VMODE = +2.90 V	
Power-Added Efficiency (1)	33 6	35 7	1 1	%	POUT = +29.5 dBm, VMODE = 0 V POUT = +16 dBm, VMODE = +2.90 V	
Quiescent Current (lcq)	-	60	80	mA	through Vcc pins, VMODE = +2.90 V	
Reference Current	-	6.5	9	mA	through VREF pin, PA "on"	
Mode Control Current	-	0.8	1.0	mA	through VMODE pin, VMODE = +2.90 V	
Leakage Current	-	<1	5	μΑ	Vcc = +4.2 V, VREF = 0 V, VMODE = 0 V	
Noise in Receive Band	-	-132	-130	dBm/Hz	fo +10 MHz, Poυτ <u><</u> +29.5 dBm	
Harmonics 2fo 3fo, 4fo	1 1	-36 -45	-30 -35	dBc	Pουτ <u><</u> +29.5 dBm	
Input Impedance	-	-	2:1	VSWR		
Spurious Output Level (all spurious outputs)	-	-	-70	dBc	POUT < +29.5 dBm In-band Load VSWR < 5:1 Out-of-band Load VSWR < 10:1 Applies over all operating ranges	
Load mismatch stress with no permanent degradation or failure	10:1	-	-	VSWR	Applies over all operating ranges	

Notes:

⁽¹⁾ Pout is derated by 0.5 dB for IS-98 / CDMA 2000 operation.

⁽²⁾ ACPR and Efficiency Measured at 415 MHz.

APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes on the ANADIGICS web site: http://www.anadigics.com

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to both the V_{REF} and V_{MODE} voltages.

High Bias Mode

The power amplifier may be placed in a high bias mode by applying a logic low level (see Operating Ranges table) to the V_{MODE} voltage.

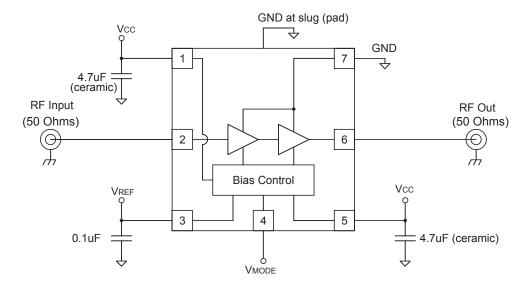


Figure 3: Application Circuit Schematic

PACKAGE OUTLINE .10[0.004] (SQUARE) GROUND PIN#1 PIN В G (PITCH) Η1 D₁ (6X)E (SQUARE) (SET BACK)

NOTE NOM. MIN. NOM. 5.88 Α 6.00 6.12 0.231 0.236 0.241 5.88 6.00 6.12 0.231 0.236 0.241 В 1.30 1.55 1.70 0.051 0.061 0.067 С 0.10 0.004 0.024 D 0.60 _ Dı 0.60 0.024 Ε 0.032 0.81 _ F 0.035 0.89 G 2.50 BSC 0.098 BSC 3 Н 3.00 0.118 Нı 4.39 0.173

INCHES

MILLIMETERS

NOTES:

- 1. CONTROLLING DIMENSIONS: MILLIMETERS
 2. UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
- 3. REFERENCE ONLY.

Figure 4: M5 Package Outline - 7 Pin 6 x 6 x 1.5 mm Surface Mount Module (Low Band)

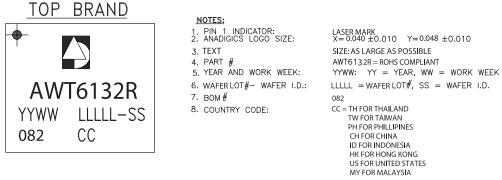


Figure 5: Branding Specification

COMPONENT PACKAGING

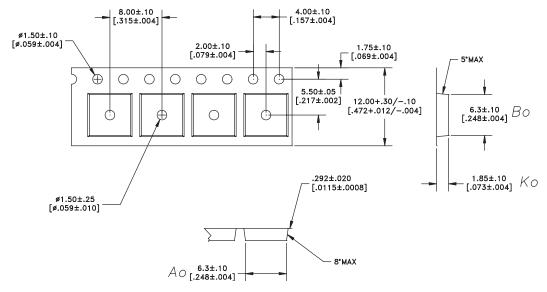


Figure 6: Tape & Reel Packaging

Table 5: Tape & Reel Dimensions

PACKAGE TYPE	TAPE WIDTH	POCKET PITCH	REEL CAPACITY	MAX REEL DIA
6 mm x 6 mm x 1.5 mm	12 mm	8 mm	2500	13"

AWT6132R ORDERING INFORMATION

ORDER	RNUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AWT61	132RM5P8	-30 °C to +90 °C	RoHS Compliant 7 Pin 6 mm x 6 mm x 1.5 mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel
AWT61	132RM5P9	-30 °C to +90 °C	RoHS Compliant 7 Pin 6 mm x 6 mm x 1.5 mm Surface Mount Module	Partial Tape and Reel



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