

Tri-State Enable/Disable 3.3V Oscillator



Model: H5C-2E3LF

RoHS Compliant / Pb Free

Rev. 9/18/2006

http://www.foxonline.com/need_a_sample.htm



FEATURES

- 3.3V Operation
- HCMOS Output
- Tri-State Enable/Disable
- 8-Pin DIP

OPTIONS

- 14-Pin DIP Available (F5C-2E3LF)

Learn more about:
[Part Marking Identification](#)
[Mechanical Specification](#)

Internet required

• PART NUMBER SELECTION [Learn More](#) - Internet Required

Part Number	Model Number	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
350LF-Freq-xxxxx	H5C-2E3LF	±100PPM	0 ~ +70	1.000~160.000
351LF-Freq-xxxxx	H5C-2E3RLF	±100PPM	-40 ~ +85	1.000~160.000
272LF-Freq-xxxxx	H6C-2E3LF	±50PPM	0 ~ +70	1.000~160.000
352LF-Freq-xxxxx	H6C-2E3RLF	±50PPM	-40 ~ +85	1.000~160.000
354LF-Freq-xxxxx	H7C-2E3LF	±25PPM	0 ~ +70	1.000~160.000
353LF-Freq-xxxxx	H7C-2E3RLF	±25PPM	-40 ~ +85	1.000~125.000
566LF-Freq-xxxxx	H8C-2E3LF	±20PPM	0 ~ +70	1.000~125.000

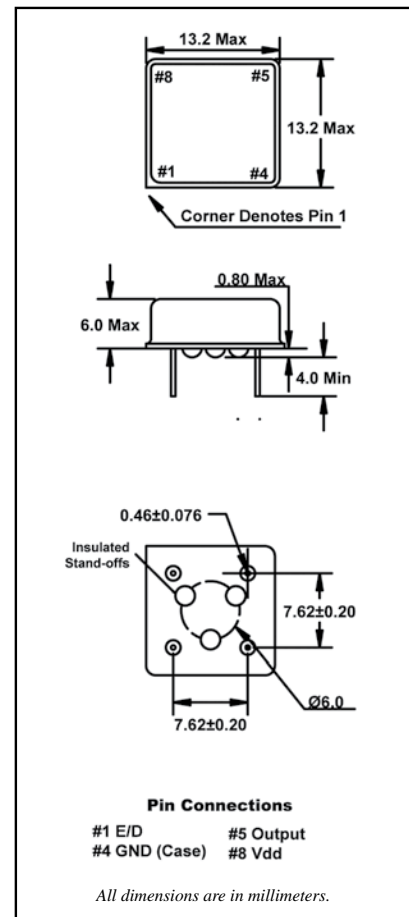
• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	1.000 ~ 160.000 MHz
Storage Temperature Range (TSTG)	-55°C ~ +125°C
Supply Voltage (VDD)	3.3V ± 10%
Input Current (IDD)	
1.000 ~ 23.999 MHz	15mA
24.000 ~ 49.999 MHz	20mA
50.000 ~ 69.999 MHz	40mA
70.000 ~ 125.000 MHz	45mA
125.000 ~ 160.000 MHz	60mA
Output Symmetry (50% VDD)	40% ~ 60%
Rise Time (10% ~ 90% VDD) (Tr)	10 nS
Fall Time (90% ~ 10% VDD) (Tf)	10 nS
Output Voltage (VOL)	10% VDD
(VOH)	90% VDD Min
Output Current (IOL)	8mA Min
(IOH)	-8mA Min
Output Load (HCMOS)	15pF
Start-up Time (Ts)	10mS
Output Enable/Disable Time ²	100nS

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

² An internal pullup resistor from pin 1 to VDD allows active output if pin 1 is left open.

All specifications subject to change without notice.



• ENABLE / DISABLE FUNCTION

INH (Pin 1)	OUTPUT (Pin 5)
OPEN ²	ACTIVE
'1' Level V _{IH} ≥ 2.2 V	ACTIVE
'0' Level V _{IL} ≤ 0.8 V	High Z