



AND-TFT-35XS-LED

3.5" TFT LCD

LCD Color Monitor

The AND-TFT-35XS-LED is a compact full color TFT LCD module, that is suitable for camcorder, digital camera applications and other electronic products which require high quality flat panel displays. This device consists of a twisted nematic (TN) liquid crystal cell, that incorporates a TFT-array that has 320 x 234 pixels on a 3.5 inch diagonal screen, X and Y drivers, an LSI controller, and a built-in CCFL backlight.

Features

- Long life LED backlight
- No controller chip is necessary
- Compatible with NTSC or PAL system (switchable)
- High Resolution: 112,320 dots
- Optimum viewing direction: 6 o'clock
- Up/Down and Left/Right image inversion
- RoHS compliant

Mechanical Characteristics

Item	Specification	Unit
Screen Size	3.5 inch diagonal	inch
Outline Dimensions	83.5 (W) x 63.1 (H) x 3.6 (D)	mm
Active Area	71.6 (W) x 52.65 (H)	mm
Surface Treatment	Anti-Glare	-
Pixel Number (RGB trio)	320 (W) x 234 (H)	-
Pixel Configuration	Delta	-
Dot Pitch	0.074 (W) x 0.225 (H)	mm
Weight	58 ± 5	g

Absolute Maximum Rating

Item			Symbol	Conditions	Absolute Maximum Rating		Unit
					Min.	Max.	
Supply Voltage	for Source Driver	Analog	V_{DD}	$T_a = 25^\circ\text{C}$	-0.3	+7.0	V
		Digital	V_{DD}		-0.3	+7.0	
	for Gate Driver	Positive	V_{GH}		-0.3	+45	
		Negative	V_{GL}		-23	+0.3	
			$V_{GH} - V_{GL}$		+15	+40	
Analog Input Voltage (means V_R, V_G, V_B)			V_{VIDEO}		-0.3	+7.3	V
Operating Temperature (define that contrast, response time, other display optical characters are $T_a=+25$)			Top	-	0	+60	$^\circ\text{C}$
Storage Temperature			Tstg	-	-20	+70	$^\circ\text{C}$

NOTES: GND = 0V, $T_a = 25^\circ\text{C}$

Product specifications contained herein may be changed without prior notice.

It is therefore advisable to contact Purdy Electronics before proceeding with the design of equipment incorporating this product.



Power Consumption (Ta =25°C)

Item	Symbol	Remarks	Specifications	Units
			Typ.	
LCD Panel Power Consumption	-	Power consumption for backlight is not included	33.5	mW
Backlight Lamp Power Consumption	-	calculated by $I_L \times V_L$.	0.65	W
Total Power Consumption	-	-	0.69	W

Backlight Connector

JST BHR-03VS-1

Recommended Operating Conditions

Item	Symbol	Remarks	Specifications			Unit
			Min.	Typ.	Max.	
Power Supply (Ta = 25°C)	V _{CC}		+4.5	+5.0	+5.5	V
	V _{DD}		+3.0	+3.3	+3.6	
	AV _{DD}		+4.5	+5.0	+5.5	
	V _{GH}		+14.5	+15.0	+15.5	
	V _{EE}		-15.5	-15.0	-14.5	
	V _{GL AC}	AC Component of V _{GL}	-	+6.0	-	V _{P-P}
	V _{GL DC}	DC Component of V _{GL}	-12.5	-11.0	-9.5	V
Video Signal (V _R , V _G , V _B)	V _{I AC}	AC Component, Note 2	-	+4.0	+4.2	V _{P-P}
	V _{I DC}	DC Component	-	+2.5	-	V
V _{COM}	V _{COM AC}	AC Component of V _{COMM}	-	+6.0	-	V _{P-P}
	V _{COM DC}	DC Component of V _{COMM}	+0.9	+1.0	+1.1	V
H Level	V _{IH}	Note 1	+0.7 V _{DD}	-	-	V
L Level	V _{IL}		-	-	+0.3 V _{DD}	

Note 1: STH1, STH2, CPH1, CPH2, CPH3, Q2H, INH, CPV, XOE, DIO1, DIO2

Note 2: Both NTSC & PAL system Video Signal input waveform is based on 8 steps gray scale.

Optical Specifications

Item	Symbol	Conditions	Specifications			Unit	
			Min.	Typ.	Max.		
Viewing Angle	Horizontal	θ	CR \geq 10	± 45	± 50	-	deg
	Vertical	θ (to 12 o'clock)		10	15	-	
		θ (to 6 o'clock)		30	35	-	
Contrast Ratio <u>Luminance when LCD is White</u> Luminance when LCD is Black	CR	At optimized viewing angle	110	150	-	-	
Response Time	Rise	Tr	$\theta = 0^\circ$	-	15	30	ms
	Fall	Tf	$\phi = 0^\circ$	-	25	50	
Transmission	Ratio	T	-	7.5	8.0	8.5	%
Uniformity	U	-	-	65	70	-	-
Brightness	LUM	-	-	200	250	-	cd/m ²
White Chromaticity	X	$\theta = 0^\circ$	-	0.280	0.310	0.340	-
	Y			0.310	0.340	0.370	
	Tc			6650	6850	7050	
Lamp Life Time	+25°C	-	decay to 75%	10,000	-	-	hr



Interface Pin Assignment

Pin No.	Symbol	Function	Input/Output	Remark
1	STH1	Start pulse for source driver	I/O	Note 1
2	AV _{SS}	Analog GND for source driver	I	–
3	AV _{DD}	Analog power input for source driver	I	Note 2
4	V _B	Video Input B	I	Note 4
5	V _G	Video Input G	I	Note 4
6	V _R	Video Input R	I	Note 4
7	V _{SS}	Digital GND	I	
8	V _{DD}	Digital power input	I	Note 3
9	CPH1	Sampling and shift clock for source driver	I	
10	CPH2	Sampling and shift clock for source driver	I	
11	CPH3	Sampling and shift clock for source driver	I	
12	STH2	Start pulse for source driver	I/O	Note 1
13	Q2H	Video input rotation control	I	
14	INH	Output enable for source driver	I	
15	R/L	Left/Right Control for source driver	I	
16	V _{COM}	Common electrode voltage	I	Note 4
17	V _{COM}	Common electrode voltage	I	Note 4
18	XOE	Output enable for gate driver	I	
19	CPV	Clock input for gate driver	I	
20	U/D	Up/Down Control for gate driver	I	
21	DIO2	Vertical start pulse	I/O	Note 5
22	DIO1	Vertical start pulse	I/O	Note 5
23	V _{GL}	Gate off voltage (alternative every 1-H)	I	Note 4
24	V _{EE}	Gate driver negative voltage	I	Note 6
25	V _{SS}	GND	I	
26	V _{CC}	Logic power for gate driver	I	Note 3
27	V _{GH}	Gate on voltage	I	Note 7
28	NC	No connection	–	

Note 1: STHL, STHR and R/L mode

R/L	STHL	STHR	Remarks
High (VDD)	Input	Output	Left to Right
Low (0 Volt.)	Output	Input	Right to Left

Note 2: AV_{DD} = +5V (Typ.)Note 3: V_{DD}, V_{CC} = +5V (Typ.)Note 4: V_{COM} = 6V_{PP}

Note 5: Dio1, DIO2 and U/D mode

U/D	DIO1	DIO2	Remarks
High (VDD)	Input	Output	Down to Up
Low (0 Volt.)	Output	Input	Up to Down

Note 6: V_{EE} = -15V (Typ.)Note 7: V_{GH} = -15V (Typ.)

Dimensional Outline

