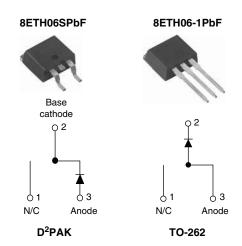




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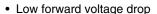
Hyperfast Rectifier, 8 A FRED PtTM



PRODUCT SUMMARY				
t _{rr} (typical)	18 ns			
I _{F(AV)}	8 A			
V _R	600 V			

FEATURES





· Low leakage current

• 175 °C operating junction temperature

• Lead (Pb)-free ("PbF" suffix)

• Designed and qualified for Q101 level



DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 144 °C	8	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	90	Α
Peak repetitive forward current	I _{FM}		16	
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-		
Famuurd valta aa	I _F = 8 A	-	2.0	2.4	V		
Forward voltage V _F		I _F = 8 A, T _J = 150 °C	-	1.3			1.8
Payaraa laakaga aurrant		$V_R = V_R$ rated	-	0.3	50		
Reverse leakage current I _R		T _J = 150 °C, V _R = V _R rated	-	55	500	μΑ	
Junction capacitance	C _T	V _R = 600 V	-	17	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -			-	nH	

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

8ETH06SPbF/8ETH06-1PbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A, } dI_F/dt = 100 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		-	18	22	
Payaraa raaayaru tima		$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	20	25	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs V _R = 390 V -	-	25	-	ns
		T _J = 125 °C		-	40	-	
D. d	I _{RRM}	T _J = 25 °C		-	2.4	-	A
Peak recovery current		T _J = 125 °C		-	4.8	-	
Davieres resolvent charge	0	T _J = 25 °C		-	25	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	120	-	nc nc
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 8 A	-	33	-	ns
Peak recovery current	I _{RRM}		$T_J = 125 ^{\circ}\text{C}$ $dI_F/dt = 600 A/\mu$	dI _F /dt = 600 A/μs	-	12	-
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	220	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	1.4	2	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Madding dayler		Case style D ² PAK	8ETH06S			
Marking device	ling device		8ETH06-1			

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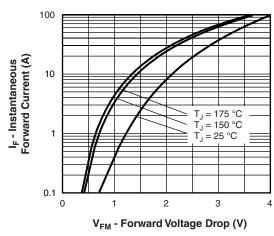


Fig. 1 - Maximum Forward Voltage Drop Characteristics

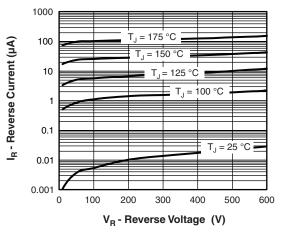


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

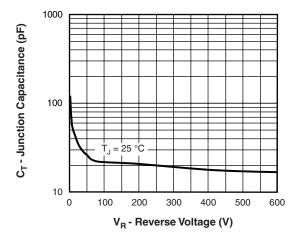


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

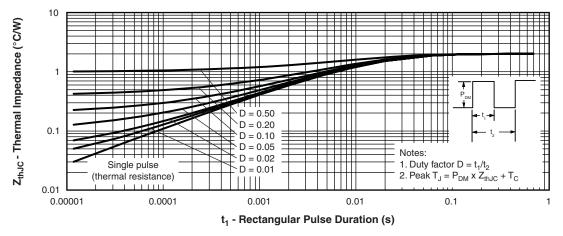


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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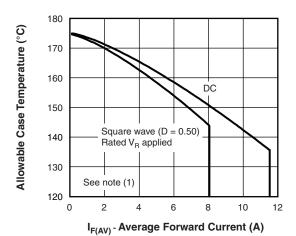
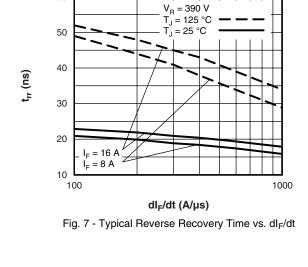


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current



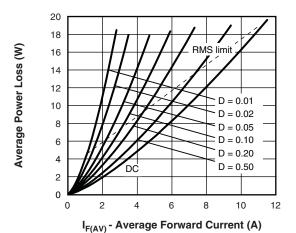


Fig. 6 - Forward Power Loss Characteristics

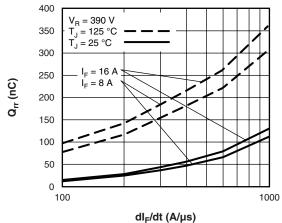


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ \text{at} \ V_{R1} = \text{Rated} \ V_R \\ \end{array}$



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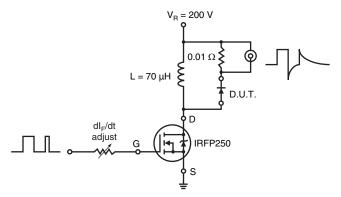
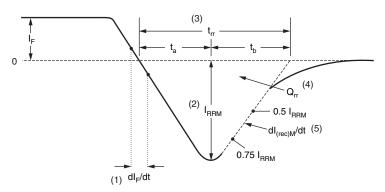


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

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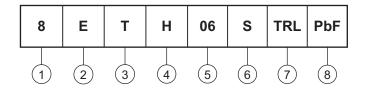
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (8 A)

2 - E = Single diode

3 - T = TO-220, D²PAK

4 - H = Hyperfast rectifier

5 - Voltage rating (06 = 600 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D²PAK package)

• TRR = Tape and reel (right oriented, for D²PAK package)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95014					
Part marking information http://www.vishay.com/doc?95008					
Packaging information http://www.vishay.com/doc?95032					



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