

100 BASE-T4 TRANSFORMERS

Designed for popular 100Base-T4 transceiver chips

Designed to provide isolation and impedance matching for 4 twisted pairs in a 100 Mbps Transmission over UTP-3 Cable.

IEEE 802.3 Compatible

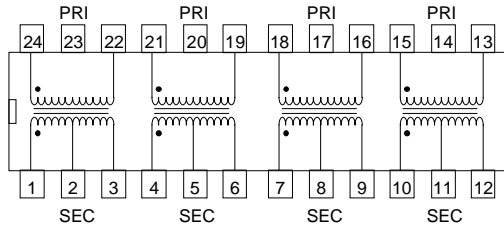
Common Mode Choke available for improved EMI performance

Electrical Specifications at 25° C

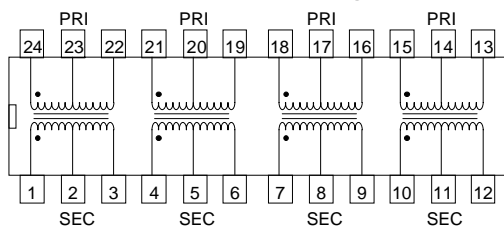
Designed for use with Transceiver I.C.	Quad SMD 24-Pin Part. No.	Dual SMD 16-Pin Part. No.	Turns Ratio $\pm 3\%$	OCL Min. (μH)	L_L max. (μH)	DCR _p max. (Ω)	C _{w/w} max. (pF)	Isolation (V _{RMS})
SEEQ 80C240P Cypress CY7C971	T-15202 T-15203	T-15200S T-15201S	1:2CT 2CT:1CT	350 350	1.0 1.0	0.5 1.0	14 10	1500 1500

Tape & Reel Available

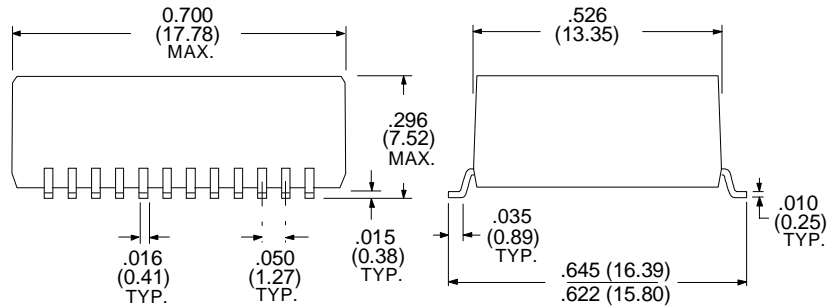
T-15202 Schematic Diagram



T-15203 Schematic Diagram

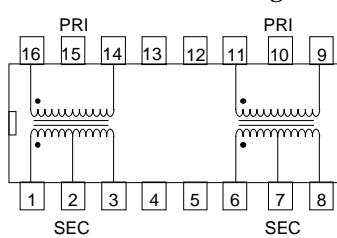


24-Pin SMD Package: T-15202, T-15203
Dimensions in Inches (mm)

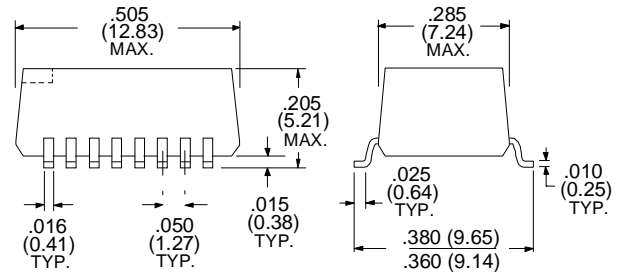
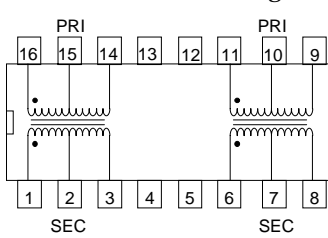


24-Pin DIP Package: T-15200S & T-15201S
Dimensions in Inches (mm)

T-15200S Schematic Diagram



T-15201S Schematic Diagram



100 BASE-T4 COMMON MODE CHOKE

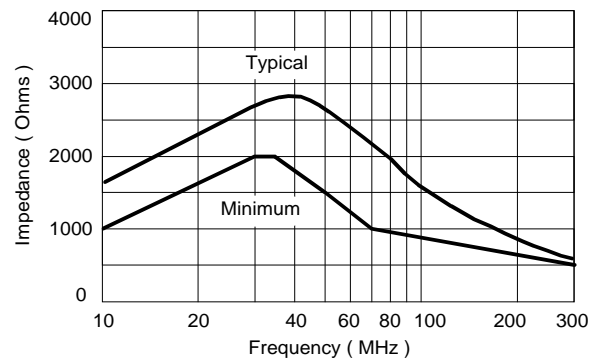
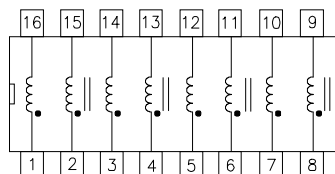
Impedance Ω (Min.)
2000 @ 30 Mhz
1500 @ 50 Mhz
1000 @ 70 Mhz

Electrical Specifications at 25° C

Part Number	Turns Ratio $\pm 3\%$	OCL Min. (μH)	L_L max. (μH)	DCR _p max. (Ω)	C _{w/w} max. (pF)	Isolation (V _{RMS})
F-1515	1 : 1	36	0.2	0.35	15	500

F-1515: 16-Pin SMD package per T-15200S above.

F-1515 Schematic Diagram



For complete details including downloadable PDF catalogs and data sheets please visit www.rhombus-ind.com