

Silicon PNP Power Transistors

2SA887

DESCRIPTION

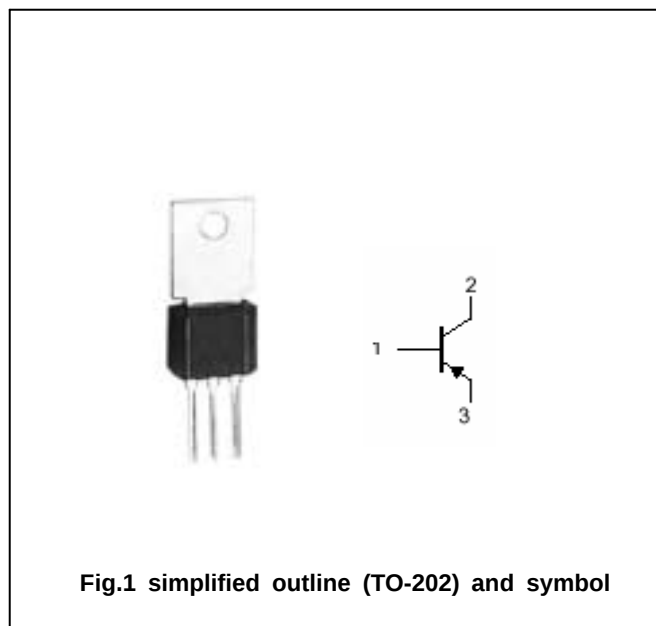
- With TO-202 package
- Complement to type 2SC1848

APPLICATIONS

- Medium power amplifier

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

Absolute maximum ratings ($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-70	V
V_{CEO}	Collector-emitter voltage	Open base	-50	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-2	A
I_{CM}	Collector current-peak		-3	A
P_C	Collector power dissipation	$T_a=25$	1.2	W
		$T_C=25$	10	
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-10mA; I _B =0	-50			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA; I _E =0	-70			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-1A ; I _B =-0.1A		-0.6	-1.2	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-2A ; I _B =-0.2 A		-1.0	-1.5	V
h _{FE-1}	DC current gain	I _C =-100mA ; V _{CE} =-5V	30			
h _{FE-2}	DC current gain	I _C =-1A ; V _{CE} =-5V	50		220	
I _{CBO}	Collector cut-off current	V _{CB} =-40V; I _E =0			-1.0	μ A
I _{CEO}	Collector cut-off current	V _{CE} =-20V; I _B =0			-100	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-10	μ A
f _T	Transition frequency	I _E =0.5A ; V _{CB} =-5V		150		MHz

◆ h_{FE-2} Classifications

P	Q	R
50-100	80-160	120-220

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PACKAGE OUTLINE

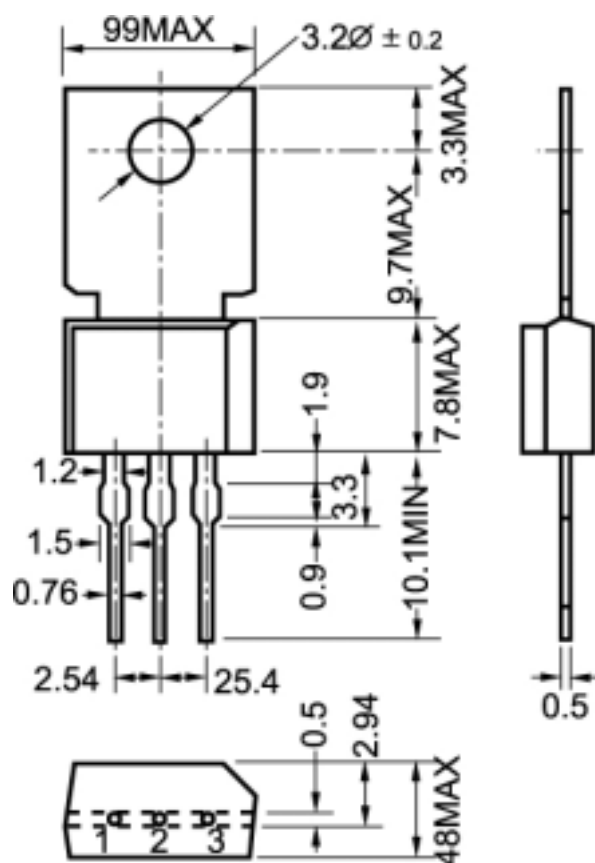


Fig.2 outline dimensions