## International IER Rectifier

## EMI FILTER HYBRID-HIGH RELIABILITY

## Description

The AME Series of EMI filters have been designed to provide full compliance with the input line reflected ripple current requirement specified by CE03 of MIL-STD-461C over the full military temperature range while operating in conjunction with the corresponding AFL series of DC-DC converters. These filters are offered as part of a complete family of conversion products providing single and dual output voltages while operating from nominal $+28,+50$ or +270 input line voltage. Other converters operating with a similar switching frequency will also benefit by use of this device.

These EMI filters are hermetically packaged in two enclosure variations, utilizing copper-core pins to minimize resistive DC losses. Three lead styles are available, each fabricated with International Rectifiers's rugged ceramic lead-to-package seal assuring long term hermetic seal integrity in harsh environments.

Manufactured in a facility fully qualified to MIL-PRF38534, these converters are available in four screening grades to satisfy a wide range of applications. The CH grade is fully compliant to the requirements of MIL-PRF-38534 for class H. The HB grade is fully processed and screened to the class H requirement, but does not include element evaluation to the class H requirement.

AME50461 SERIES


## Features

■ Up to 7.0 Ampere Output Current
■ Attenuation > 35dB @ 200 KHz
■ Low Profile ( 0.38 ") Seam Welded Package
■ Ceramic Feedthru Copper-Core Pins

- Operation Over Full Military Temp. Range
- Standard Military Drawings Available

Both grades are tested to meet the complete group "A" test specification over the full military temperature range with no derating. Two grades with more limited screening are also available for use in less demanding applications. Variations in electrical, mechanical and screen requirements can be accommodated. Contact IR San Jose for special requirements.

## Typical Connection Diagram



AME50461 Series

## Specifications

| ABSOLUTE MAXIMUM RATINGS Note 1 |  |
| :--- | :--- |
| Input Voltage | -300 V to +300 V Note 2 |
| Input Current | 7.0 A |
| Lead Soldering Temperature | $300^{\circ} \mathrm{C}$ for 10 seconds |
| Case Temperature | Operating $\quad-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
|  | Storage $\quad-65^{\circ} \mathrm{C}$ to $+135^{\circ} \mathrm{C}$ |

Electrical Characteristics $-55^{\circ} \mathrm{C} \leq \mathrm{T}_{\text {CASE }} \leq+125^{\circ} \mathrm{C},-100 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq+100 \mathrm{~V}$ unless otherwise specified

| Parameter | Group A Subgroups | Test Conditions | Min | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Leakage Current Note 3 | 1, 2, 3 | $\pm 250 \mathrm{~V}$ DC Input Voltage | 0 | 50 | $\mu \mathrm{A}$ |
| DC Resistance Note 4 | 1, 2, 3 | $\begin{aligned} & \mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{C}}=125^{\circ} \mathrm{C} \end{aligned}$ |  | $\begin{gathered} 60 \\ 100 \end{gathered}$ | $\mathrm{m} \Omega$ |
| Noise Reduction | 4, 5, 6 | $\begin{aligned} & 200 \mathrm{KHz}-500 \mathrm{KHz} \\ & 500 \mathrm{KHz}-1 \mathrm{MHz} \\ & 1 \mathrm{MHz}-10 \mathrm{MHz} \end{aligned}$ | $\begin{aligned} & 35 \\ & 60 \\ & 65 \end{aligned}$ |  | dB |
| Isolation | 1 | Any Pin to Case, Tested @ 500VDC | 100 |  | $\mathrm{M} \Omega$ |
| Capacitance | $\begin{gathered} 1 \\ 2,3 \end{gathered}$ | Measured Between Any Pin and Case | $\begin{aligned} & 34 \\ & 30 \end{aligned}$ | $\begin{aligned} & 56 \\ & 62 \end{aligned}$ | nF |

## Notes to Specifications

1. Operation above maximum ratings may cause permanent damage to the device. Operation at maximum ratings may degrade performance and affect reliability
2. Device can tolerate $\pm 300$ Volt transient whose duration is $\leq 100 \mathrm{~ms}$ when $\mathrm{R}_{\mathrm{S}} \geq 0.5 \Omega$.
3. Derate Output Current linearly from $100 \%$ at $125^{\circ} \mathrm{C}$ to 0 at $135^{\circ} \mathrm{C}$.
4. DC resistance is the total resistance of the device and includes the sum of the input to output resistance and the return in to return out resistance paths.

## Block Diagram



Refer to last page for Pin Designation

## Circuit Operation and Application Information

The AME series of filters employ three stages of filtering in a low pass configuration designed to attenuate the higher frequency components of ripple currents generated by high frequency switching DCDC converters. The Block Diagram describes the general arrangement of the principal elements which have been connected to provide both differential and normal mode buffering between the input and output terminals.

Employing only passive elements, AME filter operation is initiated simply by insertion into the input power path between one or more DC-DC converters and their input DC voltage bus. In this connection, output pins of the filter will be connected to input pins of the converters.

When a single AME filter is used in conjunction with multiple DC-DC converters, the use will be limited to the maximum output current capability specified in the AME electrical table. ${ }^{1}$ A typical connection utilizing one filter to drive two converters is illustrated on page1.

Although expressly designed to complement the AFL series of DC-DC converters, the AME50461 filters can be successfully operated in conjunction with other converters in the Advanced Analog line including the ASA, AHF, AHV and ATR series.

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## Available Screening Levels and Process Variations

| Requirement | MIL-STD-883 <br> Method | No <br> Suffix | ES <br> Suffix | HB <br> Suffix | CH <br> Suffix |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Temperature Range |  | -20 to $+85^{\circ} \mathrm{C}$ | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Element Evaluation |  |  |  |  | MIL-PRF- 38534 |
| Internal Visual | 2017 | $*$ | Yes | Yes | Yes |
| Temperature Cycle | 1010 |  | Cond B | Cond C | Cond C |
| Constant Acceleration | 2001 |  | 500 g | Cond A | Cond A |
| Burn-in | 1015 | 48 hrs@ $85^{\circ} \mathrm{C}$ | $48 \mathrm{hrs@125}^{\circ} \mathrm{C}$ | $160 \mathrm{hrs} @ 125^{\circ} \mathrm{C}$ | $160 \mathrm{hrs} @ 125^{\circ} \mathrm{C}$ |
| Final Electrical <br> (Group A) | MIL-PRF-38534 <br> \& Specification | $25^{\circ} \mathrm{C}$ | $25^{\circ} \mathrm{C}$ | $-55,+25,+125^{\circ} \mathrm{C}$ | $-55,+25,+125^{\circ} \mathrm{C}$ |
| Seal, Fine \& Gross | 1014 | Cond A | Cond A, C | Cond A, C | Cond A, C |
| External Visual | 2009 | $*$ | Yes | Yes | Yes |

* Per Commercial Standards

Pin Designation

| Pin No. | Designation |
| :---: | :--- |
| 1 | Positive Input |
| 2 | Positive Input |
| 3 | Positive Input |
| 4 | Input Return |
| 5 | Input Return |
| 6 | Input Return |
| 7 | Output Return |
| 8 | Output Return |
| 9 | Output Return |
| 10 | Positive Output |
| 11 | Positive Output |
| 12 | Positive Output |

## Part Numbering



## International

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[^0]:    1 To calculate the input current ( $i_{i n}$ ) requirement of any one converter, first determine the maximum output power by multiplying output voltage by maximum load current, divide this power by the efficiency to obtain input power and then divide input power by input voltage to obtain the input current ( $i_{i n}$ ). Note that to obtain worst case input current, you must use maximum load current, minimum efficiency and minimum line voltage in this calculation.

