

MTD2038G

DMOS Microstepping PWM Motor Driver

Features

Dual full bridge for a bipolar stepper motor driver
Load supply voltage 40V, Output current 1.0A
Constant current control (Fixed frequency PWM control)
2-bit selectable current level (Full step/Half step/Quarter step)
Stand-by function
Thermal shutdown with hysteresis
Under voltage lock out function
Surface mount package with heat sink(HSOP24)

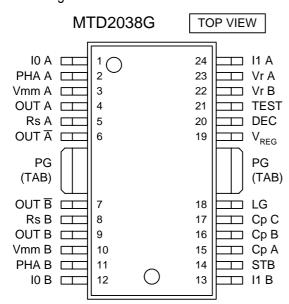


Absolute maxmum ratings / Ta=25

Parameter	Symbol	Rating	Unit
V _{REG} Output current	I _{REG}	12	mA
Logic input	V_{LOGIC}	0~6	V
Reference input	Vr	0~7	V
Load supply	Vmm	40	V
Output current	I _{OUT}	1.0	А
Power dissipation	P _D *1	2.1	W
Storage temperature range	Tstg	-40 ~ 150	
Maximum Junction temperature	Tj	150	

^{*1 :} $50.8 \times 50.8 \times 1 \text{mm}^3$ Glass Epoxy Board(FR4),200mm² Cupper Pattern

Pin Assignment



Electrical Characteristics

Vmm=24V,Ta=25 unless otherwise specified

Parameter	Symbol	Condition	MIN	TYP	MAX	Unit
Output stage	Cyllibol	Condition	IVIIIV	'''	INIVA	Oill
Load supply current (All circuit OFF)	Imm(OFF)	V _{I0A} =V _{I1A} =V _{I0B} =V _{I1B} =5V	-	13	22	mA
Load supply current (All circuit ON)	Imm(ON)		-	13	22	mA
Load supply current (Stand-by)	Imm(STB)	V _{I0A} =V _{I1A} =V _{I0B} =V _{I1B} =0V	-	-	200	μA
Upper MOSFET ON resistance		V _{STB} =5V lout=-0.8A	-	1.8	2.0	μΑ
Lower MOSFET ON resistance	R _{ON} H	lout=0.8A	-	0.8	1.0	
	R _{ON} L			0.6		^
Upper MOSFET leakage current	IrH	Vmm=35V, V _{OUT} =0V	-	-	100	μA
Lower MOSFET leakage current	IrL	V _{OUT} =35V, V _{RS} =0V	-	-	100	μA
Upper MOSFET reverse voltage	V _F H	I _F =0.8A	-	1.4	1.6	V
Lower MOSFET reverse voltage	V _F L	I _F =0.8A	-	1.1	1.3	V
VcpA under voltage lock out threshold	VcpAUVLO	-	Vmm+3	Vmm+4	Vmm+6	V
Logic stage	1					
V _{REG} output voltage	V	I _{REG} =500 μ A	4.85	5.00	5.15	V
V _{REG} odiput Voltage	V_{REG}	I _{REG} =12mA	4.75	-	5.25	V
PHA/I0/I1/DEC "H" input voltage	$V_{LOGIC}H$	-	2.0	-	V_{REG}	V
PHA/I0/I1/DEC "L" input voltage	V _{LOGIC} L	-	GND	-	0.8	V
STB "H" input voltage	V _{STB} H	-	2.3	-	V_{REG}	V
STB "L" input voltage	V _{STB} L	-	GND	-	0.8	V
PHA/I0/I1 "H" input current	I _{PHA/I0/I1} H	V _{PHA/I0/I1} =5V	-	-	10	μA
PHA/I0/I1 "L" input current	I _{PHA/I0/I1} L	V _{PHA/I0/I1} =0V	-	-20	-50	μΑ
STB "H" input current	I _{STB} H	V _{STB} =5V	-	45	90	μA
STB "L" input current	I _{STB} L	V _{STB} =0V	-	-	-10	μA
DEC "H" input current	I _{DEC} H	V _{DEC} =5V	-	75	150	μA
DEC "L" input current	I _{DEC} L	V _{DEC} =0V	-	-	-10	μA
Vr "H" input current	IrefH	Vr=4V	-	1	10	μΑ
Vr "L" input current	IrefL	Vr=0V	-	-1	-10	<u>μ</u> Α
Comparator threshold voltage (100%)	Vs1	V _{I0(AorB)} ="L", V _{I1(AorB)} ="L"	95	100	105	%
Comparator threshold voltage (70%)	Vs2	V _{I0(AorB)} ="H", V _{I1(AorB)} ="L"	64	70	76	%
Comparator threshold voltage (40%)	Vs3	V _{I0(AorB)} ="L", V _{I1(AorB)} ="H"	36	40	44	%
Oscillator frequency	f _{osc}	- (AOIB) , IJ(AOIB)	20	30	40	kHz
Comparator blanking time	tb	-	-	0.65	-	μs
CpA charging time *1	Tchg	Cp1=0.22 μ F,Cp2=0.01 μ F,VSTB=5V 0V	_	-	1.5	ms
Thermal shutdown temperature *2		-	150	170	190	
morniai shataown temperature Z	T _{TSD}		100	1,,,	100	

^{*1:} Charge of a charge pump takes time. Therefore, please take the time more than Tchg from aftre Vcc input or Stand-by release to even before a motor drive start.

*2: T_{TSD} is design value.

Thermal resistance

Symbol	Rating	Unit
ja *3	58	/W

^{*3: 50.8} x 50.8 x 1mm3 Glass Epoxy Board(FR4),200mm2 Cupper Pattern



Recommended operation conditions

Parameter	Symbol	Recommendation	Unit
Junction temperature	Tj	-25 ~ 120	
Load supply	Vmm	15 ~ 35	V
Reference voltage	Vr	0~6	V

Truth table

I0 A(B) and I1 A(B)	PHA A or B	OUT A or B	OUT \overline{A} or \overline{B}
L	Н	Н	L
L	L	L	Н
Н	×	OFF	OFF

x:don't care

I0 A(B)	I1 A(B)	Current Level(%)		
L	L	100		
Н	L	70		
L	Н	40		
Н	Н	0		

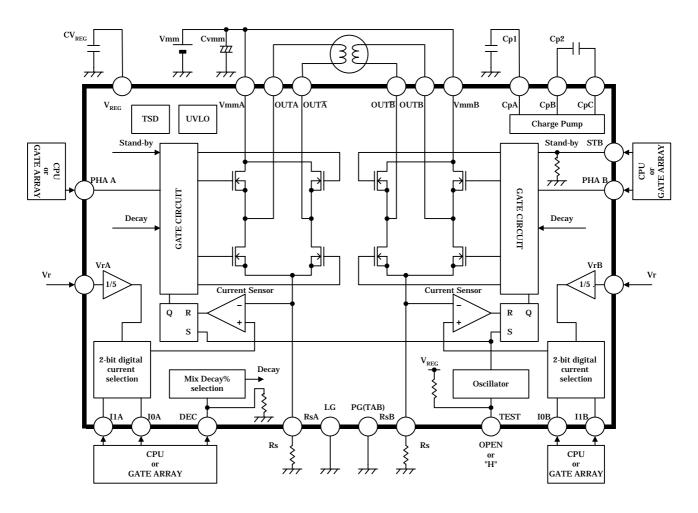
STB *1	Mode
L or OPEN	ACTIVE
Н	Stand-By

DEC *1	Current Decay Mode	
L or OPEN	Slow Decay	
Н	Mix Decay(37.5% Fast)	

^{*1} built-in pull-down resistance



Block diagram / Typical application



Recommended component values

Symbol	Recommended component values	Unit
Cp1	0.22	μF
Cp2	0.01	μF
CV _{REG}	0.1	μF
Rs	0.68	
Cvmm *1	47	μF

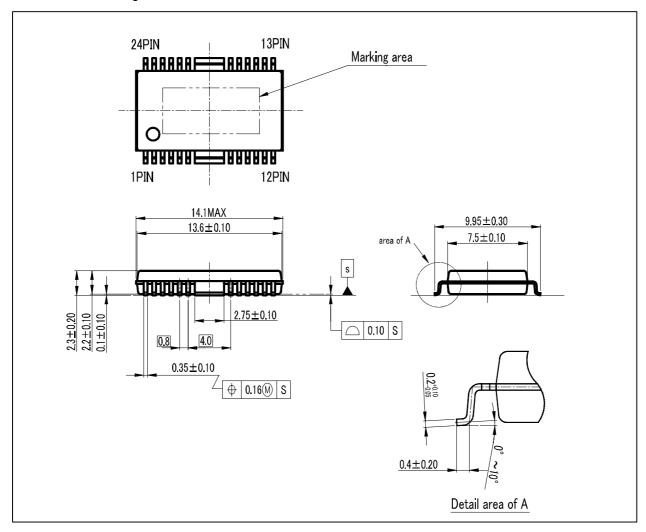
^{*1:} It recommend the electrolytic capacitor for the noise absorption connect near IC to Load supply.

Constant chopping current level

$$Ichop = \frac{Vr}{5 \times Rs}$$
Output current



Outline Drawing



(Unit: mm)



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