MCM LNA

LCL3212-L / LCL3512-L LCL3712-L



Product Features

- Multichip Hybrid Module
- GaAs p-HEMT & MESFET chip on board
- No matching circuit needed
- High IP3 & Low Noise
- Single Supply Voltage (+5V)
- Surface Mount Hybrid Type
- Tape & Reel Packaging
- · Small Size, High Heatsink
- · Alumina Substrate
- Pb Free / RoHS Standard

Applications

- 2G & 3G Repeater
- Base Station
- PCS, CDMA, W-CDMA
- GSM, DCS, UMTS
- · WiMAX, Wibro, WLAN
- RF Sub-Systems



Package: CP-16B

Descriptions

RFHIC's LOW Noise Amplifier series are all hybrid LNA type products which includes all matching for the convenience of customers. LCL series are focused on higher OIP3 while maintaining low noise. The structure of the device is built with GaAs p-HEMT die attached on a ceramic thick film substrate. The device is still smaller than the area one would use for the application notes all together. Depending on the part number, one can use this in different frequency applications. All LNA hybrids are possible to have custom frequency & spec without any additional NRE cost involved.

All RFHIC products are RoHS compliant.

Electrical Specifications

Parameter	Units	LCL3212-L	LCL3512-L	LCL3712-L
Frequency Range	MHz	2900~3400	3400~3600	3600~3800
Small Signal Gain (S ₂₁)	dB	20.5	19	18.5
Gain Flatness	dB	±1.0	±1.0	±1.0
Input Return Loss (S ₁₁)	dB	-15	-15	-15
Output Return Loss (S ₂₂)	dB	-10	-10	-10
1dB Compression Point (P ₁ dB)	dBm	21	21	21
Output 3 rd Order Intercept Point (OIP3) (TYP.)	dBm	42	42	42
Noise Figure (TYP.)	dB	1.1	1.2	1.3
DC Supply Current (Vdc=+5V)	mA	180~240	180~240	180~240

Test Condition

- ① Supply voltage = +5V, 50ohm System, Ta = 25 $^{\circ}$ C
- ② OIP3 is measured with two tones, at an output power of +10dBm/tone separated by 1MHz.

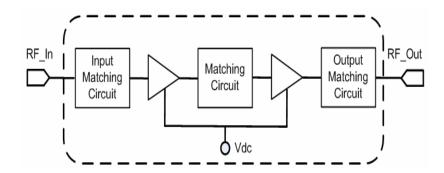


Absolute Maximum Ratings*

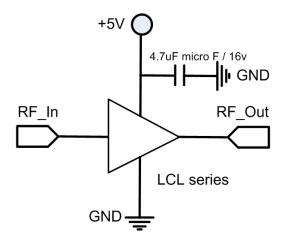
PARAMETER	Unit	Rating	Remark
Supply Voltage	V	+7	
Operating Temperature	${\mathbb C}$	-40 to +85	
Storage Temperature	°C	-50 to +125	

^{*} Operation of this device in excess of any one of these parameters may cause permanent damage.

Functional Diagram

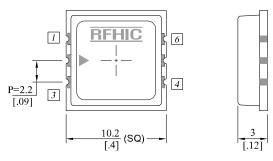


Application Circuit

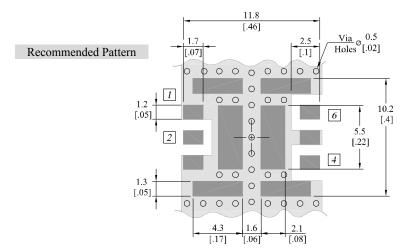




Package Dimensions (Type: CP-16B)



Unit: mm [inch]	Tolerance: $\pm \frac{0.2}{.008}$	
Pin No.	Function	
1, 3, 5	Ground	
2	Input	
4	Vcc	
6	Output	



ESD Protection

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices. Some of the precautions recommended are;

- Person at a workbench should be earthed via a wrist strap and a resistor.
- All mains-powered equipment should be connected to the mains via an earth-leakage switch.
- Equipment cases should be grounded.
- Relative humidity should be maintained between 40% and 50%.
- An ionizer is recommended.
- Keep static materials, such as plastic envelopes and plastic trays etc. away from the workbench.

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