



UNISONIC TECHNOLOGIES CO.,LTD.

RXXLD10

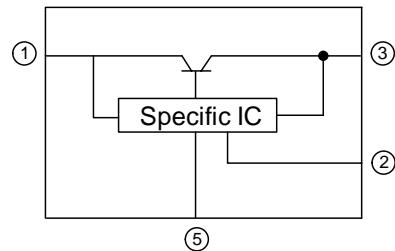
LINEAR INTEGRATED CIRCUIT

**LOW VOLTAGE OPERATION
LOW POWER-LOSS VOLTAGE
REGULATORS**

■ FEATURES

- * Low voltage operation (Minimum operating voltage: 2.35V) 2.5V input available 1.5 ~ 1.8V
- * Low dissipation current
- * Built-in overcurrent protection and over temperature protection functions

■ EQUIVALENT



*Pb-free plating product number: RXXLD10L

■ PIN DESCRIPTION

PIN NO.	PIN NAME
1	INPUT
2	ON/OFF
3	OUTPUT
4	NC
5	GND

■ ORDERING INFORMATION

Order Number		Package	Packing
Normal	Lead Free Plating		
RXXLD10-TN5-R	RXXLD10L-TN5-R	TO-252-5	Tape Reel

Note: xx: Output Voltage, refer to Marking Information.

■ MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
TO-252-5	15:1.5V 18:1.8V 25:2.5V 03:3.0V 33:3.3V	<p>VOLTAGE CODE Space :Pb/Sn L:Pb-free DATE CODE</p>

■ ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V_{IN}	10	V
ON/OFF Control Terminal Voltage (Note 2)	V_C	10	V
Output Current	I_{OUT}	1.0	A
Power Dissipation (with infinite heat sink)	P_D	8	W
Junction Temperature	T_J	125	$^\circ\text{C}$
Operating Temperature	T_{OPR}	-20 ~ +85	$^\circ\text{C}$
Storage Temperature	T_{STG}	-40 ~ +150	$^\circ\text{C}$

Note 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2: All are open except GND and applicable terminals.

3. The device is guaranteed to meet performance specification within $0^\circ\text{C} \sim 70^\circ\text{C}$ operating temperature range and assured by design from $-20^\circ\text{C} \sim 85^\circ\text{C}$.

■ ELECTRICAL CHARACTERISTICS

($V_{IN} = V_{O(TYP.)} + 1\text{V}$, $I_{OUT} = 0.5\text{A}$, $V_C = 2.7\text{V}$, $T_a = 25^\circ\text{C}$, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	R15LD10	V_{IN}	2.35	10	10	V
	R18LD10		2.35	10	10	
	R25LD10		$V_{OUT} + 0.5$	10	10	
	R03LD10		$V_{OUT} + 0.5$	10	10	
	R33LD10		$V_{OUT} + 0.5$	10	10	
	R15LD10		1.45	1.5	1.55	
Output Voltage	R18LD10	V_{OUT}	1.75	1.8	1.85	V
	R25LD10		2.438	2.5	2.562	
	R03LD10		2.925	3	3.075	
	R33LD10		3.218	3.3	3.382	
	ON	$V_C(ON)$	2			V
Voltage for Control (Note 1)	OFF	$V_C(OFF)$			0.8	V
	ON	$I_C(ON)$			200	μA
Current for Control	OFF	$I_C(OFF)$	$V_C = 0.4\text{V}$		2	μA
	Quiescent Current	I_Q	$I_{OUT} = 0\text{A}$		1	mA
Output Off-state Dissipation Current		I_{QS}	$I_{OUT} = 0\text{A}$, $V_C = 0.4\text{V}$		5	μA
Load Regulation		ΔV_{OUT}	$I_{OUT} = 5\text{mA} \sim 1\text{A}$		0.2	2 %
Line Regulation		ΔV_{OUT}	$V_{IN} = V_{O(TYP.)} + 1\text{V} \sim V_{O(TYP.)} + 6\text{V}$ $I_{OUT} = 5\text{mA}$		0.1	1 %
Dropout Voltage (Note 2)		V_D	$I_{OUT} = 0.5\text{A}$		0.2	0.5 V
Temperature Coefficient of Output Voltage		$T_C V_o$	$T_J = 0 \sim 125^\circ\text{C}$, $I_{OUT} = 5\text{mA}$		± 0.01	$^\circ\text{C}$
Ripple Rejection		RR	Refer to Fig.2	45	60	dB

Note 1: In case of opening control terminal pin 2, output voltage turns off.

2: Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

RXXLD10

LINEAR INTEGRATED CIRCUIT

■ TEST CIRCUIT

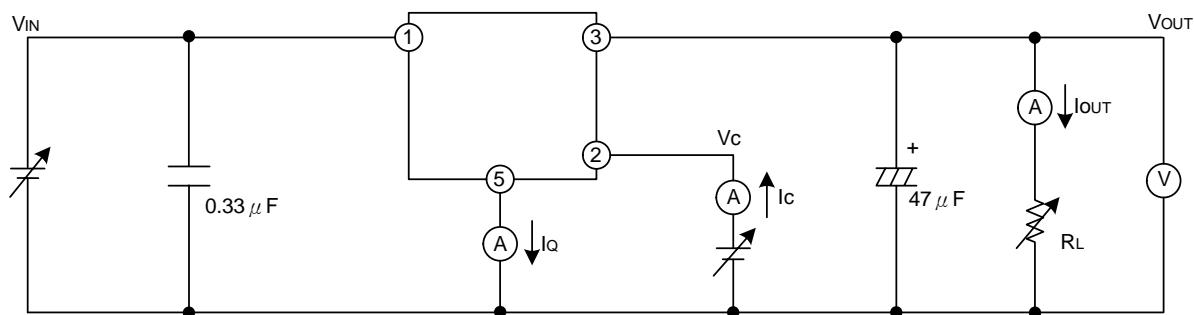
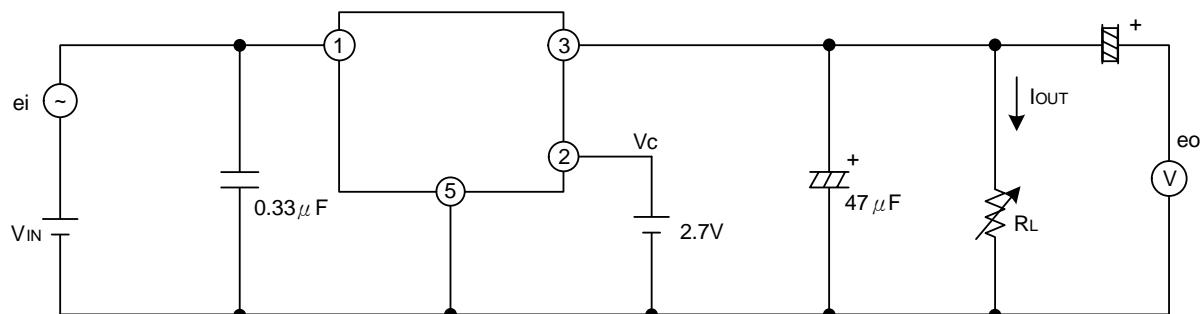


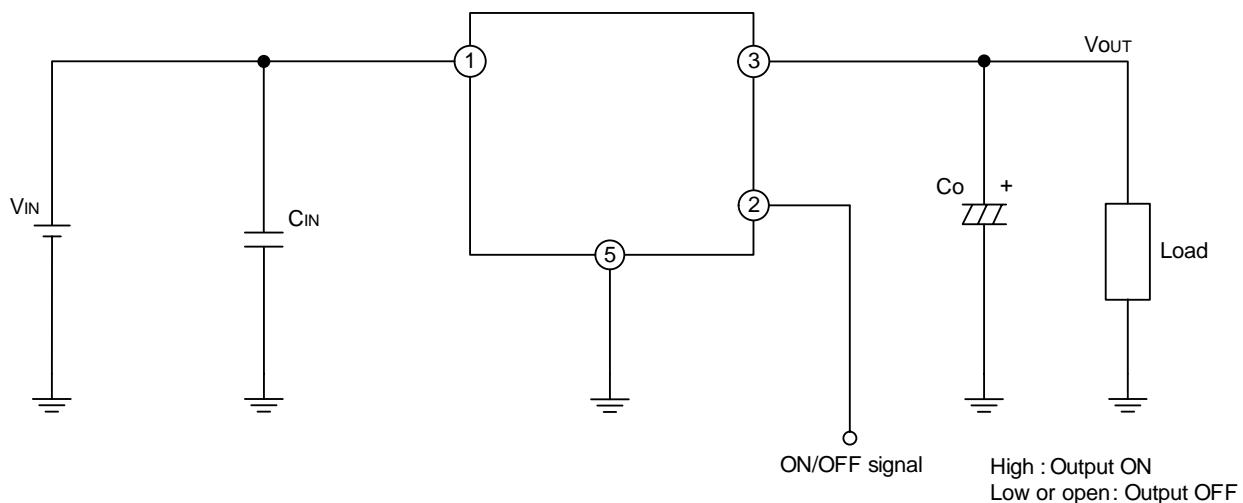
Fig. 1



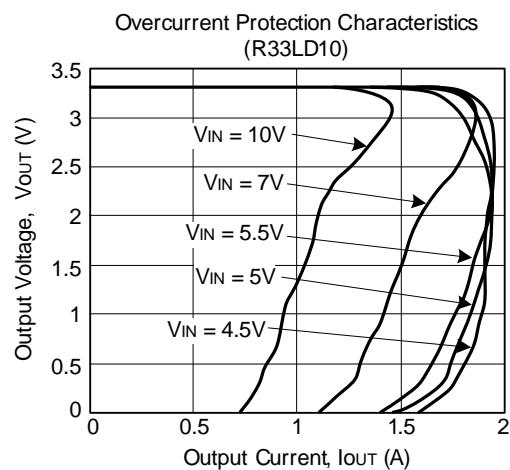
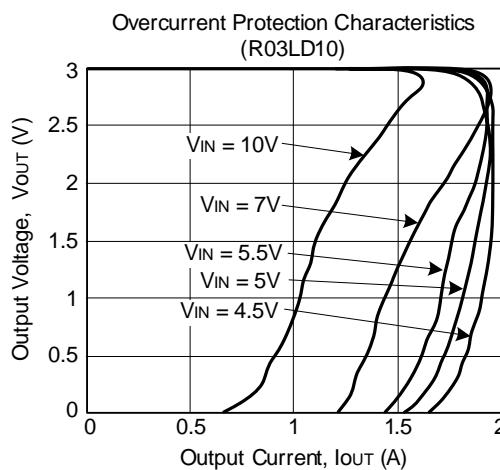
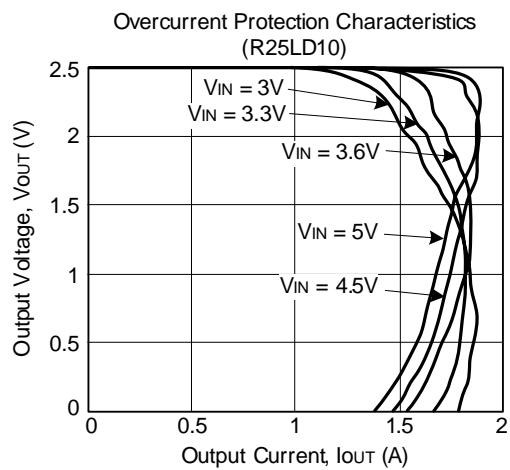
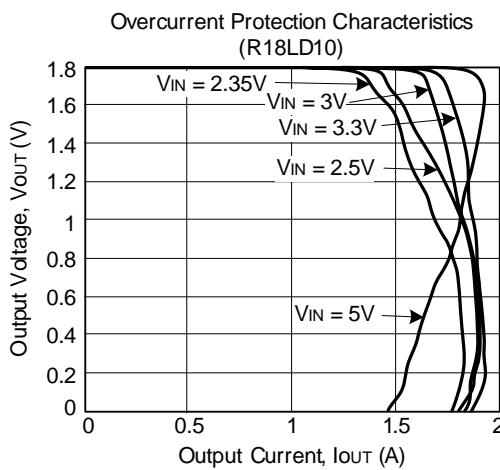
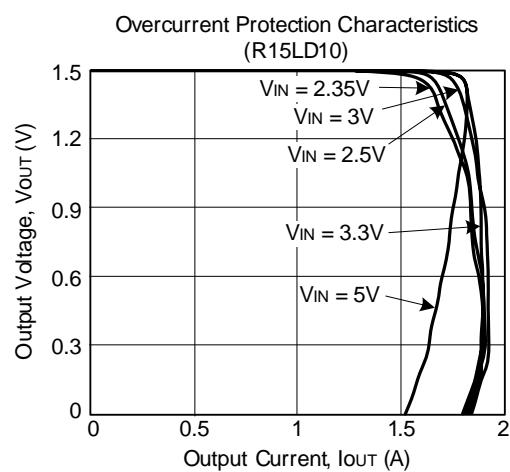
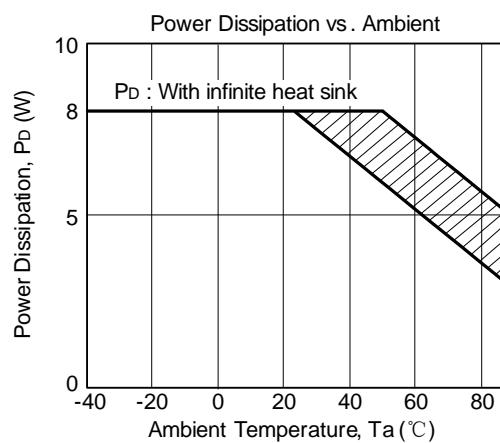
f = 120Hz (sine wave), ei (rms) = 0.5V, VIN = VO (TYP)+2V, IOUT = 0.5A, RR = 20log (ei (rms) /eo (rms))

Fig.2 For Ripple Rejection

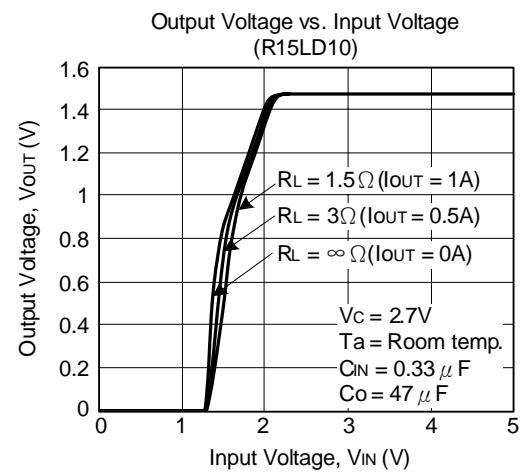
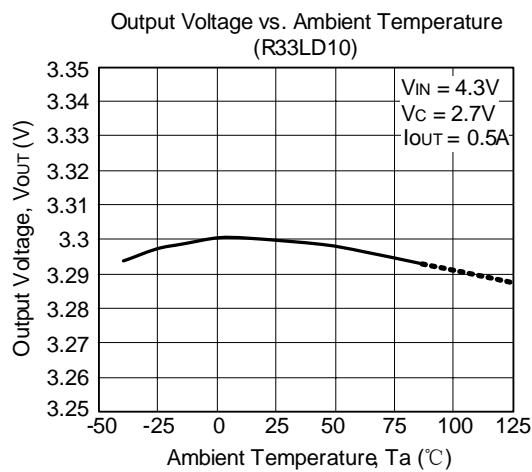
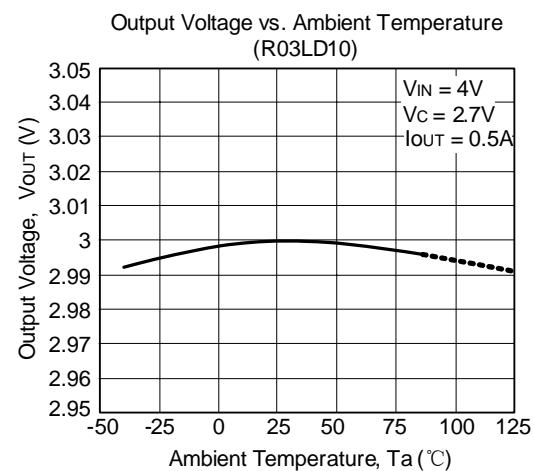
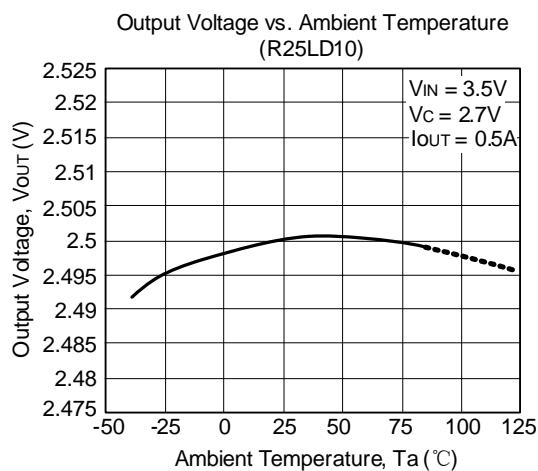
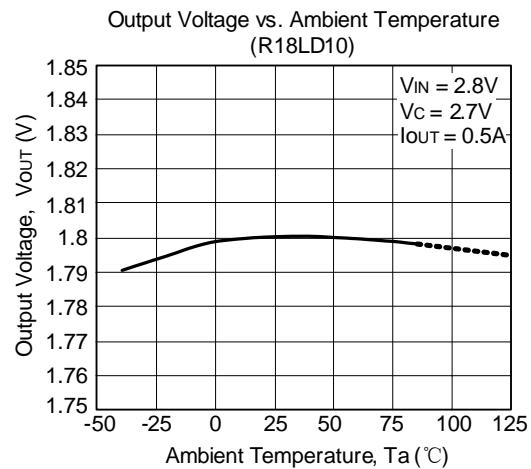
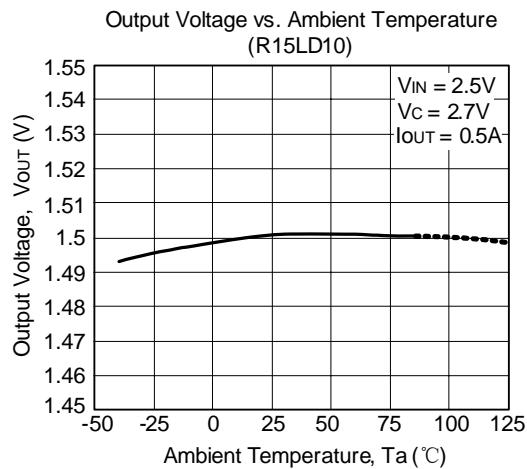
■ TYPICAL APPLICATION CIRCUIT



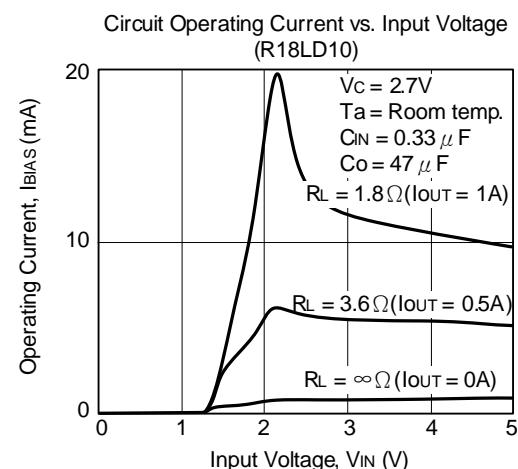
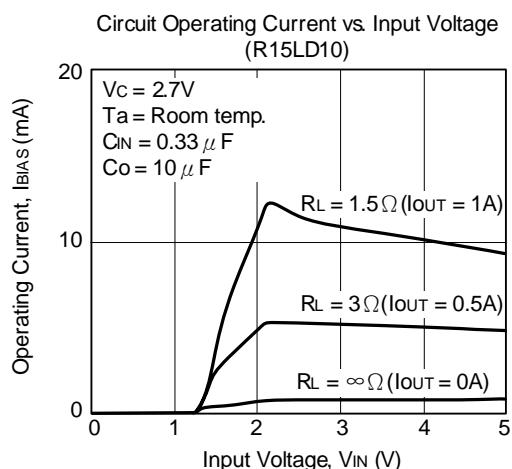
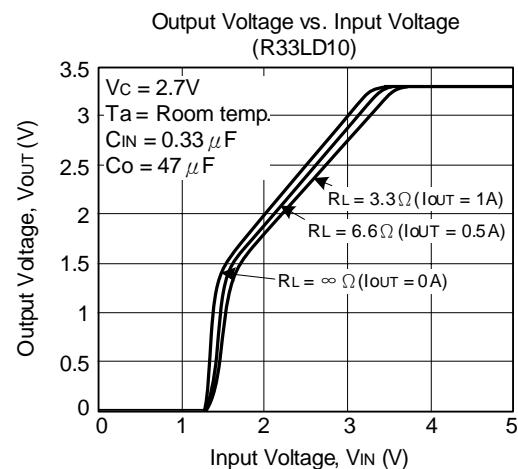
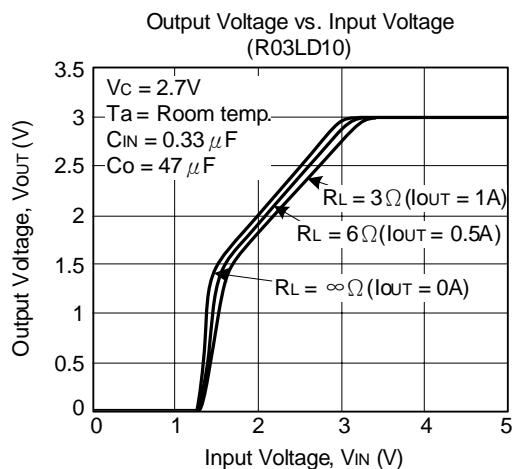
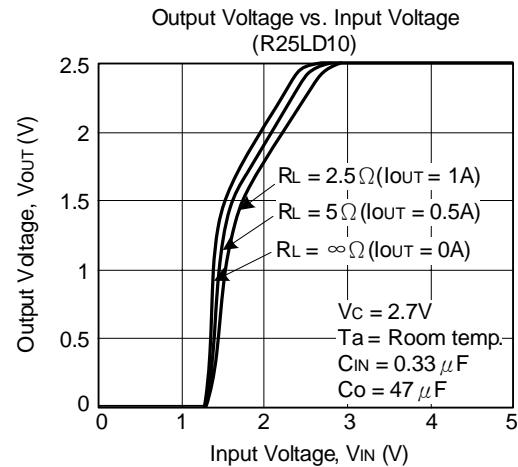
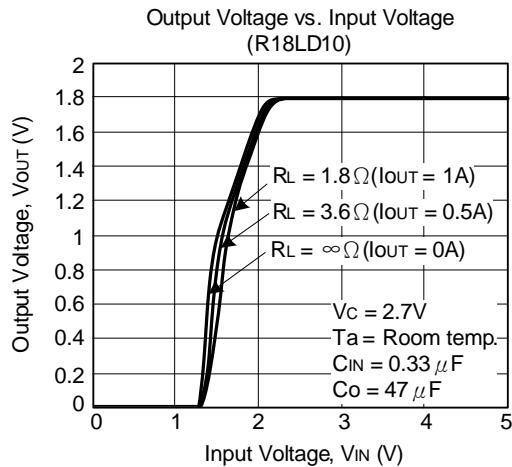
■ TYPICAL CHARACTERISTICS



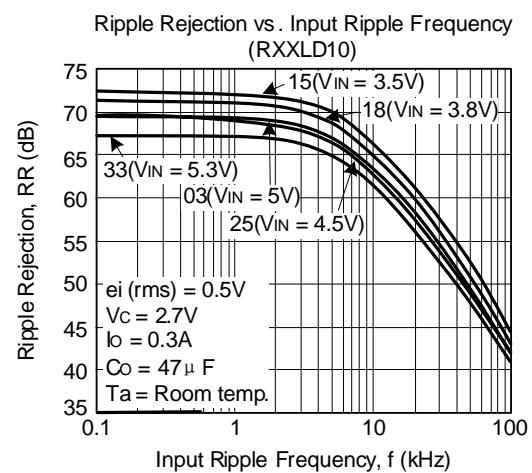
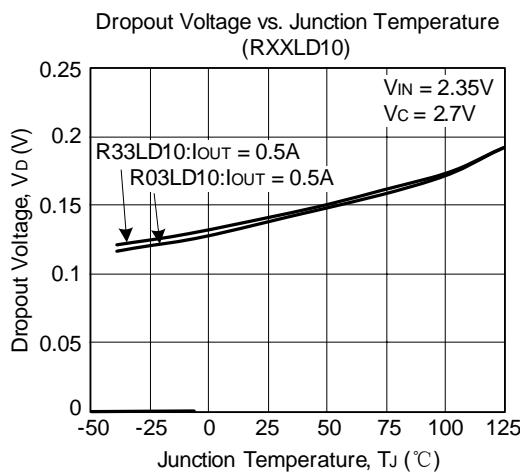
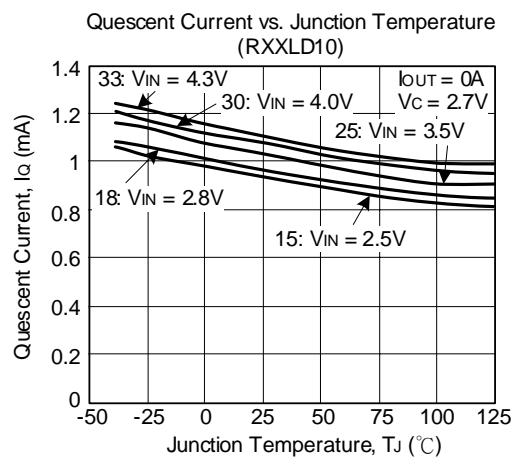
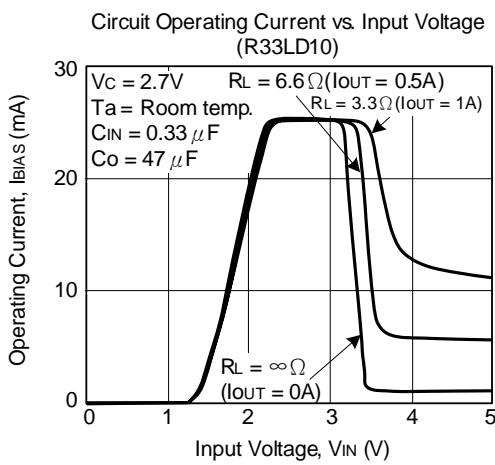
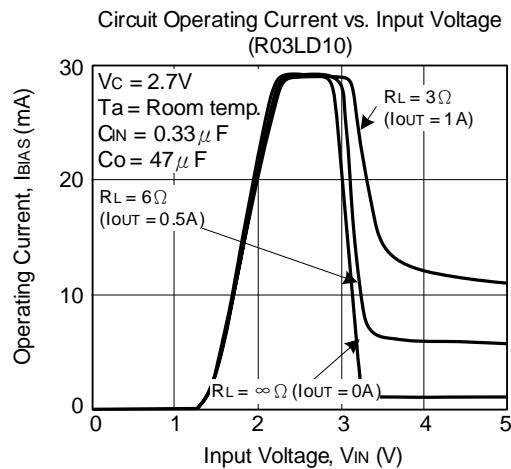
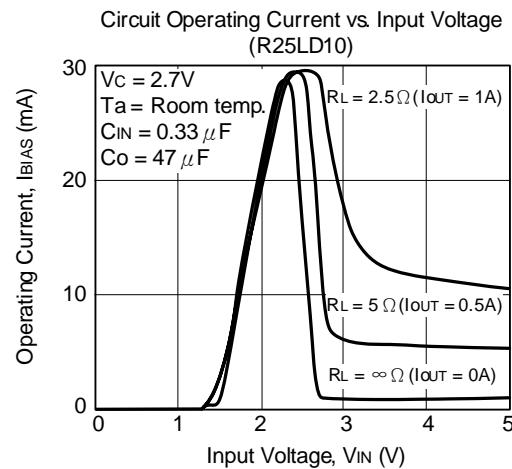
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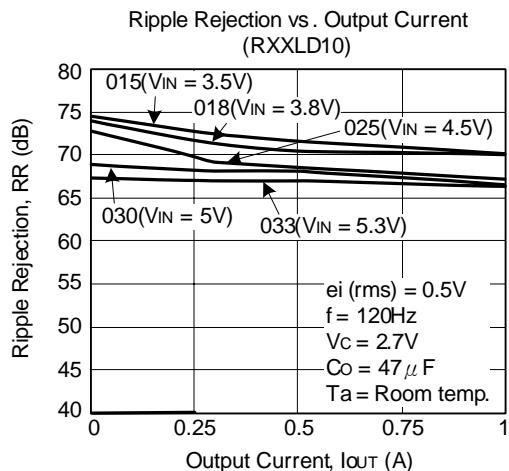
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■ TYPICAL CHARACTERISTICS(cont.)



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