## Features

$X$ Integrated Balanced Mixer, LO Buffer and LO Doubler
$X+23.0 \mathrm{dBm}$ Input Third Order Intercept (IIP3)
$X+2.0 \mathrm{dBm}$ LO Drive Level
$X 4 \times 4 \mathrm{~mm}$ QFN Package
$X 100 \% \mathrm{RF}$ and DC Testing

## General Description

Mimix Broadband's $17.0-34.0 \mathrm{GHz}$ GaAs packaged up-converter has an input third order intercept point of +23.0 dBm and 10.0 dB of conversion loss. The device consists of a balanced resistive pHEMT mixer, LO buffer amplifier and LO doubler packaged in an industry standard, fully molded $4 \times 4 \mathrm{~mm}$ QFN package. IF and *IF mixer inputs are provided and an external 180 degree hybrid is required. The device includes on-chip ESD protection structures and DC by-pass capacitors to ease the implementation and volume assembly of the packaged part. This device is well suited for Millimeter-wave Point-to-Point Radio, LMDS, SATCOM and VSAT applications.


Absolute Maximum Ratings ${ }^{1}$

| Drain Voltage Supply (Vdd) | +4.3 V |
| :--- | :--- |
| Drain Current (Ids) | 350 mA |
| Input Power (Pin) IF | +5 dBm |
| Input Power (Pin) LO | +10 dBm |
| Storage Temperature (Tstg) | -65 to $+165^{\circ} \mathrm{C}$ |
| Operating Temperature (Tb) | -55 to $+85^{\circ} \mathrm{C}$ |
| ESD Min. - Machine Model (MM) | Class A |
| ESD Min. - Human Body Model (HBM) | Class 0 |
| MSL Level | MSL3 |

Electrical Characteristics (Ambient Temperature T = $25{ }^{\circ} \mathrm{C}$ )

| Parameter | Units | Min. | Typ. | Max. |
| :--- | :---: | :---: | :---: | :---: |
| Frequency Range (RF) | GHz | 17 |  | 34 |
| Frequency Range (LO) | GHz | 8 |  | 20 |
| Frequency Range (IF) | GHz | DC |  | 3.5 |
| Conversion Loss (CL) | dB |  | 10 |  |
| Input Third Order Intercept (IIP3) | dBm |  | 23 |  |
| LO Input Drive | dBm |  | 2 |  |
| RF Input Return Loss | dB |  | 15 |  |
| LO Input Return Loss | dB |  | 15 |  |
| IF Input Return Loss | dB |  | 15 |  |
| 2xLO Leakage at RF | dBm |  | -20 |  |
| 1xLO Leakage at RF | dBm |  | -30 |  |
| Drain Bias Voltage (Vd1,2) | VDC |  | 4.0 |  |
| Gate Bias Voltage (Vg1) | VDC |  | $\sim-0.2$ |  |
| Gate Bias Voltage (Vg2,3) | VDC |  | -0.8 |  |
| Gate Bias Voltage (Vss) | VDC |  | -4.0 |  |
| Supply Current (Id1) | mA |  | 130 |  |
| Supply Current (Id2) | mA |  | $\sim 60$ |  |
| Supply Current (Iss) | mA |  | 45 |  |

## Up-Converter Measurements


$\mathrm{VDI}=5 \mathrm{~V}, \mathrm{IDI}=140 \mathrm{~mA}, \mathrm{VD} 2=5 \mathrm{~V}, \mathrm{VG2VG3}=-0.8 \mathrm{~V}, \mathrm{VSS}=-5 \mathrm{~V}$

XU1010-QH, Glob Top, R10C2: USB Conversion Loss \& IIP3 vs. RF,
PLO $=\mathbf{2 ~ d B m}$, PIFscl $=\mathbf{0} \mathbf{~ d B m}$, IF1 - IF2 = 10 MHz , Doubler and Mixer bias swept


- Loss, VD2 (V) $=4.5$, VG2VG3 $(\mathrm{M})=0.8$
 - Loss, VD2 $(\mathrm{V})=4.5, \mathrm{VG2VG3} \mathrm{M}=0.0 .6$
$-\mathrm{Loss}, \mathrm{VD2}(\mathrm{~V})=5, \mathrm{VG2VG3}(\mathrm{~V})=0.8$ LLoss, VD2 ( V$)=5, \mathrm{VG2VG3}(\mathrm{~V})=0.0$
LLoss, VD2 V$)=5, \mathrm{VG2VG3}(\mathrm{~V})=0.7$


 $-\|$ P3, VD2 $(\mathrm{V})=4.5, \mathrm{VG2VG3}(\mathrm{~V})=0.8$
$-\| P 3, \mathrm{VD2}(\mathrm{~V})=4, \mathrm{VG} 2 \mathrm{VG}(\mathrm{V})=07$
 - 1 P3, VD2 $(\mathrm{V})=5$, VG2VG3 $(\mathrm{N})=0.8$
 * $11 P 3$, VD2 $(\mathbb{V})=5.5, \mathrm{VG} 2 \mathrm{VG} 3(\mathrm{~V})=0.8$

$\mathrm{VDI}=5 \mathrm{~V}, \mathrm{IDI}=140 \mathrm{~mA}, \mathrm{VD} 2=5 \mathrm{~V}, \mathrm{VG} 2 \mathrm{VG} 3=-0.8 \mathrm{~V}, \mathrm{VSS}=-5 \mathrm{~V}$

$\mathrm{VDI}=4.5,5 \& 5.5 \mathrm{~V}, \mathrm{IDI}$ swept, $\mathrm{VD} 2=5 \mathrm{~V}, \mathrm{VG} 2 \mathrm{VG} 3=-0.8 \mathrm{~V}, \mathrm{VSS}=-5 \mathrm{~V}$

$\mathrm{VDI}=4.5,5 \& 5.5 \mathrm{~V}$, IDI swept, $\mathrm{VD} 2=5 \mathrm{~V}, \mathrm{VG2VG} 3=-0.8 \mathrm{~V}, \mathrm{VSS}=-5 \mathrm{~V}$

App Note [1] Biasing - As shown in the Pin Designations table, the device is operated by biasing VD1 and VD2 at 4.0V with 130 mA and approximately 60 mA respectively. VG2,3 and VSS require fixed voltage biasing with VSS biased at -4.0 V and 45 mA . It is recommended to use active bias on VG1 to keep the current in VD1 constant in order to maintain the best performance over temperature. Depending on the supply voltage available and the power dissipation constraints, the bias circuit may be a single transistor or a low power operational amplifier, with a low value resistor in series with the drain supply used to sense the current. The gate of the pHEMT is controlled to maintain correct drain current and thus drain voltage. The typical gate voltage needed to do this is -0.2 V . Make sure to sequence the applied voltage to ensure negative gate bias is available before applying the positive drain supply.

App Note [2] Board Layout - As shown in the board layout, it is recommended to provide 100pF decoupling caps as close to the bias pins as possible, with additional $10 \mu \mathrm{~F}$ decoupling caps.

Recommended Board Layout


## Package Dimensions / Layout




TOP VIEW

MARKNGES:
PW 1/BOM REV/Pb FREE SMM MIMXX PART/MODEL NO. TMAFER LOT MUMBER DATE CODE


BOTTOM VIEW

RECOMMENDED SOLDER PAD PITCH AND DIMENSIONS


Functional Block Diagram


NOTES:

1. DIMENSIONS ARE IN MM.
2. VIEWS ARE NOT TO SCALE: USE DIMENSIONS AND TABLE.

Pin Designations

| Pin Number | Pin Name | Pin Function | Nominal Value |
| :---: | :---: | :---: | :---: |
| 1 | GND |  |  |
| 2 | IF1 | IF1 Output |  |
| $3-4$ | NC | Not Connected |  |
| 5 | IF2 | IF2 Output |  |
| $6-8$ | GND |  |  |
| 7 | NC | Not Connected |  |
| 8 | GND |  |  |
| 9 | LO | LO Input |  |
| 10 | GND |  |  |
| $11-12$ | NC | Not Connected |  |
| 13 | VD1 | LO Buffer Drain | $4.0 \mathrm{~V}, 130 \mathrm{~mA}$ |
| 14 | VD2 | LO Doubler Drain | $4.0 \mathrm{~V}, 60 \mathrm{~mA}$ |
| $15-16$ | GND |  |  |
| 17 | VSS | Doubler/ESD Bias | $4.0 \mathrm{~V}, 45 \mathrm{~mA}$ |
| 18 | VG1 | LO Buffer Gate | -0.2 V |
| 19 | VG2,3 | LO Doubler/Mixer Gate | -0.8 V |
| 20 | NC | Not Connected |  |
| 21 | GND |  |  |
| 22 | RF | RF Input |  |
| 23 | GND |  |  |
| 24 | NC | Not Connected |  |

## Handling and Assembly Information

CAUTION! - Mimix Broadband MMIC Products contain gallium arsenide (GaAs) which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- Do not ingest.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.

Electrostatic Sensitive Device - Observe all necessary precautions when handling.
Life Support Policy - Mimix Broadband's products are not authorized for use as critical components in life support devices or systems without the express written approval of the President and General Counsel of Mimix Broadband. As used herein: (1) Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user. (2) A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

Package Attachment - This packaged product from Mimix Broadband is provided as a rugged surface mount package compatible with high volume solder installation. Vacuum tools or other suitable pick and place equipment may be used to pick and place this part. Care should be taken to ensure that there are no voids or gaps in the solder connection so that good RF, DC and ground connections are maintained. Voids or gaps can eventually lead not only to RF performance degradation, but reduced reliability and life of the product due to thermal stress.

## Typical Reflow Profiles

| Reflow Profile | SnPb | Pb Free |
| :--- | :--- | :--- |
| Ramp Up Rate | $3-4{ }^{\circ} \mathrm{C} / \mathrm{sec}$ | $3-4{ }^{\circ} \mathrm{C} / \mathrm{sec}$ |
| Activation Time and Temperature | $60-120 \mathrm{sec} @ 140-160^{\circ} \mathrm{C}$ | $60-180 \mathrm{sec} @ 170-200^{\circ} \mathrm{C}$ |
| Time Above Melting Point | $60-150 \mathrm{sec}$ | $60-150 \mathrm{sec}$ |
| Max Peak Temperature | $240^{\circ} \mathrm{C}$ | $265^{\circ} \mathrm{C}$ |
| Time Within $5^{\circ} \mathrm{C}$ of Peak | $10-20 \mathrm{sec}$ | $10-20 \mathrm{sec}$ |
| Ramp Down Rate | $4-6{ }^{\circ} \mathrm{C} / \mathrm{sec}$ | $4-6^{\circ} \mathrm{C} / \mathrm{sec}$ |

Mimix Lead-Free RoHS Compliant Program - Mimix has an active program in place to meet customer and governmental requirements for eliminating lead ( Pb ) and other environmentally hazardous materials from our products. All Mimix RoHS compliant components are form, fit and functional replacements for their non-RoHS equivalents. Lead plating of our RoHS compliant parts is $100 \%$ matte tin ( Sn ) over copper alloy and is backwards compatible with current standard SnPb low-temperature reflow processes as well as higher temperature ( $260^{\circ} \mathrm{C}$ reflow) " Pb Free" processes.

## Part Number for Ordering

XU1010-QH-0G00
XU1010-QH-0G0T
XU1010-QH-EV1

## Description

Matte Tin plated RoHS compliant $4 \times 424$ L QFN surface mount package in bulk quantity
Matte Tin plated RoHS compliant $4 \times 424$ L QFN surface mount package in tape and reel
XU1010-QH evaluation board


Caution: ESD Sensitive
Appropriate precautions in handling, packaging and testing devices must be observed.

Proper ESD procedures should be followed when handling this device.

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