

Silicon PNP Power Transistors

2SB919

DESCRIPTION

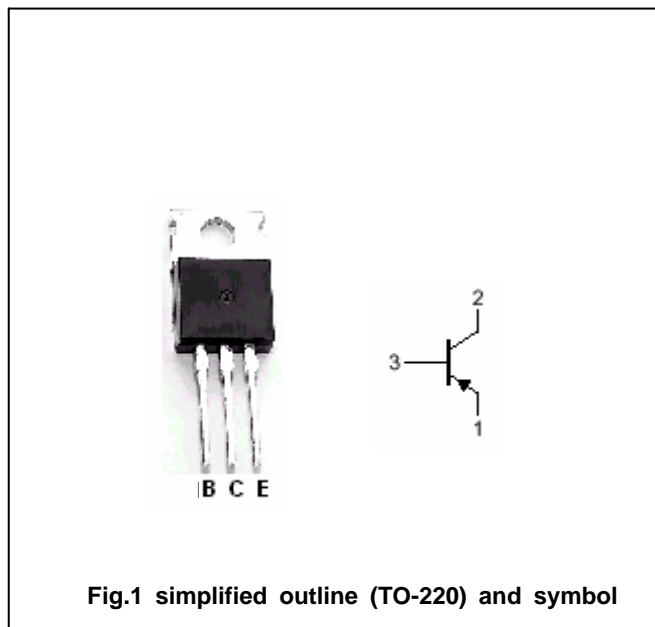
- With TO-220C package
- Complement to type 2SD1235
- Low collector saturation voltage
- Large current capacity

APPLICATIONS

- Large current switching of relay drivers, high-speed inverters, converters

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector; connected to mounting base
3	Base

Absolute maximum ratings($T_a=25$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-60	V
V_{CEO}	Collector-emitter voltage	Open base	-30	V
V_{EBO}	Emitter-base voltage	Open collector	-6	V
I_C	Collector current (DC)		-8	A
I_{CM}	Collector current-Peak		-15	A
P_C	Collector dissipation	$T_a=25$	1.75	W
		$T_C=25$	30	
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-50~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-1mA; R _{BE} =	-30			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA; I _E =0	-60			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA; I _C =0	-6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-3A; I _B =-0.15A			-0.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-40V; I _E =0			-0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =-4V; I _C =0			-0.1	mA
h _{FE-1}	DC current gain	I _C =-1A ; V _{CE} =-2V	70		280	
h _{FE-2}	DC current gain	I _C =-4A ; V _{CE} =-2V	30			
f _T	Transition frequency	I _C =-1A ; V _{CE} =-5V		120		MHz

Switching times

t _{on}	Turn-on time	I _C =-4A ; V _{CC} =-10V I _{B1} =-I _{B2} =-0.2A; R _L =2.5		0.1		μs
t _{stg}	Storage time			0.2		μs
t _f	Turn-off time			0.03		μs

◆ h_{FE-1} Classifications

Q	R	S
70-140	100-200	140-280

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance: ± 0.10mm)

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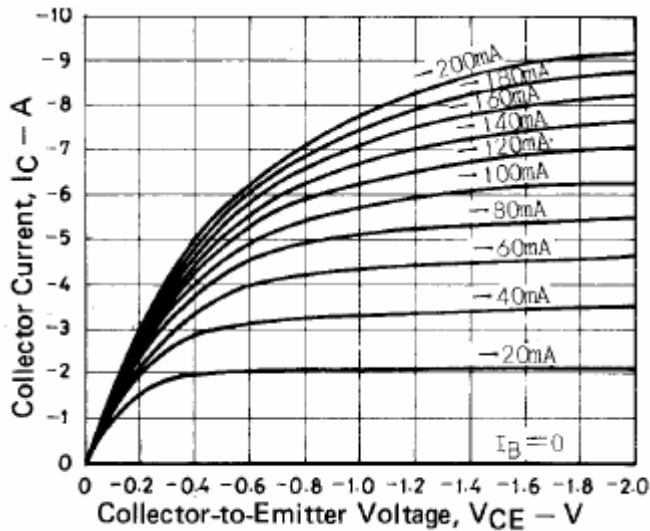


Fig.3 Static Characteristic

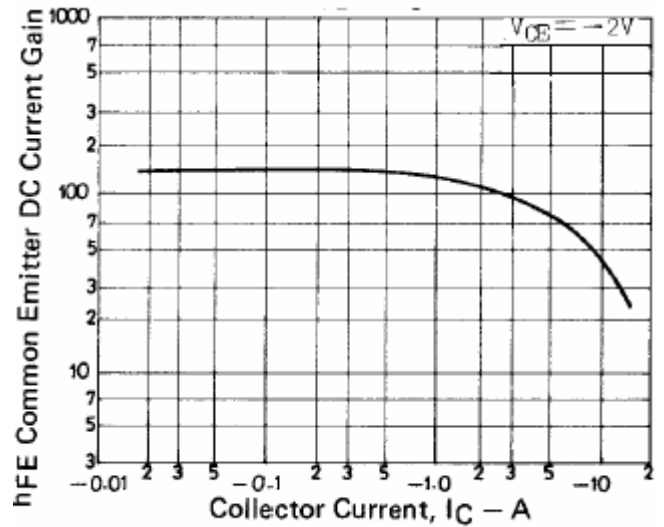


Fig.4 DC current Gain

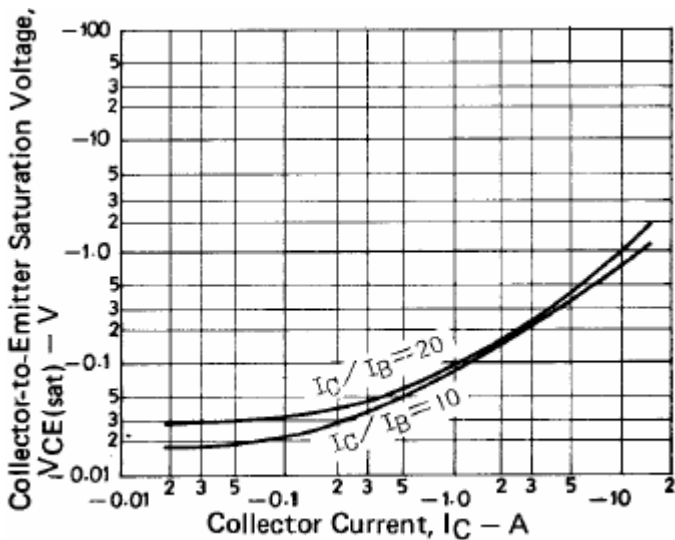


Fig.5 Collector-Emmitter Saturation Voltage

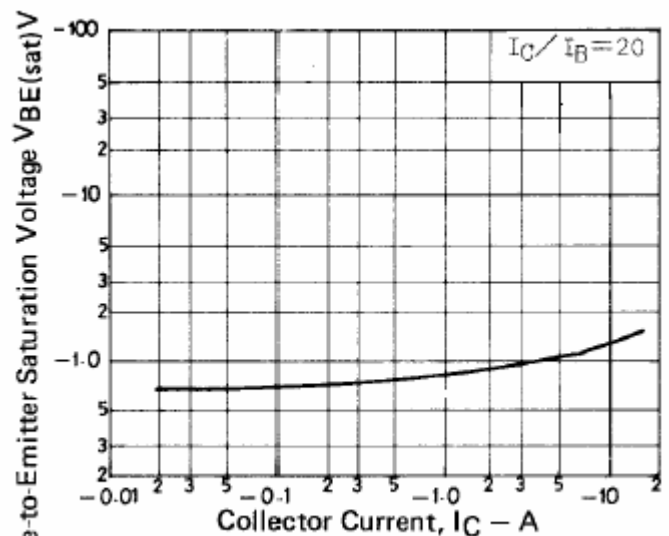


Fig.6 Base-Emmitter Saturation Voltage

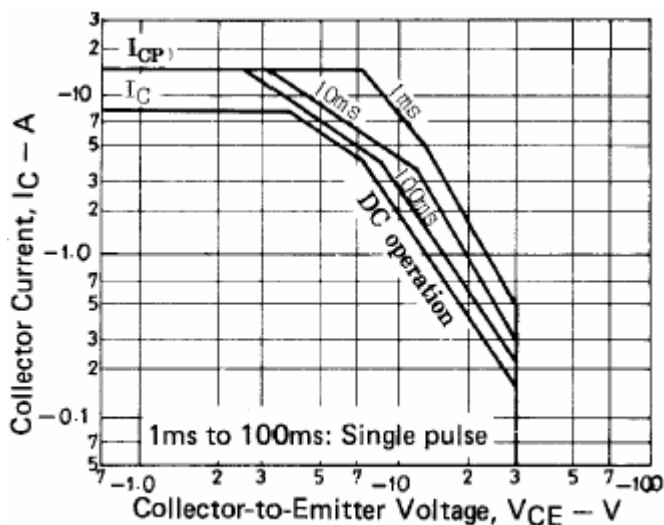


Fig.7 Safe Operating Area