

GM2501

P-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	-20V
RDS(ON)	200mΩ
ID	-2.6A

Description

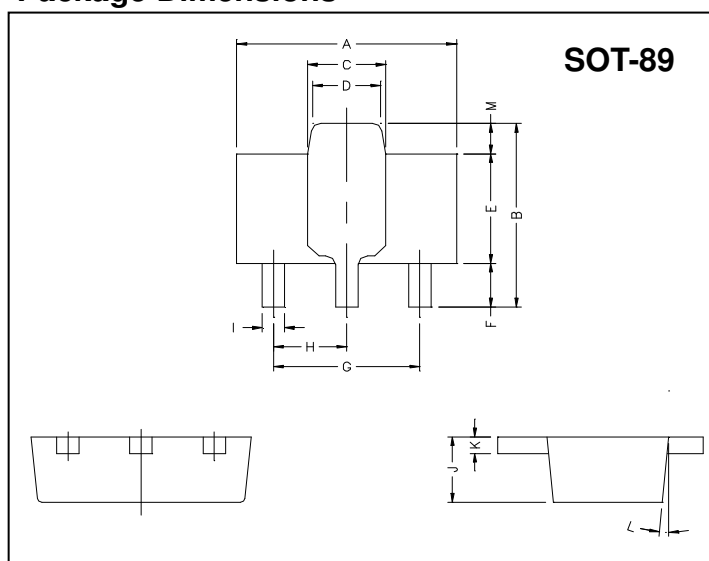
The GM2501 provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The SOT-89 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage and battery power applications.

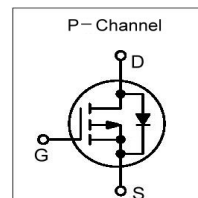
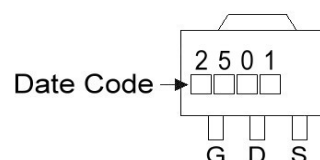
Features

- *Simple Drive Requirement
- *Surface Mount Device

Package Dimensions

**SOT-89**

Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.4	4.6	G	3.00	REF.
B	4.05	4.25	H	1.50	REF.
C	1.50	1.70	I	0.40	0.52
D	1.30	1.50	J	1.40	1.60
E	2.40	2.60	K	0.35	0.41
F	0.89	1.20	L	5° TYP.	
			M	0.70 REF.	

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	±12	V
Continuous Drain Current ³	$I_D @ TA=25^{\circ}C$	-2.6	A
Continuous Drain Current ³	$I_D @ TA=70^{\circ}C$	-2.1	A
Pulsed Drain Current ^{1,2}	I_{DM}	-10	A
Total Power Dissipation	$P_D @ TA=25^{\circ}C$	1.5	W
Linear Derating Factor		0.012	W/°C
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient Max.	R_{thj-a}	83.3	°C/W

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	-20	-	-	V	V _{GS} =0, I _D =-250uA
Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} /ΔT _j	-	-0.1	-	V/°C	Reference to 25°C, I _D =-1mA
Gate Threshold Voltage	V _{GS(th)}	-0.5	-	-	V	V _{DS} =V _{GS} , I _D =-250uA
Forward Transconductance	g _{fs}	-	4.4	-	S	V _{DS} =-5V, I _D =-2.6A
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} = ±12V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	-1	uA	V _{DS} =-20V, V _{GS} =0
Drain-Source Leakage Current(T _j =70°C)		-	-	-10	uA	V _{DS} =-16V, V _{GS} =0
Static Drain-Source On-Resistance ²	R _{DS(ON)}	-	-	200	mΩ	V _{GS} =-10V, I _D =-2.6A
		-	-	250		V _{GS} =-4.5V, I _D =-2.0A
		-	-	300		V _{GS} =-2.5V, I _D =-1.0A
Total Gate Charge ²	Q _g	-	5.2	10	nC	I _D =-2.8A V _{DS} =-6V V _{GS} =-5V
Gate-Source Charge	Q _{gs}	-	1.36	-		
Gate-Drain ("Miller") Change	Q _{gd}	-	0.6	-		
Turn-on Delay Time ²	T _{d(on)}	-	5.2	-	ns	V _{DS} =-15V I _D =-1A V _{GS} =-10V R _G =6Ω R _D =15Ω
Rise Time	T _r	-	9.7	-		
Turn-off Delay Time	T _{d(off)}	-	19	-		
Fall Time	T _f	-	29	-		
Input Capacitance	C _{iss}	-	295	-	pF	V _{GS} =0V V _{DS} =-6V f=1.0MHz
Output Capacitance	C _{oss}	-	170	-		
Reverse Transfer Capacitance	C _{rss}	-	65	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	-1.2	V	I _S =-1.6A, V _{GS} =0V, T _j =25°C
Continuous Source Current (Body Diode)	I _S	-	-	-1	A	V _D =V _G =0V, V _S =-1.2V
Pulsed Source Current (Body Diode) ¹	I _{SM}	-	-	-10	A	

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Surface mounted on FR4 board, t ≤ 10sec.

Characteristics Curve

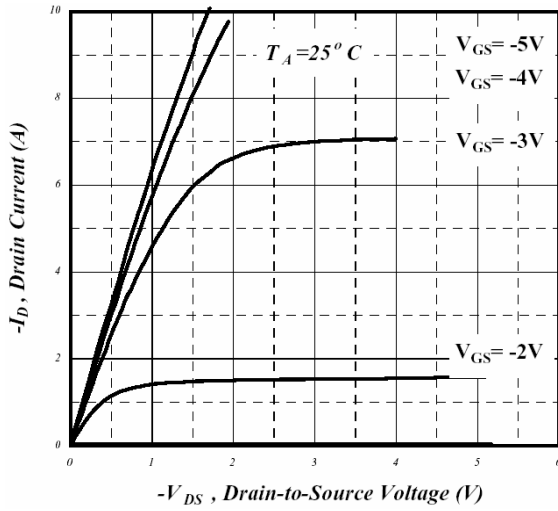


Fig 1. Typical Output Characteristics

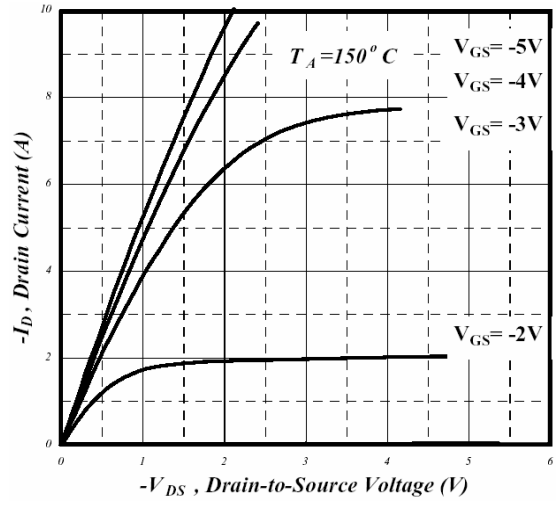


Fig 2. Typical Output Characteristics

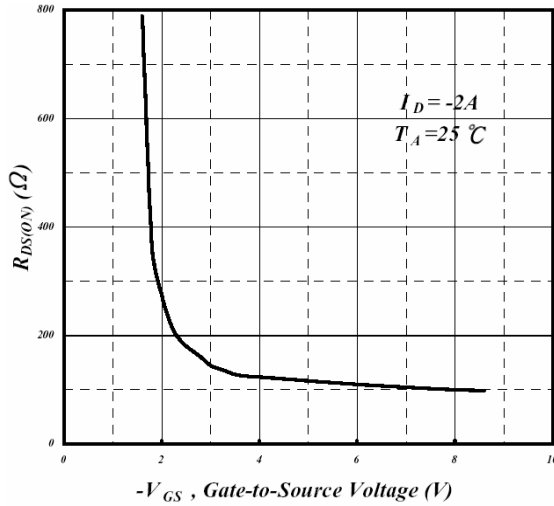


Fig 3. On-Resistance v.s. Gate Voltage

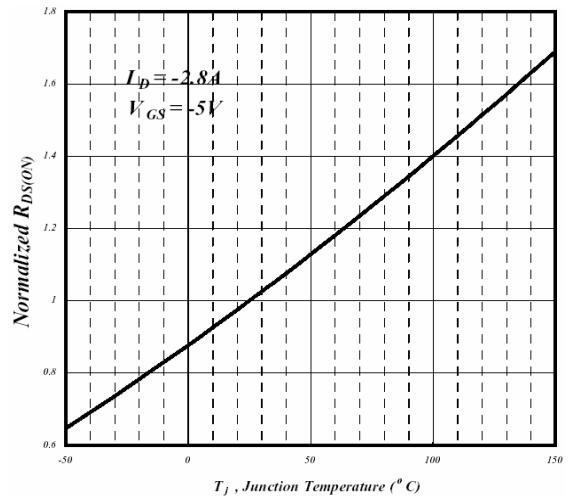


Fig 4. Normalized On-Resistance v.s. Junction Temperature

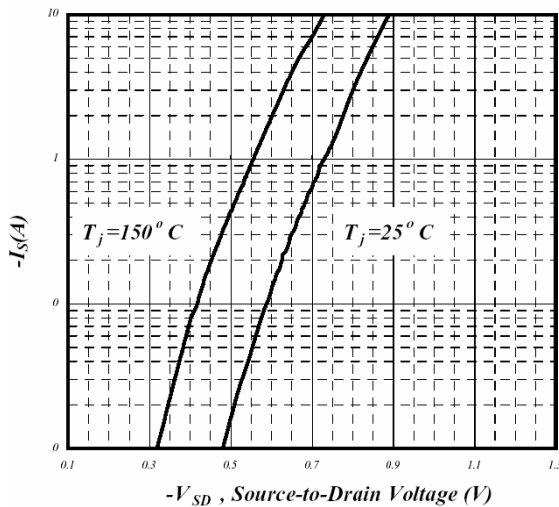


Fig 5. Forward Characteristics of Reverse Diode

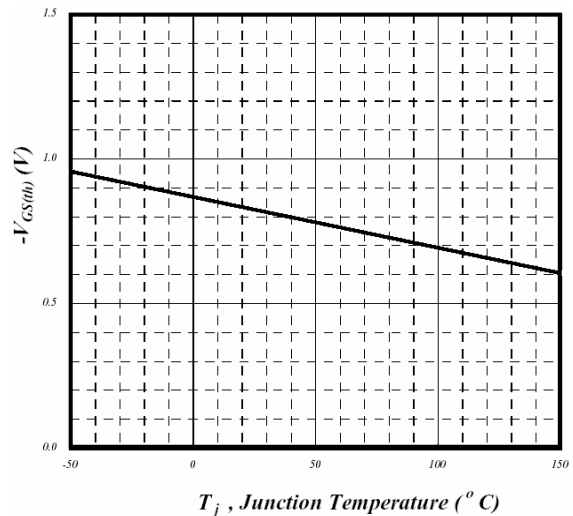


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

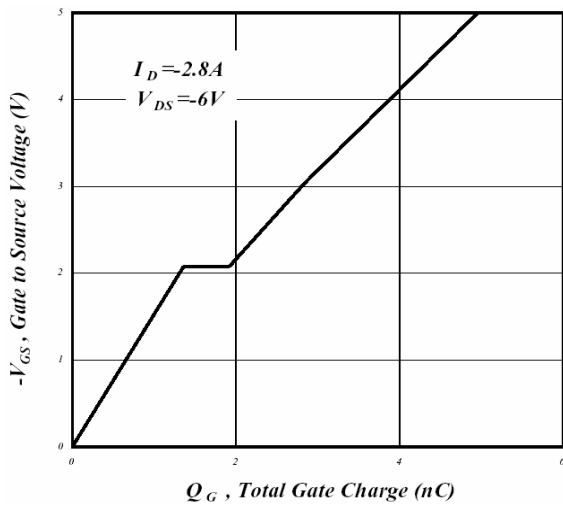


Fig 7. Gate Charge Characteristics

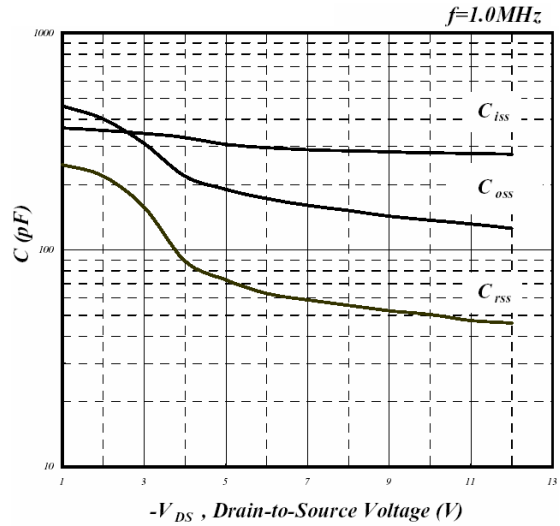


Fig 8. Typical Capacitance Characteristics

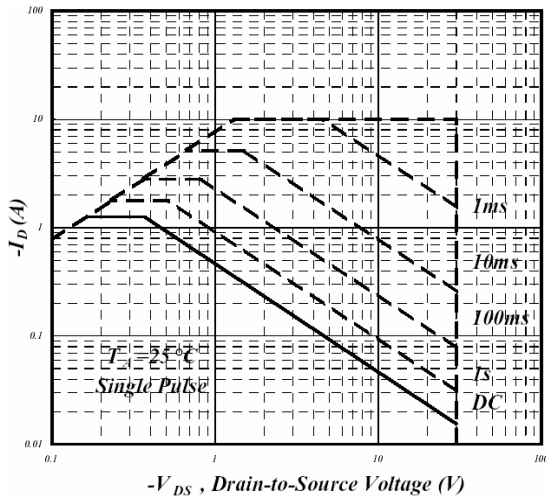


Fig 9. Maximum Safe Operating Area

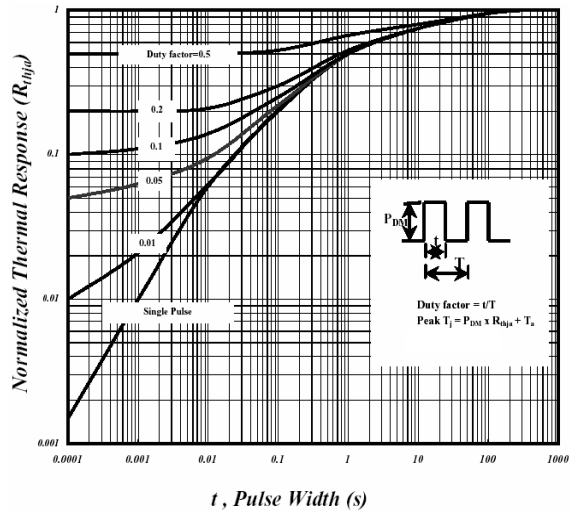


Fig 10. Effective Transient Thermal Impedance



Fig 11. Switching Time Waveform

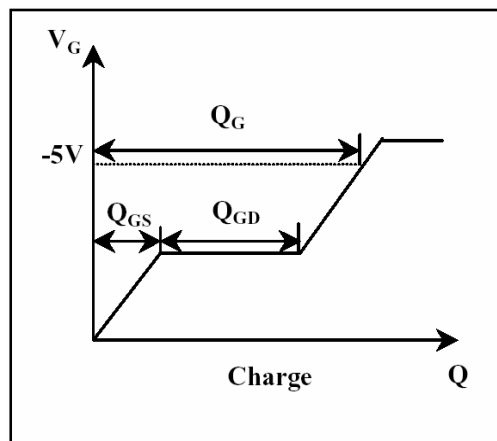


Fig 12. Gate Charge Waveform

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Head Office And Factory:
 • **Taiwan:** No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
 TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
 • **China:** (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
 TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165