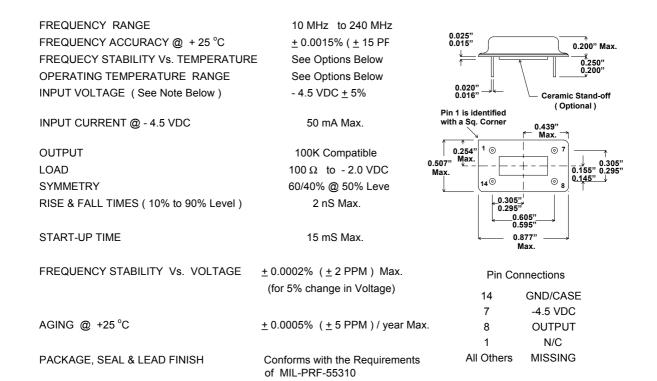


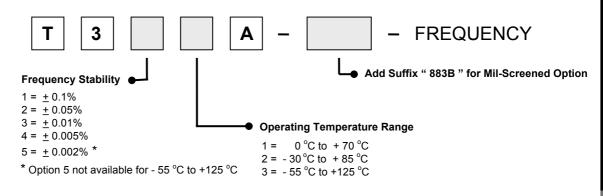
T300A SERIES (ECL) STANDARD SPECIFICATIONS



NOTE: For PECL applications, Xsis 300 Series ECL oscillators can be operated with +5 VDC ± 10% on Pin 14 and power supply return on Pin 7. The output logic levels will still be referenced to +5 VDC and the case will be at +5 VDC, however, 0.8 V peak to peak output signal can be AC or DC coupled as necessary.

Contact Xsis Engineering for special requirements such as, Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.

ORDERING INFORMATION (Select from options below):



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EXAMPLE: T343A - 883B - 24.000 MHz = 14 Pin Package, 100K ECL, \pm 0.005% over -55 °C to +125 °C, Mil-Screened, and 24.000 MHz