

PECL Series 4-Bit Programmable 10K ECL Delays

GENERAL: For Operating Specifications and Test Conditions, see Tables I and VII on page 8 of this catalog. Delays specified for the Leading Edge.

Minimum Input Pulse Width 30% max. Delay
Temperature Coefficient ≤ 300 ppm/°C typical
Supply Current, I_{EE} 120 mA typ., 135 mA max.

Electrical Specifications at 25°C

4 Bit 10K ECL Part Number	Delay per Step (ns)	Output Delay (ns) per Program Setting (P4*P3*P2*P1)																Max. Deviation ref. to 0000 (ns)
		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111	
PECL6-0.5	$0.5 \pm .25$	6±1	6.5	7.0	7.5	8.0	8.5	9.0	9.5	10.0	10.5	11.0	11.5	12.0	13.0	14.0	13.5	± 0.7
PECL6-1	$1.0 \pm .50$	6±1	7.0	8.0	9.0	10.0	11.0	12.0	13.0	14.0	15.0	16.0	17.0	18.0	19.0	20.0	21.0	± 1.0
PECL6-2	$2.0 \pm .70$	6±1	8.0	10.0	12.0	14.0	16.0	18.0	20.0	22.0	24.0	26.0	28.0	30.0	32.0	34.0	36.0	± 1.5
PECL6-5	5.0 ± 1.0	6±1	11.0	16.0	21.0	26.0	31.0	36.0	41.0	46.0	51.0	56.0	61.0	66.0	71.0	76.0	81	± 3.0
PECL10-1	$1.0 \pm .50$	10±1	11.0	12.0	13.0	14.0	15.0	16.0	17.0	18.0	19.0	20.0	21.0	22.0	23.0	24.0	25.0	± 1.0
PECL10-2	$2.0 \pm .70$	10±1	12.0	14.0	16.0	18.0	20.0	22.0	24.0	26.0	28.0	30.0	32.0	34.0	36.0	38.0	40.0	± 1.5
PECL10-5	5.0 ± 1.0	10±1	15.0	20.0	25.0	30.0	35.0	40.0	45.0	50.0	55.0	60.0	65.0	70.0	75.0	80.0	85.0	± 3.0
PECL15-1	$1.0 \pm .50$	15±1	16.0	17.0	18.0	19.0	20.0	21.0	22.0	23.0	24.0	25.0	26.0	27.0	28.0	29.0	30.0	± 1.0

4PECLH Series 4-Bit Programmable 10KH ECL Delays

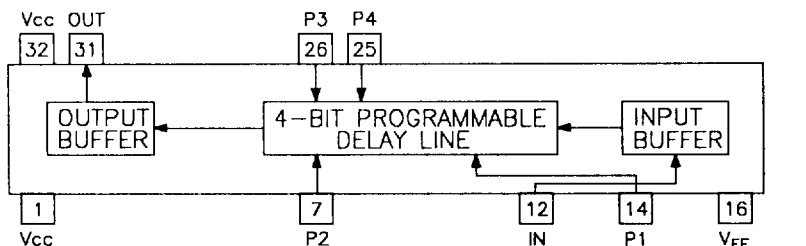
GENERAL: For Operating Specifications and Test Conditions, see Tables V and VIII on page 7 of this catalog. Delays specified for the Leading Edge.

Minimum Input Pulse Width 30% max. Delay
Temperature Coefficient ≤ 300 ppm/°C typical
Supply Current, I_{EE} 120 mA typ., 135 mA max.

Electrical Specifications at 25°C

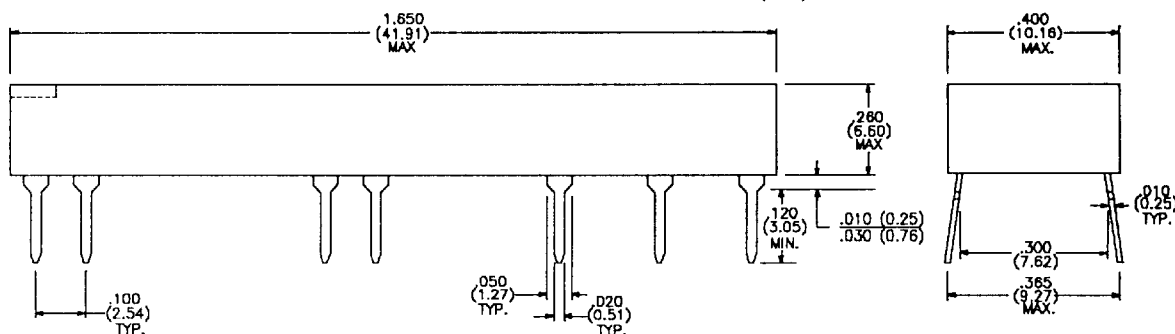
4 Bit 10KH ECL Part Number	Delay per Step (ns)	Output Delay (ns) per Program Setting (P4*P3*P2*P1)																Max. Deviation ref. to 0000 (ns)
		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111	
4PECLH-0.5	$0.5 \pm .25$	3±1.0	3.5	4.0	4.5	5.0	5.5	6.0	6.5	7.0	7.5	8.0	8.5	9.0	9.5	10.0	10.5	± .30
4PECLH-1	$1.0 \pm .40$	3±1.0	4.0	5.0	6.0	7.0	8.0	9.0	10.0	11.0	12.0	13.0	14.0	15.0	16.0	17.0	18.0	± .50
4PECLH-1.5	$1.5 \pm .50$	3±1.0	4.5	6.0	7.5	9.0	10.5	12.0	13.5	15.0	16.5	18.0	19.5	21.0	22.5	24.0	25.5	± .70
4PECLH-2	$2.0 \pm .70$	3±1.0	5.0	7.0	9.0	11.0	13.0	15.0	17.0	19.0	21.0	23.0	25.0	27.0	29.0	31.0	33.0	± .80
4PECLH-2.5	$2.5 \pm .70$	3±1.0	5.5	8.0	10.5	13.0	15.5	18.0	20.5	23.0	25.5	28.0	30.5	33.0	35.5	38.0	40.5	± .90
4PECLH-3	$3.0 \pm .70$	3±1.0	6.0	9.0	12.0	15.0	18.0	21.0	24.0	27.0	30.0	33.0	36.0	39.0	42.0	45.0	48.0	± 1.0
4PECLH-5	5.0 ± 1.0	3±1.0	8.0	13.0	18.0	23.0	28.0	33.0	38.0	43.0	48.0	53.0	58.0	63.0	68.0	73.0	78.0	± 1.5

CUMULATIVE TOLERANCES: Maximum Deviation Tolerances for Programmed Delays Referenced to Initial Delay, Setting "0000." Tables display values of delay with respect to input, whose tolerance is the cumulative error of the initial delay (± 1.0) and the Maximum Deviation. For example, the setting "111" delay of PECL6-1 is 15.0 ± 1.0 ref. to "0000," and 21.0 ± 2.0 ref. to the input.



SCHEMATIC DIAGRAM

PHYSICAL DIMENSIONS inches (mm)



VARIATIONS AVAILABLE. FOR INTERMEDIATE VALUES AND/OR CUSTOM DESIGNS PLEASE CONSULT THE FACTORY.

Specifications subject to change without notice.

PECL 4-7/93

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