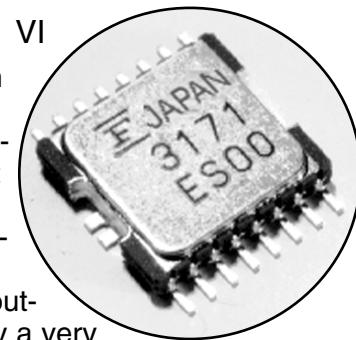


DESCRIPTION

The FMM3171VI is a laser driver IC for up to 1.2Gb/s optical transmission systems. The GaAs MES-FET IC process allows for high speed operation with low power consumption. There are two data inputs options; a complementary input or a single-ended input using a selectable D-F/F. The output duty ratio can be set by adjusting the input reference voltage (Vref). The differential mark-density monitor (Mmk) output proportional to the peak current output signal. The peak current and bias current output is disabled by the shutdown terminal, which is ECL compatible. Logic "high" causes the output to be disabled. The High speed turn-on and turn-off is accomplished by a very low compute time constant. The amount of peak current and bias current are monitored by the current flow at Ip and Ib terminal which must be connected to VSS. This FMM3171VI is an excellent choice use as a laser driver for OC-3/OC-12/OC-24 and STM1/STM4 transmitters.



FEATURES

- ECL Compatible Data Input
- Built in D-F/F (Optional)
- Complementary Data Input (Optional)
- Modulation Current: 0 to 70mA
- Bias Current: 0 to 70mA
- Output Shutdown Function
- Modulation Current and Bias Current Monitor
- Duty Ratio Monitor
- Duty Ratio Control
- Single Power Supply: -5.2V
- Separated Peak Current and Bias Current Outputs
- Small Package: SSOP-16

ABSOLUTE MAXIMUM RATINGS

(VDD = 0V)

Parameter	Symbol	Ratings	Unit
Storage Temperature	Tstg	-40 to +125	°C
Operating Temperature*	TOP	-40 to +85	°C
Supply Voltage	VSS	-7.0 to 0	V
Input Voltage	DIN, \bar{D} IN, CIN	VSS to VDD	V
Peak Current Control Voltage	V _{IP}	VSS -2.0 to VSS +2.1	V
Bias Current Control Voltage	V _{IB}	VSS -2.0 to VSS +2.1	V

(*) Lower limit temperature corresponds to the ambient temperature and higher limit temperature corresponds to the case (the bottom of the case) temperature.

RECOMMENDED OPERATING CONDITIONS

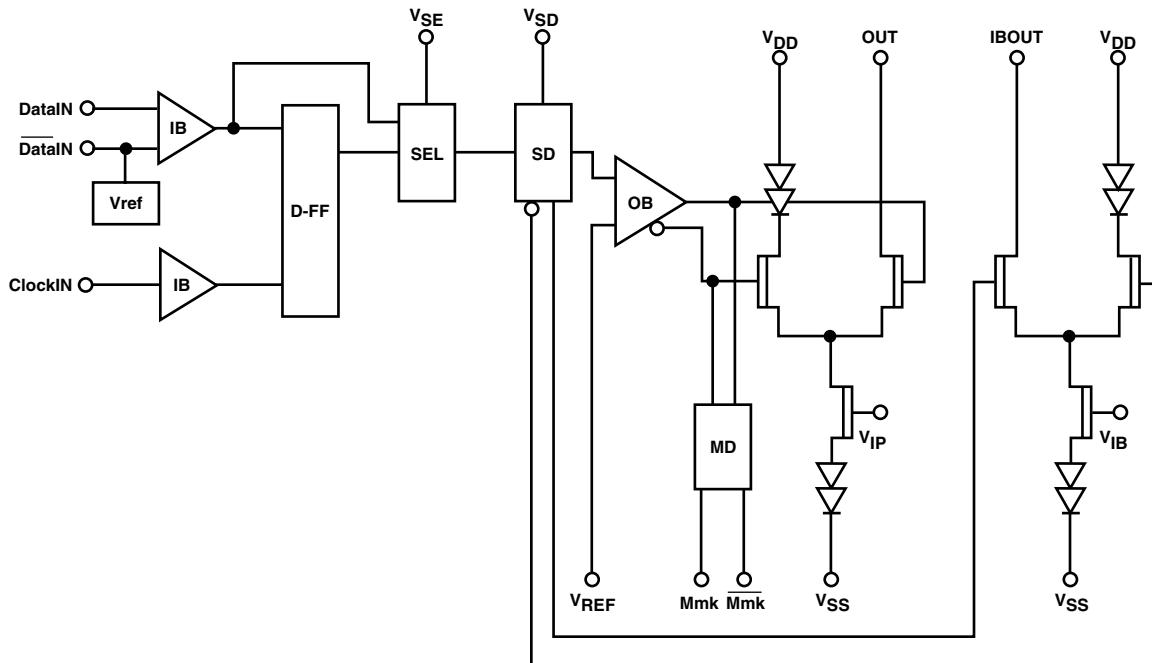
(VDD = 0V)

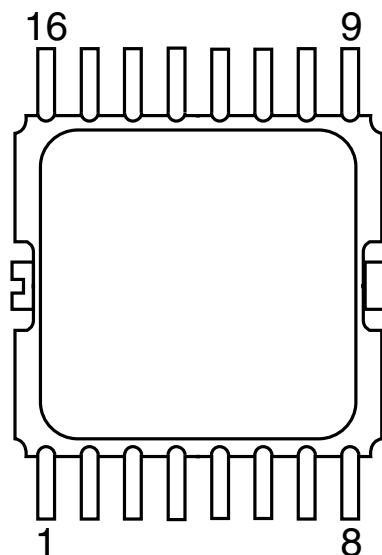
Parameter	Symbol	Test Conditions	Limit			Unit
			Min.	Typ.	Max.	
Power Supply Voltage	VSS		-5.72	-5.2	-4.94	V
Data & Clock Input	V _{IH}		-1.0	-0.9	-0.7	V
	V _{IL}		-1.9	-1.7	-1.6	V
Peak Current Control Voltage	V _{IP}		VSS	-	VSS +2.1	V
Bias Current Control Voltage	V _{IB}		VSS	-	VSS +2.1	V
Output Shutdown Control Voltage	V _{SD}	Output enable	-1.9	-1.7	-1.6	V
		Output disable (Shutdown)	-1.0	-0.9	-0.7	V
Selector Control Voltage	V _{SE}	D-F/F active	-	VDD	-	V
		D-F/F inactive	-	Open	-	-

ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $T_c=25^\circ\text{C}$)

Parameter	Symbol	Test Conditions	Limit			Unit
			Min.	Typ.	Max.	
Maximum Data Rate		NRZ	1.25	-	-	Gbps
Peak Current	IP	V _{IB} =V _{ss} V _{SD} ="Low"	V _{IP} =V _{ss}	-	1	2 mA
			V _{IP} =V _{ss} +2.1V	60	70	mA
Bias Current	IB	V _{IP} =V _{ss} V _{SD} ="Low"	V _{IB} =V _{ss}	-	1	2 mA
			V _{IB} =V _{ss} +2.1V	60	70	mA
Residual Output Current	I _{sd}	V _{SD} ="High", V _{IP} =V _{IB} =V _{ss} +2.1V	-	2	4	mA
Output Rise Time	tr	RL=10Ω IP=60mA 20%-80%	-	-	250	psec.
Output Fall Time			-	-	250	psec.
Mark-Density Monitor Output	VMmk	RL(for Mmk) =1kΩ/15nF to GND, NRZ, Mark density =50%	Input Duty	-	-	-
			100%	-	-1.0	- V
			50%	-	-0.5	- V
			0%	-	GND	-
Power Supply Current	I _{ss}	V _{ss} =-5.2, IP=60mA, IB=30mA	-	160	180	mA

FMM3171VI Block Diagram



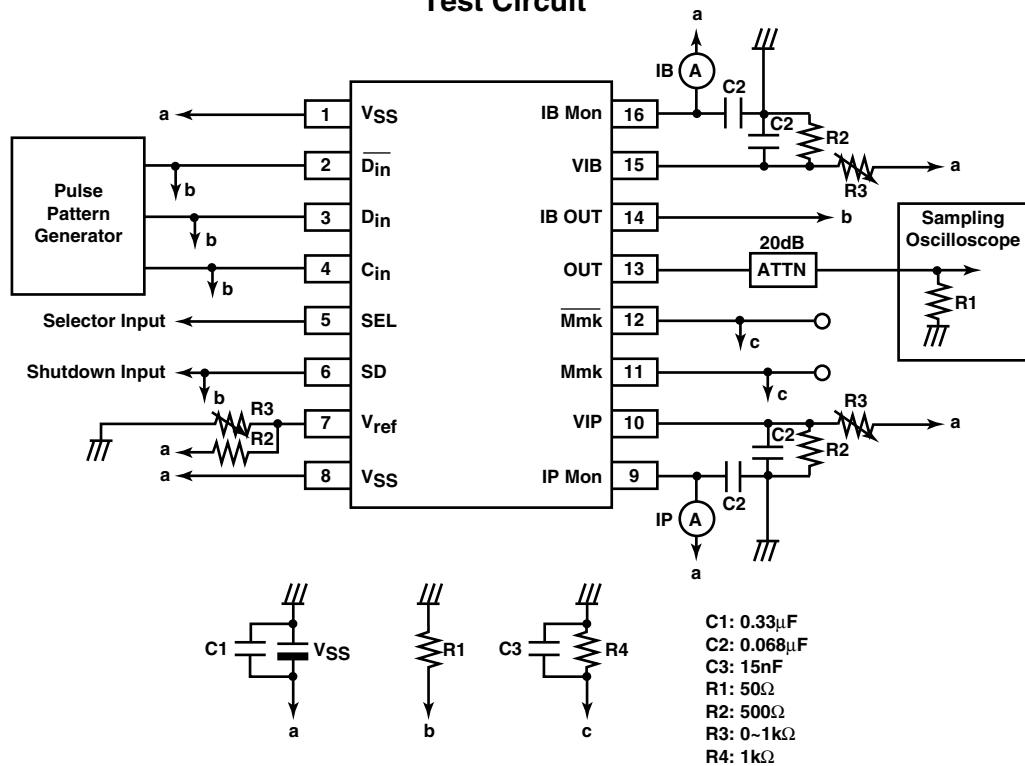


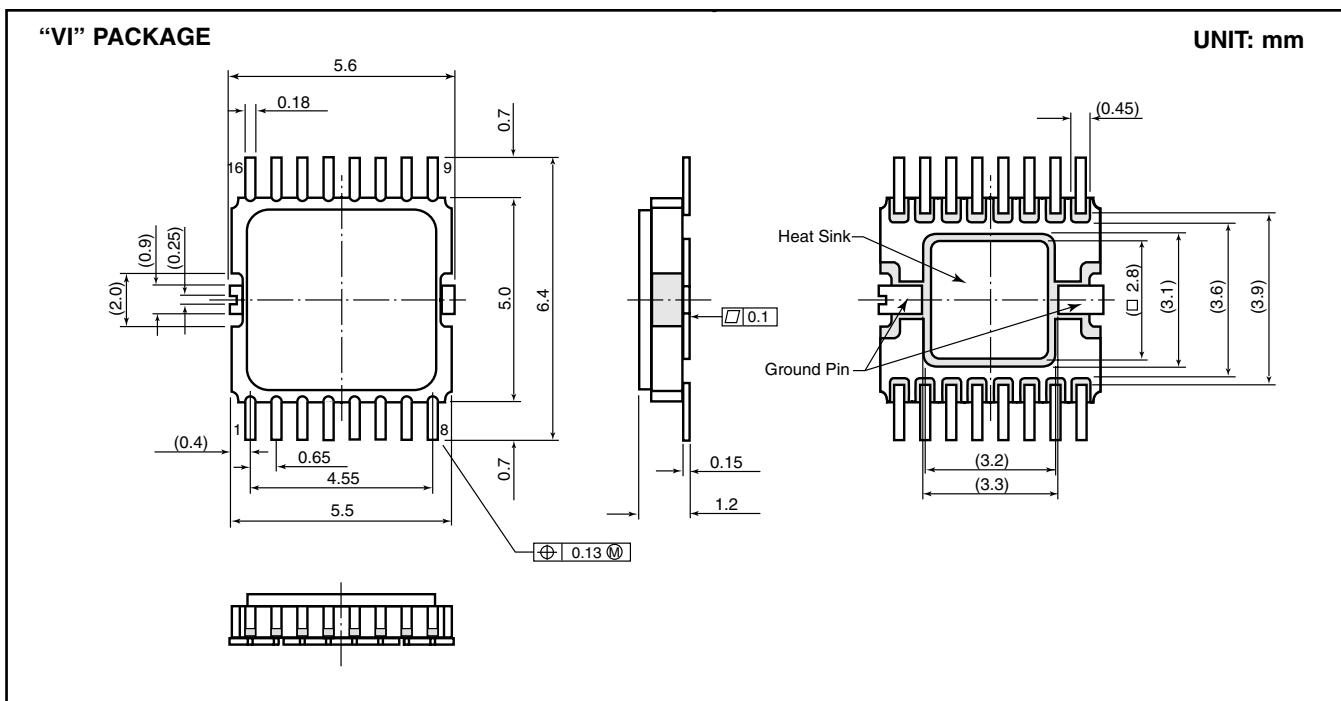
PIN DESCRIPTION FMM3171VI

#	FUNCTION
1	VSS
2	DATA INPUT
3	DATA INPUT
4	CLOCK INPUT
5	SELECTOR (VSE)
6	SHUT DOWN (VSD)
7	VREF(Duty Control)
8	VSS
9	IP MON (VSS)
10	VIP
11	Mmk(Duty Monitor)
12	Mmk(Duty Monitor)
13	OUT
14	IBOUT
15	VIB
16	IB MON (VSS)

Note: VDD is connected to the back side of the package

Test Circuit





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- Do not put this product into the mouth.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.

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