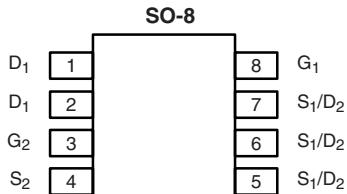


## Dual N-Channel 30-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY				
	V <sub>DS</sub> (V)	R <sub>DS(on)</sub> ( $\Omega$ )	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
Channel-1	30	0.021 at V <sub>GS</sub> = 10 V	8.4	6.7
		0.027 at V <sub>GS</sub> = 4.5 V	7.4	
Channel-2		0.020 at V <sub>GS</sub> = 10 V	8 <sup>d</sup>	7.0
		0.025 at V <sub>GS</sub> = 4.5 V	8 <sup>d</sup>	

SCHOTTKY PRODUCT SUMMARY		
V <sub>DS</sub> (V)	V <sub>SD</sub> (V) Diode Forward Voltage	I <sub>F</sub> (A)
30	0.50 V at 1.0 A	2.0



Top View

Ordering Information: Si4914BDY-T1-E3 (Lead (Pb)-free)  
Si4914BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

### FEATURES

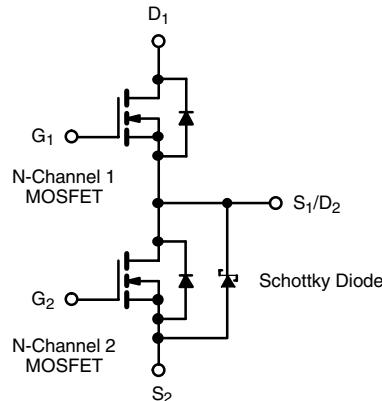
- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT® Plus Integrated Schottky
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS  
COMPLIANT  
HALOGEN  
FREE  
Available

### APPLICATIONS

- Notebook PC
  - System Power dc-to-dc



ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter	Symbol	Channel-1	Channel-2	Unit
Drain-Source Voltage	V <sub>DS</sub>	30		V
Gate-Source Voltage	V <sub>GS</sub>	20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>C</sub> = 25 °C	I <sub>D</sub>	8.4	8 <sup>d</sup>
	T <sub>C</sub> = 70 °C		6.7	7.4
	T <sub>A</sub> = 25 °C		6.7 <sup>b, c</sup>	7.4 <sup>b, c</sup>
	T <sub>A</sub> = 70 °C		5.3 <sup>b, c</sup>	5.7 <sup>b, c</sup>
Pulsed Drain Current (10 µs Pulse Width)	I <sub>DM</sub>	40	40	A
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	2.4	2.8
	T <sub>A</sub> = 25 °C		1.0 <sup>b, c</sup>	1.1 <sup>b, c</sup>
PulseD Source-Drain Current	I <sub>SM</sub>	40	40	
Single-Pulse Avalanche Current	I <sub>AS</sub>	15		
Single-Pulse Avalanche Energy	E <sub>AS</sub>	11.2		mJ
Maximum Power Dissipation <sup>a, b</sup>	T <sub>C</sub> = 25 °C	P <sub>D</sub>	2.7	3.1
	T <sub>C</sub> = 70 °C		1.7	2.0
	T <sub>A</sub> = 25 °C		1.7 <sup>b, c</sup>	2.0 <sup>b, c</sup>
	T <sub>A</sub> = 70 °C		1.1 <sup>b, c</sup>	1.2 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

Notes:

- a. Based on T<sub>C</sub> = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Package limited.

**THERMAL RESISTANCE RATINGS**

Parameter	Symbol	Channel-1		Channel-2		Unit
		Typ.	Max.	Typ.	Max.	
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10 \text{ s}$	$R_{\text{thJA}}$	59	70	52	62.5
Maximum Junction-to-Foot (Drain)	Steady State	$R_{\text{thJF}}$	36	45	32	40

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Maximum under Steady State conditions is 120 °C/W for Channel 1 and 115 °C/W for Channel 2.

**MOSFET SPECIFICATIONS**  $T_J = 25 \text{ }^{\circ}\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	Ch-1 Ch-2	30 30		V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250 \mu\text{A}$	Ch-1		35	mV/°C
$V_{GS(\text{th})}$ Temperature Coefficient	$\Delta V_{GS(\text{th})}/T_J$		Ch-1		- 6.2	
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	Ch-1 Ch-2	1.2 1.2	2.7 2.7	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$	Ch-1 Ch-2		100 100	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	Ch-1 Ch-2		1 100	μA
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85 \text{ }^{\circ}\text{C}$	Ch-1 Ch-2		15 10000	
			Ch-1 Ch-2		15 10000	
On-State Drain Current <sup>b</sup>	$I_{D(\text{on})}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	Ch-1 Ch-2	20 20		A
Drain-Source On-State Resistance <sup>b</sup>	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$	Ch-1 Ch-2		0.0165 0.0155	Ω
		$V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$	Ch-1 Ch-2		0.0165 0.0155	
		$V_{GS} = 4.5 \text{ V}, I_D = 6 \text{ A}$	Ch-1 Ch-2		0.0215 0.020	
		$V_{GS} = 4.5 \text{ V}, I_D = 6 \text{ A}$	Ch-1 Ch-2		0.0215 0.020	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = 15 \text{ V}, I_D = 8 \text{ A}$	Ch-1 Ch-2		29 33	S
		$V_{DS} = 15 \text{ V}, I_D = 8 \text{ A}$	Ch-1 Ch-2		29 33	
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$	Ch-1 Ch-2		0.77 0.46	V
		$I_S = 1 \text{ A}, V_{GS} = 0 \text{ V}$	Ch-1 Ch-2		1.1 0.5	
<b>Dynamic<sup>a</sup></b>						
Total Gate Charge	$Q_g$	Channel-1 $V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$ Channel-2 $V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$	Ch-1 Ch-2		6.7 7.0	nC
Gate-Source Charge	$Q_{gs}$		Ch-1 Ch-2		2.8 2.8	
Gate-Drain Charge	$Q_{gd}$		Ch-1 Ch-2		2.0 2.0	
Gate Resistance	$R_g$		Ch-1 Ch-2		2.9 2.0	
					6.0 4.0	Ω

<b>MOSFET SPECIFICATIONS</b> $T_J = 25^\circ\text{C}$ , unless otherwise noted								
Parameter	Symbol	Test Conditions			Min.	Typ. <sup>a</sup>	Max.	
<b>Dynamic<sup>a</sup></b>								
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15 \text{ V}$ , $R_L = 3 \Omega$ $I_D \geq 5 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 1 \Omega$	Ch-1		9	18	ns	
			Ch-2		10	20		
	$t_r$		Ch-1		10	20		
			Ch-2		9	18		
	$t_{d(off)}$	Channel-2 $V_{DD} = 15 \text{ V}$ , $R_L = 3 \Omega$ $I_D \geq 5 \text{ A}$ , $V_{GEN} = 10 \text{ V}$ , $R_g = 1 \Omega$	Ch-1		16	32		
			Ch-2		16	32		
	$t_f$		Ch-1		9	18		
			Ch-2		8	16		
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-1		35	55	nC	
		$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-2		21	35		
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-1		40			
		$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-2		11			
Reverse Recovery Fall Time	$t_a$	$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-1		19		ns	
		$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-2		11			
Reverse Recovery Rise Time	$t_b$	$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-1		16			
		$I_F = 2.2 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$	Ch-2		10			

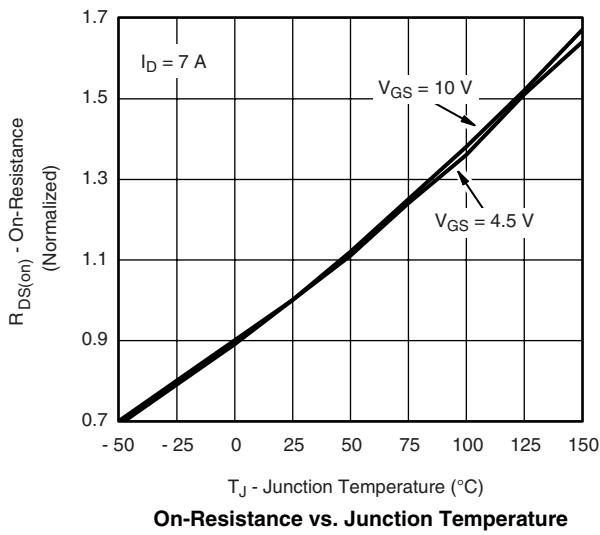
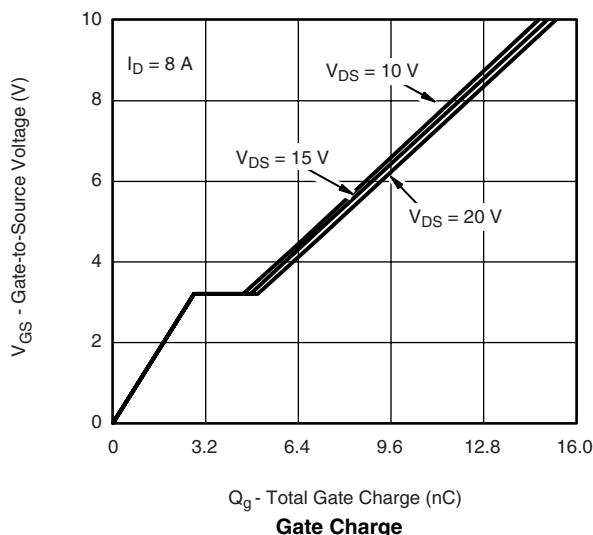
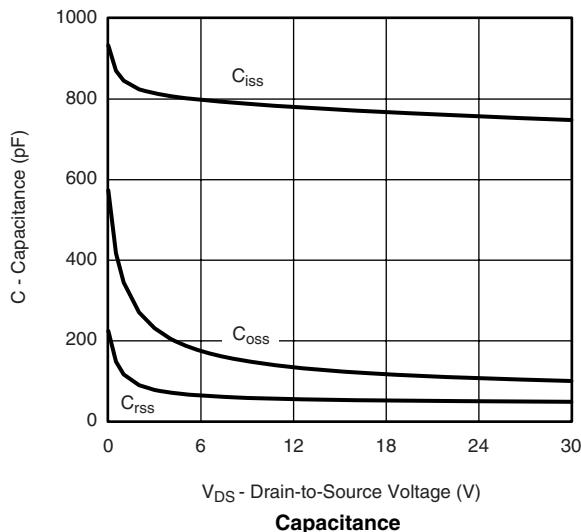
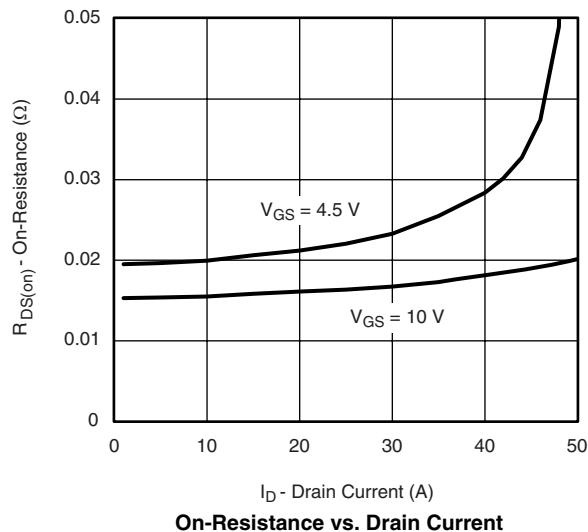
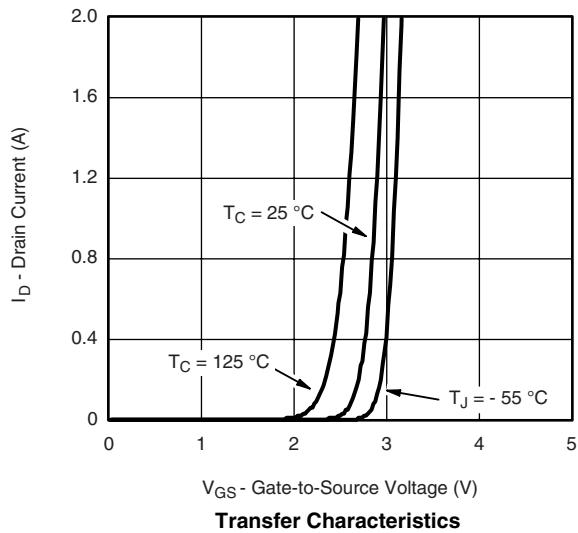
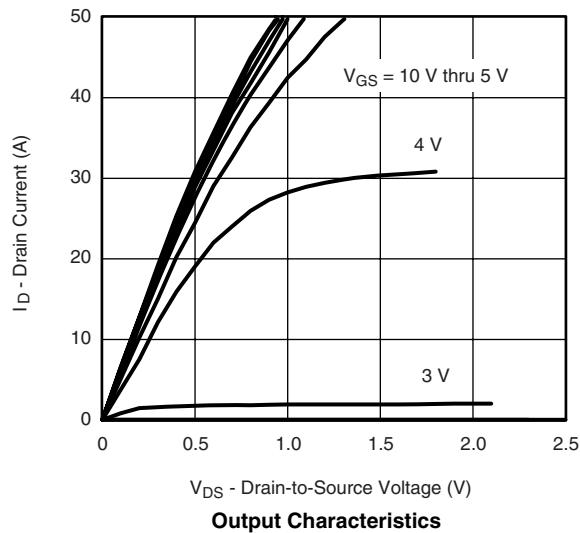
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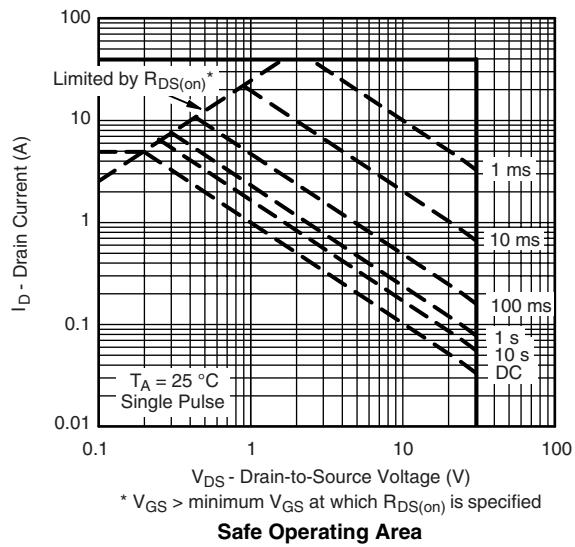
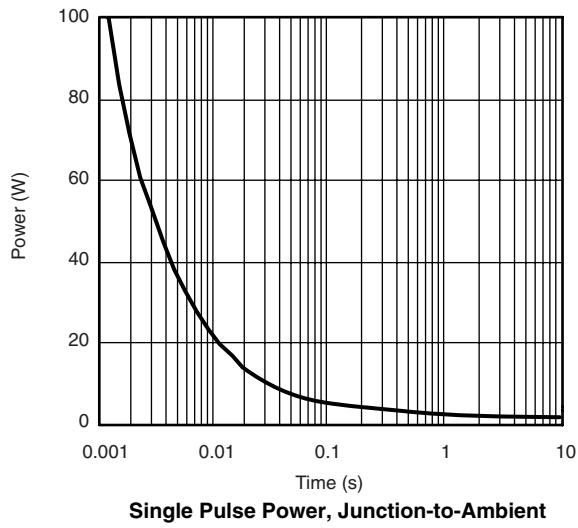
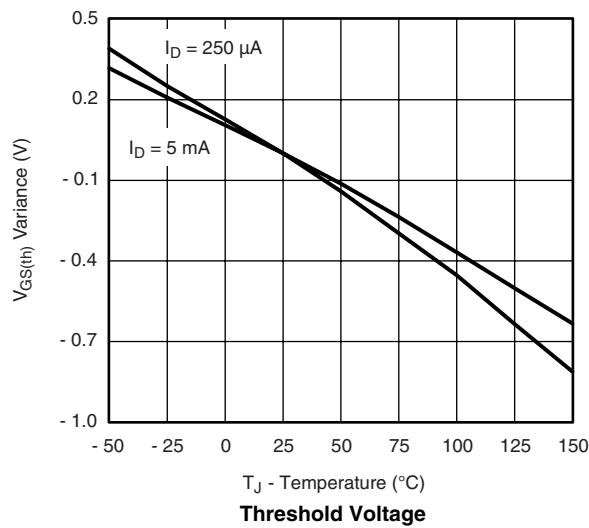
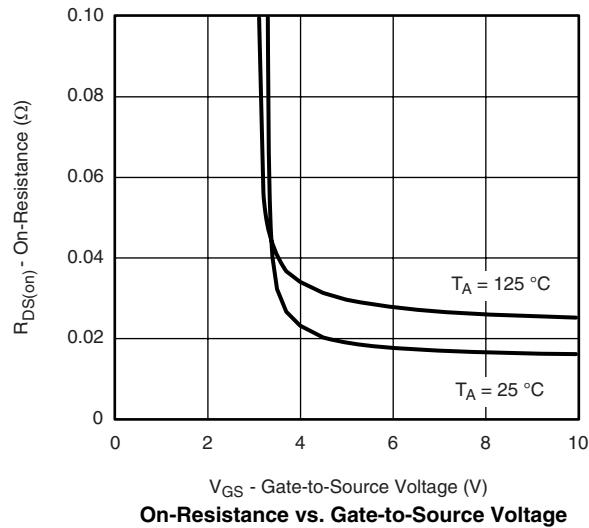
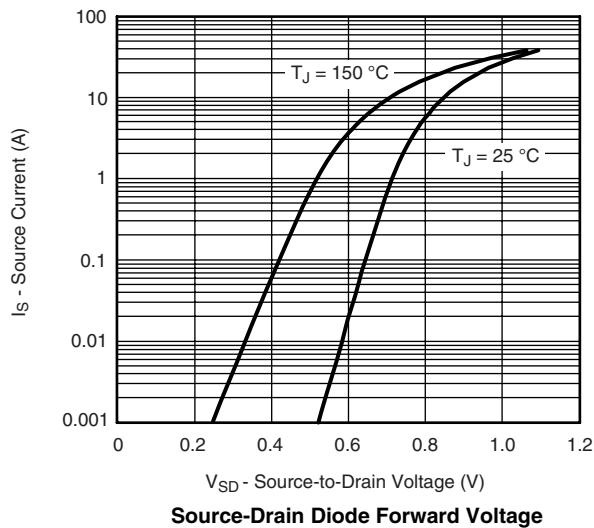
a. Guaranteed by design, not subject to production testing.

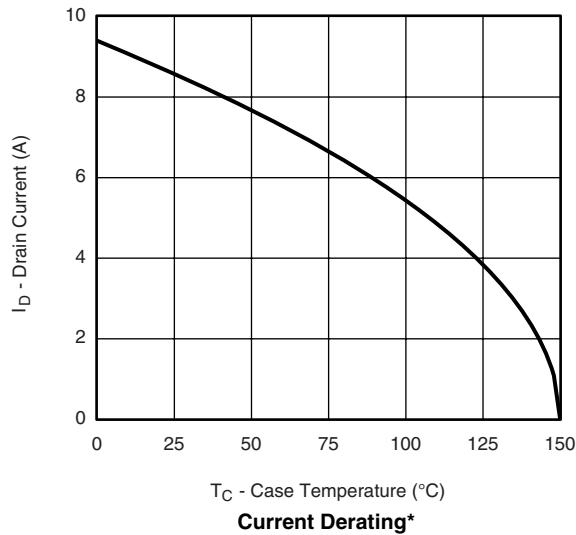
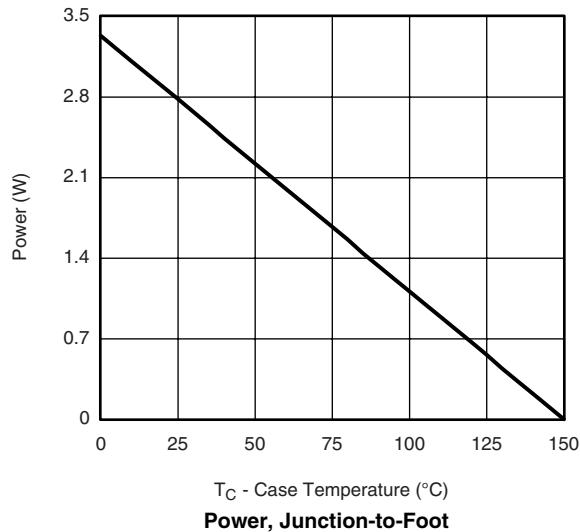
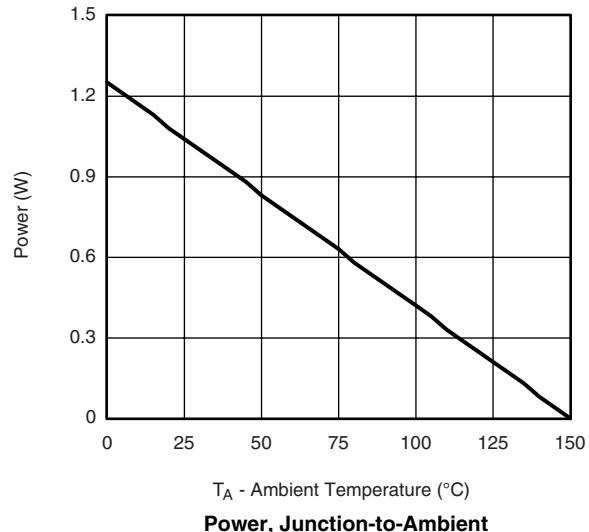
b. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2 \%$ .

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

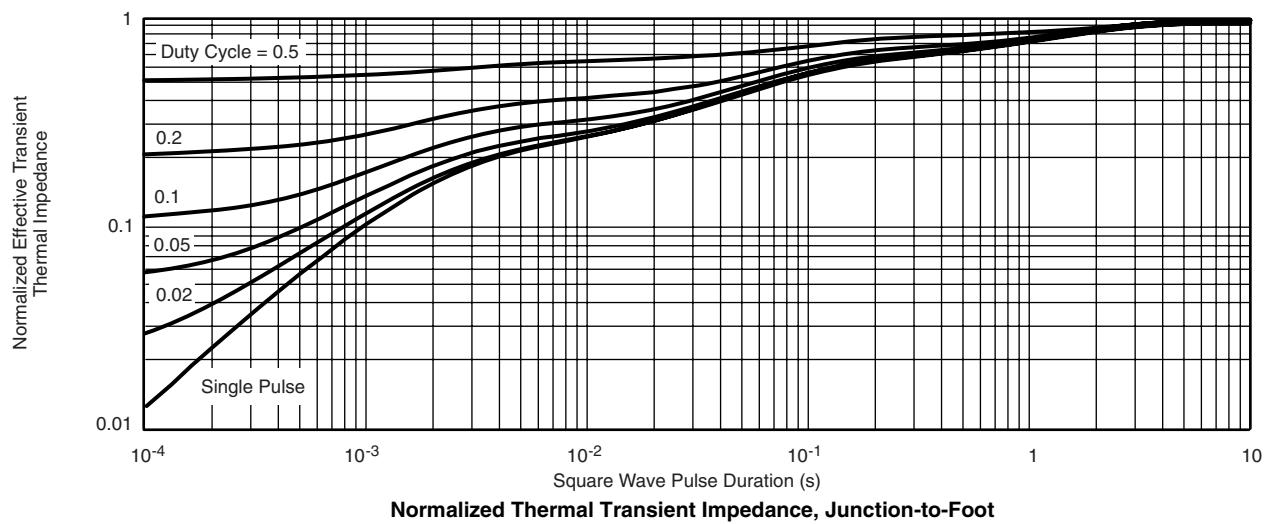
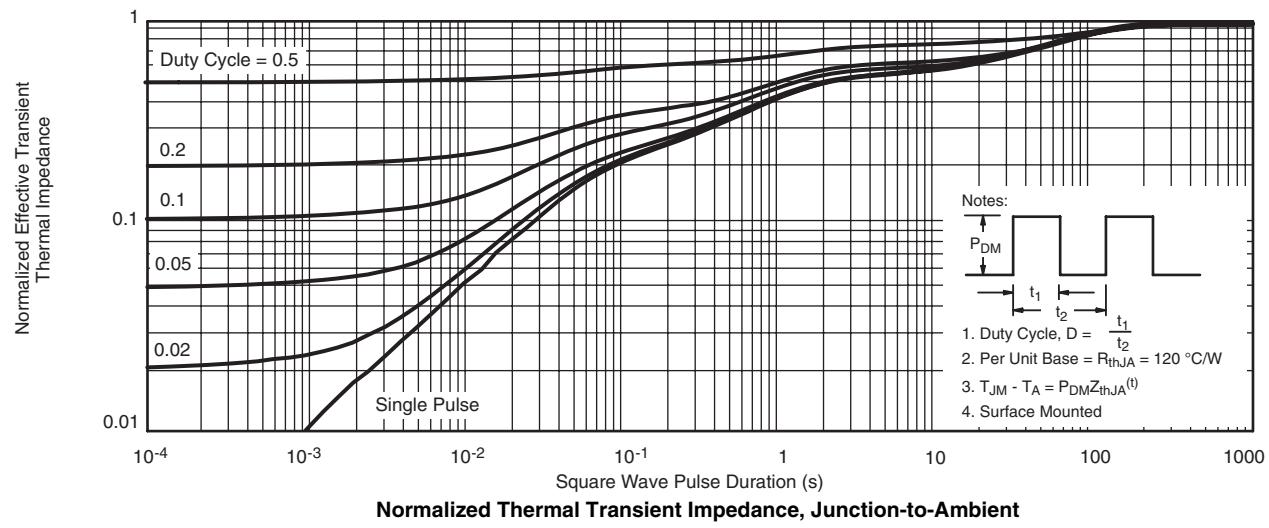
## CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



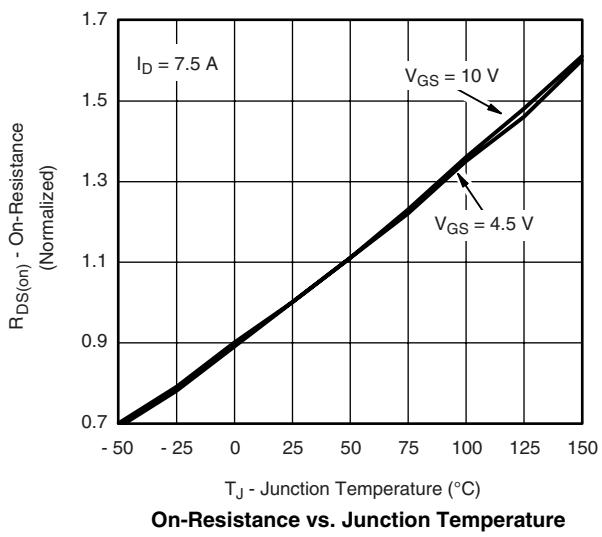
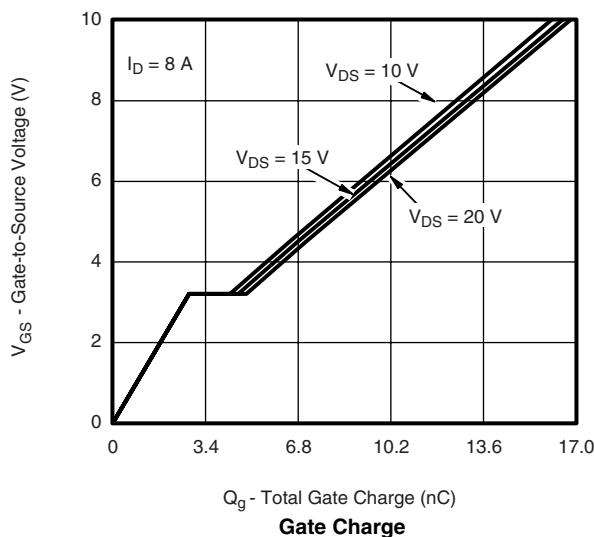
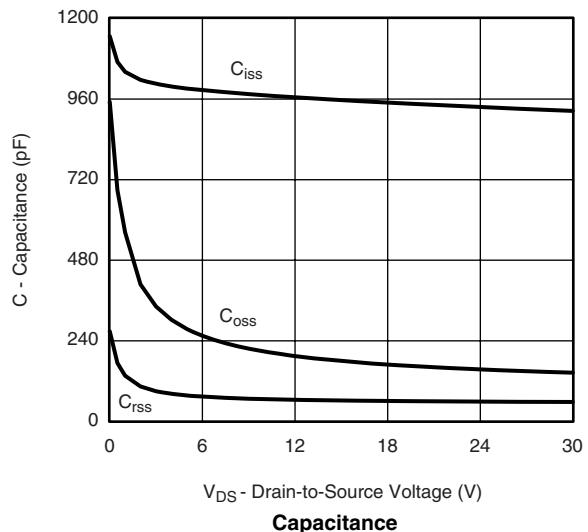
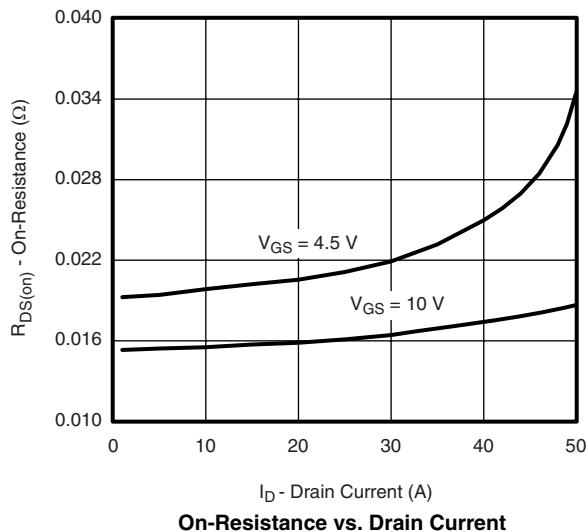
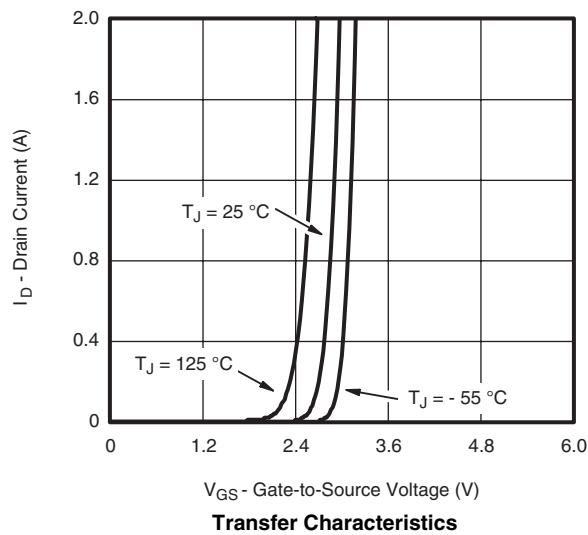
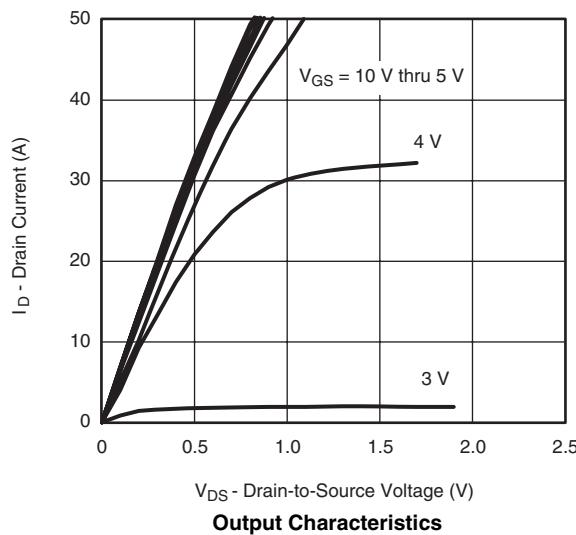
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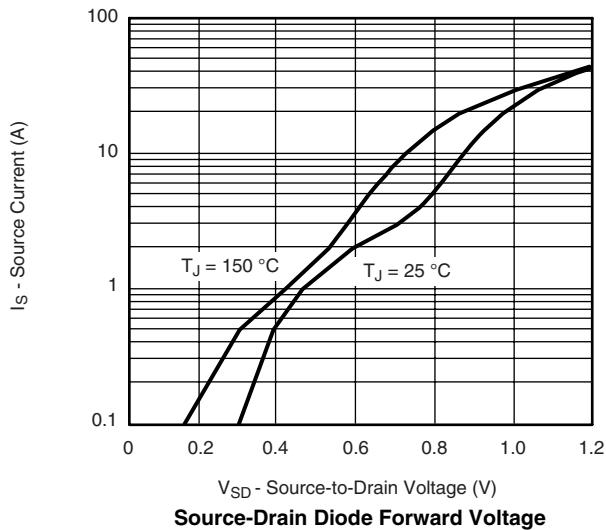
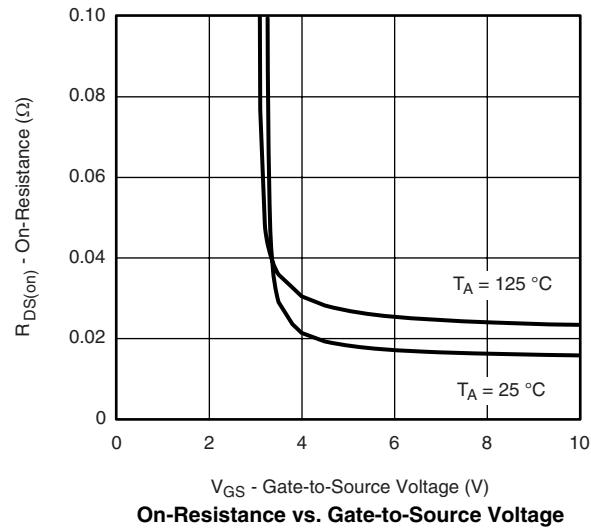
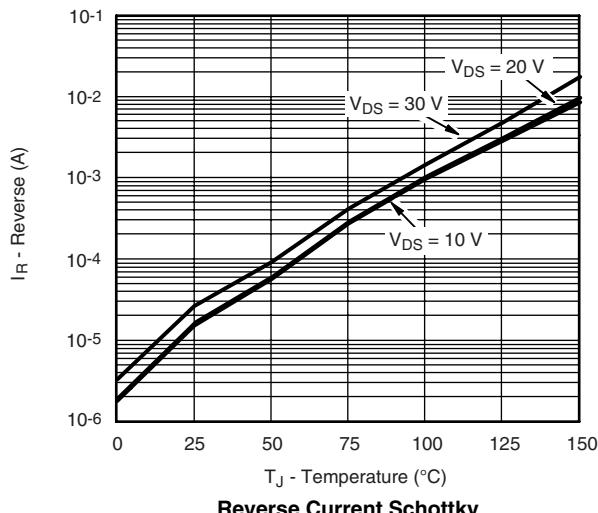
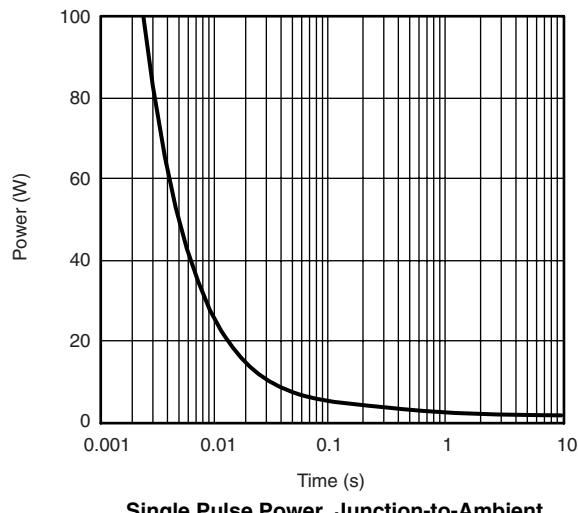
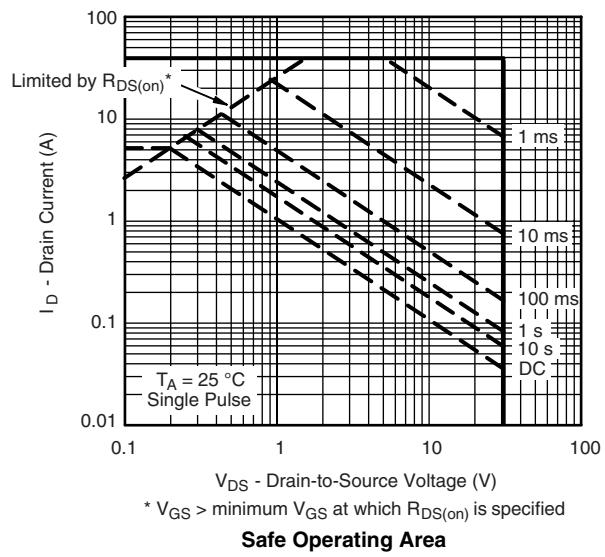
**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted $T_C$  - Case Temperature (°C)**Current Derating\*** $T_C$  - Case Temperature (°C)**Power, Junction-to-Foot** $T_A$  - Ambient Temperature (°C)**Power, Junction-to-Ambient**

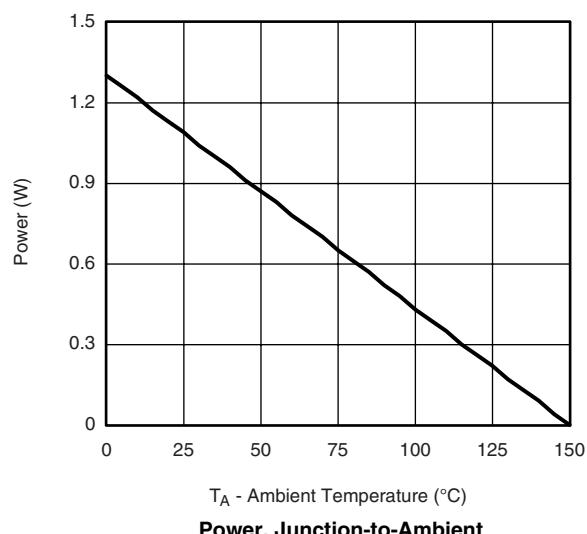
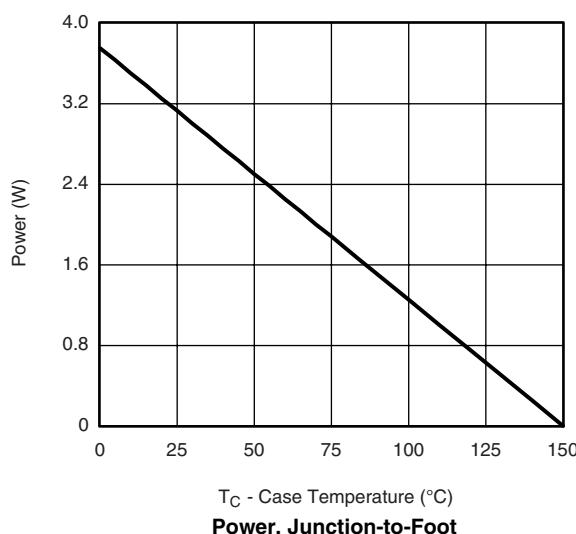
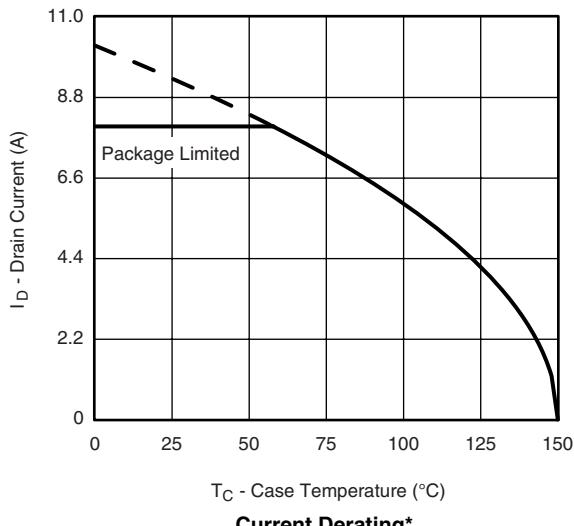
\* The power dissipation  $P_D$  is based on  $T_{J(\max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted


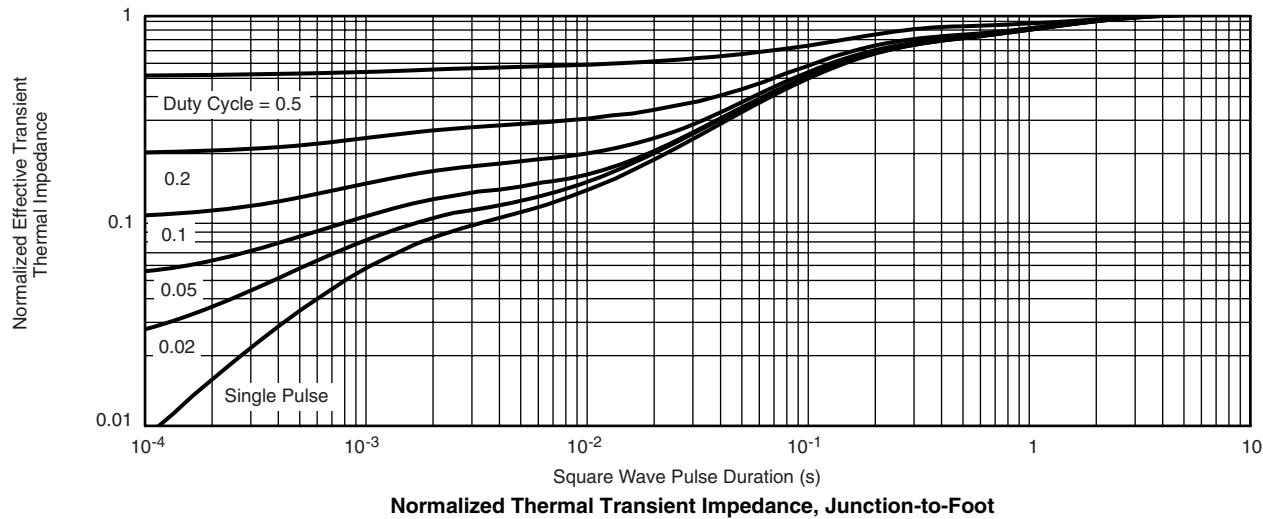
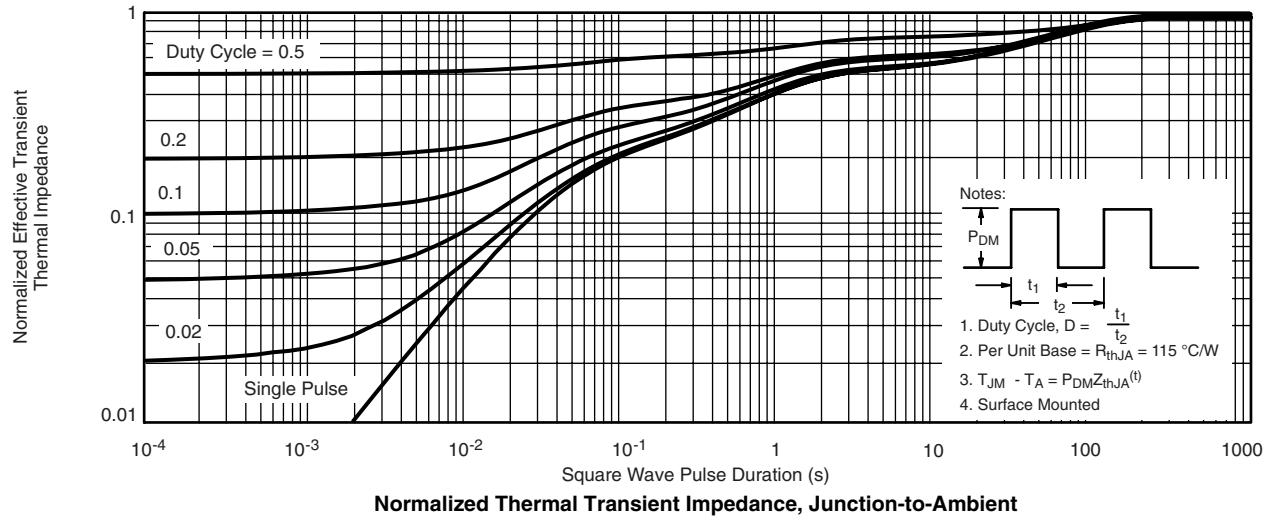
### CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Reverse Current Schottky**

**Single Pulse Power, Junction-to-Ambient**

**Safe Operating Area**
 $* V_{GS} > \text{minimum } V_{GS} \text{ at which } R_{DS(on)} \text{ is specified}$

**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

\* The power dissipation P<sub>D</sub> is based on T<sub>J(max)</sub> = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted


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