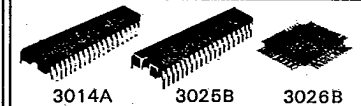


LC6502C, 6505C

C-MOS LSI

CIRCUIT DRAWING
No. 1011**SINGLE-CHIP 4-BIT MICROCOMPUTER
FOR CONTROLLER****Hardware features**

- Low power dissipation
- 5V single power supply
- 9-port/34-pin input/output
 - Input ports (2 ports/8 pins)
 - Input/output common ports (2 ports/8 pins)
 - Output ports (5 ports/18 pins)
- Clock generator (C, R or ceramic resonator attached externally)
- ROM capacity 1k bytes/2k bytes model pin compatible
- Initial reset and external interrupt input pins containing Schmitt gate
- Hold control input to minimize power dissipation by stopping internal operation

Software features

- Powerful instruction set of 80 instructions
- ROM capacity 1k bytes/2k bytes model instruction compatible
- Halt function to minimize power dissipation by stopping internal operation with HALT instruction
- 4-level subroutine nesting
- Abundant flags, working registers and manipulation instructions
- Flag 16 pcs.
- Working register 8 pcs.
- General-purpose register 1 pc.
- Interrupt function and test instruction
 - External interrupt 1
 - Internal (timer) interrupt 1
- Programmable timer (4-bit prescaler/8-bit timer)
- Basic instruction execution cycle 10μsec (clock oscillation frequency 400kHz)

ROM, RAM capacity and package

Type No.	ROM	RAM	Package	Package No.
LC6502C	2048 bytes	128 x 4 bits	DIP 42 DIP 42S DIP 64	No. 3014A No. 3024B No. 3026A
LC6505C	1024 bytes	64 x 4 bits	DIP 42 DIP 42S DIP 64	No. 3014A No. 3025B No. 3026A
LC65PG99	Externally attached 4096 bytes max	Built-in 192 x 4 bits max	QIC 42PG	(Note 1)
LC6599	Externally attached 4096 bytes max	Built-in 192 x 4 bits max	QIC 80	(Note 2)

(Notes 1) The LC65PG99 is a simulation chip for the LC6500 series.

(Notes 2) The LC6599 is an evaluation chip for the application development of the LC6500 series.