



■ FEATURES

- Output voltage: 3.3V, 5.0V, 12V, and adjustable output version
- Adjustable version output voltage range:1.23V to 18V±4%
- 150KHz±15% fixed switching frequency
- Voltage mode non-synchronous PWM control
- Thermal-shutdown and current-limit protection
- ON/OFF shutdown control input
- Operating voltage can be up to 22V
- Output load current: 3A
- Low power standby mode
- Built-in switching transistor on chip
- TO220-5L, TO263-5L and PDIP8L packages

■ APPLICATIONS

- Simple High-efficiency step-down regulator
- On-card switching regulators
- Positive to negative converter

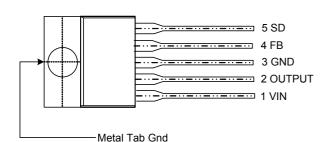
■ GENERAL DESCRIPTION

The FSP3123 series are monolithic IC that design for a step-down DC/DC converter, and own the ability of driving a 3A load without additional transistor component. Due to reducing the number of external component, the board space can be saved easily. The external shutdown function can be controlled by logic level and then come into standby mode. The internal compensation makes feedback control have good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. If current limit function occurred and V_{FB} is down to 0.5V below, the switching frequency will be reduced. The FSP3123 series operates at a switching frequency of 150 KHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Other features include a guaranteed ±4% tolerance on output voltage under specified input voltage and output load conditions, and ±15% on the oscillator frequency. The output version included fixed 3.3V. 5V, 12V, and an adjustable type. The packages are available in a standard 5-lead TO-220 package, a 5-lead TO-263 package or 8-lead PDIP.

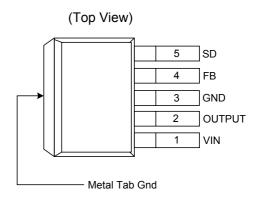
■ PIN CONFIGURATION

(1) TO220-5L

(Top View)

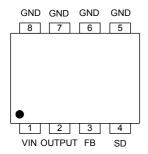


(2) TO263-5L



(3) PDIP8L

(Top View)



Symbo	Symbol		Descriptions
TO220/TO263	PDIP		
1	1	Vin	Operating Voltage Input
2	2	Output	Switching Output
3	5~8	Gnd	Ground
4	3	FB	Output Voltage Feedback Control
5	4	SD	ON/OFF Shutdown



■ ABSOLUTE MAXIMUM RATINGS

Symbol	Paramrter	Rating	Unit
V_{CC}	Supply Voltage	+24	V
V_{SD}	ON/OFF Pin input Voltage	-0.3 to +18	V
V_{FB}	Feedback Pin Voltage	-0.3 to +18	V
V_{OUT}	Output Voltage to Ground	-1	V
P_{D}	Power Dissipation	Internally Limited	W
T _{ST}	Storage Temperature	-65 to +150	${\mathbb C}$
T _{OP}	Operating Temperature	-40 to +125	${\mathbb C}$
V_{OP}	Operating Voltage	+4.5 to +22	V

■ ELECTRICAL CHARACTERISTICS (ALL OUTPUT VOLTAGE VERSIONS)

Unless otherwise specified, V_{IN} =12V for 3.3V,5V, adjustable version and V_{IN} =18V for the 12V version. I_{LOAD} =0.5A

Symbol	Para	mrter	Cond	itions	Min.	Тур.	Max.	Unit
I _B	Feedback b	ias current	V _{FB} =1.3V(Ao version only			-10	-50 -100	nA
Fosc	Oscillator fre	equency			127 110	150	173 173	KHz
F _{CSP}		it Oscillator uency	When Current Limit Occur and $V_{FB} < 0.5V, T_A$ =25°C		10	30	50	kHz
V _{SAT}	Saturation v	oltage	I _{OUT} =3A no outside o V _{FB} =0V force				1.4 1.5	V
DC	Max.Duty C Min.Duty Cy		V _{FB} =0V forc		0		100	%
	-	nt limit	Peak curren	it	3.6		6.9	Α
I _{CL}	Curre	ונ וווווונ	no outside circuit V _{FB} =0V force driver on		3.4		7.5	A
IL	Output=0	Output leakage	no outside o	circuit ce driver off			-50	μΑ
	Output=-1	current	V _{IN} =22V				-30	mA
IQ	Quiescent C	Current	V _{FB} =12V force driver off			5	10	mA
I _{STBY}	Standby Qu Current	iesient	ON/OFF Pin=5V V _{IN} =22V			70	200 250	μΑ
V _{IL}		logic input	Low(regu	ltaor ON)		1.3	0.6	V
V_{IH}	threshold vo	oltage	High(regu	Itaor OFF)	2.0	1.0		,
I _H	ON/OFF pin current		V _{LOGIC} =2.	.5V(OFF)			-15	μA
IL	ON/OFF pin current	input	V _{LOGIC} =0.5V(ON)				-5	μΑ
θ _{JC}	Thermal Re	sistence	TO220-5L TO263-5L PDIP8L Junction to case			2.5 3.5 12		°C/W
θ_{JA}	Thermal Re with Copper Approximate	Area of	TO220-5L TO263-5L PDIP8L	Junction to ambient		28 23 35		°C/W

Specifications with **boldface type** are for full operating temperature range, the other type are for TJ=25°C.

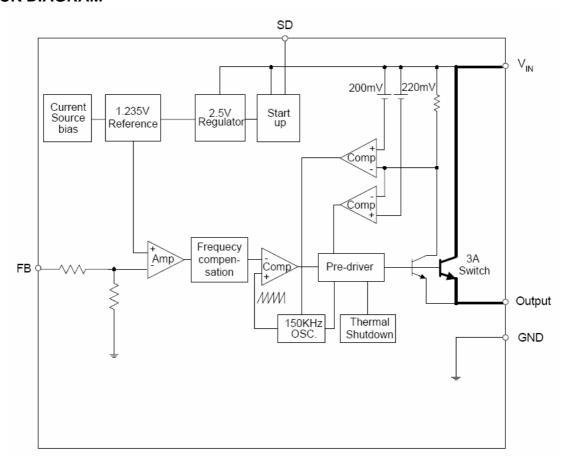


■ ELECTRICAL CHARACTERISTICS (CONTINUED)

	Symbol	Parameter	Conditions	Тур.	Limit	Unit
FSP3123-ADJ	V_{FB}	Output Feedback		1.23	1.193/ 1.18 1.267/ 1.28	V V _{MIN} V _{MAX}
	η	Efficiency	$V_{IN} = 12V$, $I_{LOAD} = 3A$	75		%
FSP3123-3.3V	V _{OUT}	Output voltage	5.5V≤V _{IN} ≤22V 0.2A≤I _{LOAD} ≤3A	3.3	3.168/ 3.135 3.432/ 3.465	V V _{MIN} V _{MAX}
	η	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		%		
FSP3123-5.0V	V _{OUT}	Output voltage	8V≤V _{IN} ≤22V	5		V V _{MIN} V _{MAX}
	η	Efficiency	$V_{IN} = 12V$, $I_{LOAD} = 3A$	80		%
FSP3123-12V	V _{OUT}	Output voltage	15V≤V _{IN} ≤22V	12	11.52/ 11.4 12.48/ 12.6	$egin{array}{c} egin{array}{c} \egin{array}{c} \egin{array}{c} \egin{array}{c} \egin{array}$
	η	Efficiency	V_{IN} =15V, I_{LOAD} =3A	90		%

Specifications with **boldface type** are for full operating temperature range, the other type are for TJ=25°C.

■ BLOCK DIAGRAM







■ FUNCTION DESCRIPTION

Pin Function

 V_{IN}

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be present at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

Ground

Circuit ground.

Output

Internal switch. The voltage at this pin switches between $(V_{IN} - V_{SAT})$ and approximately -0.5V, with a duty cycle of approximately V_{OUT} / V_{IN} . To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be kept a minimum.

Feedback

Senses the regulated output voltage to complete the feedback loop.

ON/OFF

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 150uA. Pulling this pin below a threshold voltage of approximately 1.3V turns the regulator on, and pulling this pin above 1.3V (up to a maximum of 18V) shuts the regulator down. If this shutdown feature is not needed, the ON/OFF pin can be wired to the ground pin.

Thermal Considerations

The FSP3123 is available in three packages, a 5-pin TO-220, 5-pin TO263 and a 8-pin PDIP Package. The TO-220 package needs a heat sink under most conditions. The size of the heat sink depends on the input voltage, the output voltage, the load current and the ambient temperature. The FSP3123 junction temperature rises above ambient temperature for a 3A load and different input and output voltages. The data for these curves was taken with the FSP3123 (TO-220 package) operating as a buck switching regulator in an ambient temperature of 25°C (still air). These temperature rise numbers are all approximate and there are many factors that can affect these temperatures. Higher ambient temperatures require more heat sinking.

The TO-263 surface mount package tab is designed to be soldered to the copper on a printed circuit board. The copper and the board are the heat sink for this package and the other heat producing components, such as the catch diode and inductor. The PC board copper area that the package is soldered to should be at least 0.4 in², and ideally should have 2 or more square inches of 2 oz. Additional copper area improves the thermal characteristics, but with copper areas greater than approximately 6 in², only small improvements in heat dissipation are realized. If further thermal improvements are needed, double sided, multilayer PC board with large copper areas and/or airflow are recommended.

The FSP3123 (TO-263 package) junction temperature rise above ambient temperature with a 3A load for various input and output voltages. This data was taken with the circuit operating as a buck switching regulator with all components mounted on a PC board to simulate the junction temperature under actual operating conditions. This curve can be used for a quick check for the approximate junction temperature for various conditions, but be aware that there are many factors that can affect the junction temperature. When load currents higher than 2A are used, double sided or multilayer PC boards with large copper areas and/or airflow might be needed, especially for high ambient temperatures and high output voltages.

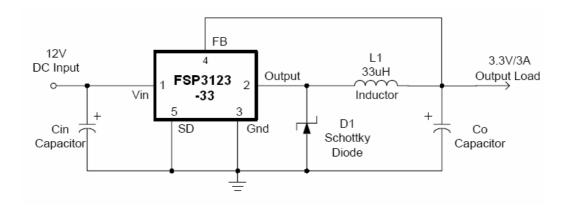
For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper should be used in the board layout. (Once exception to this is the output (switch) pin, which should not have large areas of copper.) Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding air, and moving air lowers the thermal resistance even further.

Package thermal resistance and junction temperature rise numbers are all approximate, and there are many factors that will affect these numbers. Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multilayer board and the amount of solder on the board. The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving. Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.

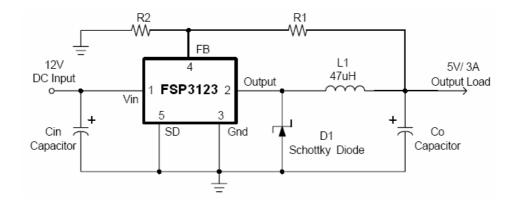


■ TYPICAL APPLICATION CIRCUITS

(1) Fixed Output Circuit



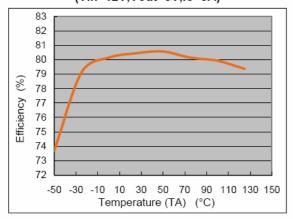
(2) Adjustable Output Circuit



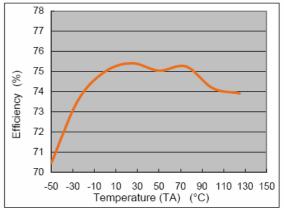


■ TYPICAL PERFORMANCE CHARACTERISTICS

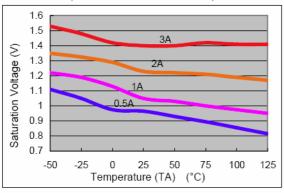
FSP3123 Efficiency v.s. Temperature (Vin=12V,Vout=5V,Io=3A)



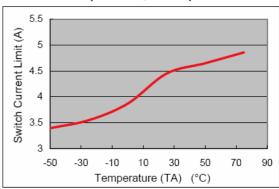
FSP3123 Efficiency v.s. Temperature (Vin=12V,Vout=3.3V,Io=3A)



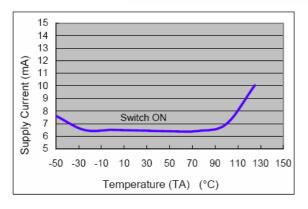
FSP3123Saturation Voltage v.s. Temperature (Vcc=12V,Vfb=0V,VSD=0)

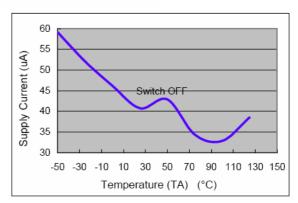


FSP3123 Switch Current Limit v.s. Temperature (Vcc=12V,Vfb=0V)



FSP3123 Supply Current v.s. Temperature (Vcc=12V, No Load, Von/off =0V(Switch ON), Von/off =5V(Switch OFF))

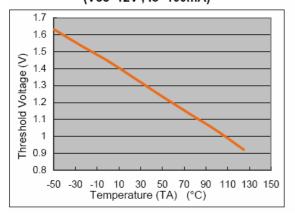




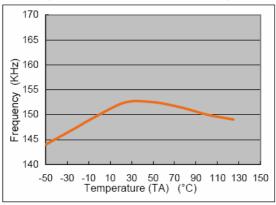


■ TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

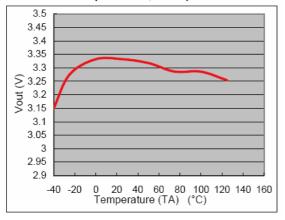
FSP3123Threshold Voltage v.s. Temperature (Vcc=12V, Io=100mA)



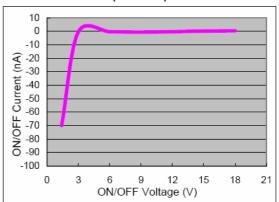
FSP3123 Frequency v.s. Temperature (Vcc=12V, Io=500mA, Vout=5V)



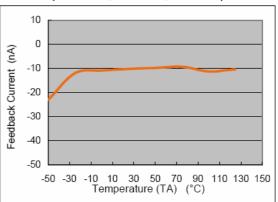
FSP3123Output Voltage v.s. Temperature (Vin=12V ,lo=3A)



FSP3123ON/OFF Current v.s. ON/OFF Voltage (Vin=12V)



FSP3123 Feedback Current v.s. Temperature (Vcc=12V , Vout=5V,Vfb=1.3V)





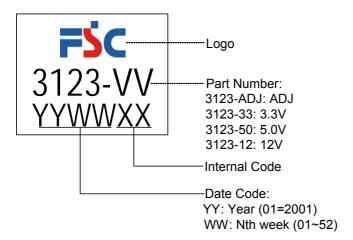


ORDERING INFORMATION

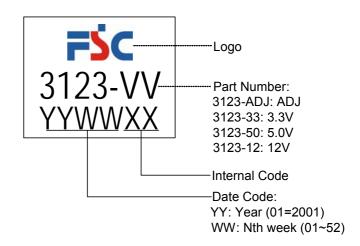
Package: Output Voltage: Packing: Temperature Grade:
T: TO220-5L Blank: ADJ Blank: Tube or Bulk E: -40~125℃
K: TO263-5L 33: 3.3V A: Tape & Reel
N: PDIP8L 50: 5.0V
12: 12V

■ MARKING INFORMATION

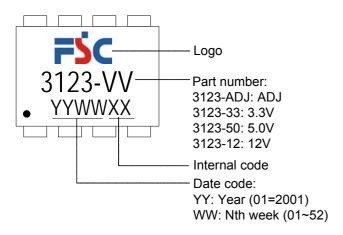
1) TO220-5L



2) TO263-5L



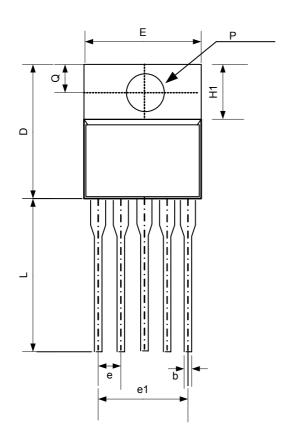
(3) PDIP8L

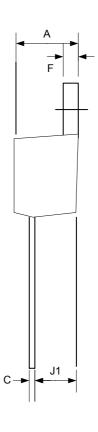




■ PACKAGE INFORMATION

1) TO220-5L





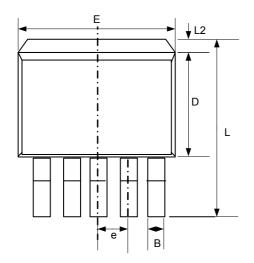
Symbol	Dime	nsions In Millim	eters	Dimensions In Inches		
Symbol	Min.	Nom.	Max.	Min.	Nom.	Max.
Α	4.07	4.45	4.82	0.160	0.175	0.190
b	0.76	0.89	1.02	0.030	0.035	0.040
С	0.36	0.50	0.64	0.014	0.020	0.025
D	14.22	14.86	15.50	0.560	0.585	0.610
E	9.78	10.16	10.54	0.385	0.400	0.415
е	1.57	1.71	1.85	0.062	0.067	0.073
e1	6.68	6.81	6.93	0.263	0.268	0.273
F	1.14	1.27	1.40	0.045	0.050	0.055
H1	5.46	6.16	6.86	0.215	0.243	0.270
J1	2.29	2.74	3.18	0.090	0.108	0.125
L	13.21	13.97	14.73	0.520	0.550	0.580
Р	3.68	3.81	3.94	0.145	0.150	0.155
Q	2.54	2.73	2.92	0.100	0.107	0.115

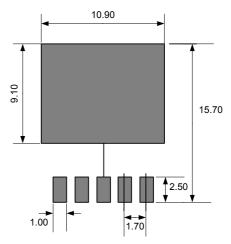




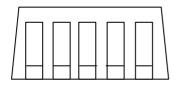
2) TO263-5L

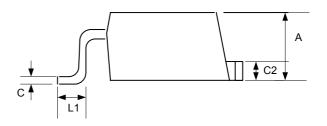
FSP3123





Land Pattern Recommendation (Unit: mm)





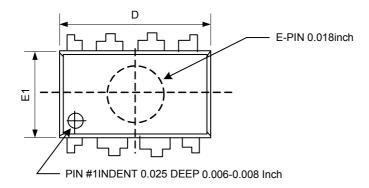
Symbol	Dime	nsions In Millim	eters	Dimensions In Inches		
Symbol	Min.	Nom.	Max.	Min.	Nom.	Max.
Α	4.07	4.46	4.85	0.163	0.176	0.194
В	0.51	0.84	1.02	0.020	0.033	0.041
С	0.36	0.50	0.74	0.014	0.020	0.030
C2	1.14	1.27	1.65	0.046	0.050	0.066
D	8.20	9.15	9.65	0.328	0.360	0.380
Е	9.65	10.16	10.67	0.386	0.400	0.427
е	1.57	1.71	1.85	0.063	0.068	0.074
L	14.45	15.24	15.88	0.578	0.600	0.635
L1	1.78	2.54	2.79	0.071	0.100	0.110
L2			2.92			0.115

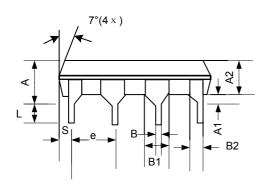


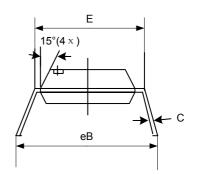


(3) PDIP8L

FSP3123







Cymphol	Dim	ensions In Millim	eters	Dimensions In Inches		
Symbol	Min.	Nom.	Max.	Min.	Nom.	Max.
Α			5.33			0.210
A1	0.38			0.015		
A2	3.1	3.30	3.5	0.122	0.130	0.138
В	0.36	0.46	0.56	0.014	0.018	0.022
B1	1.4	1.52	1.65	0.055	0.060	0.065
B2	0.81	0.99	1.14	0.032	0.039	0.045
С	0.20	0.25	0.36	0.008	0.010	0.014
D	9.02	9.27	9.53	0.335	0.365	0.375
E	7.62	7.94	8.26	0.300	0.313	0.325
E1	6.15	6.35	6.55	0.242	0.250	0.258
е		2.54			0.100	
L	2.92	3.3	3.81	0.115	0.130	0.150
eB	8.38	8.89	9.40	0.330	0.350	0.370
S	0.71	0.84	0.97	0.028	0.033	0.038