Silicon N-Channel MOS FET

HITACHI

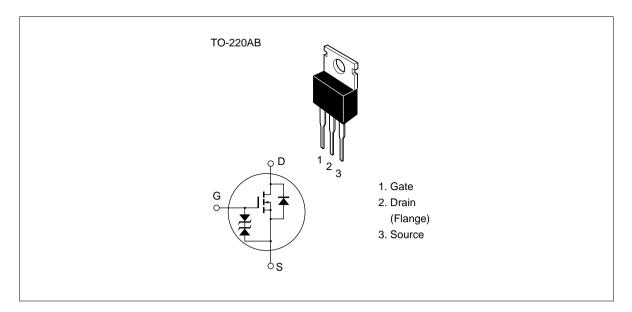
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switchingregulator, DC-DC converter

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	900	V	
Gate to source voltage	V _{GSS}	±30	V	
Drain current	I _D	4	А	
Drain peak current	I*1	10	А	
Body to drain diode reverse drain current	I _{DR}	4	A	
Channel dissipation	Pch*2	60	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	۵°C	

www.DataSitesi40.comW \leq 10 µs, duty cycle \leq 1 %

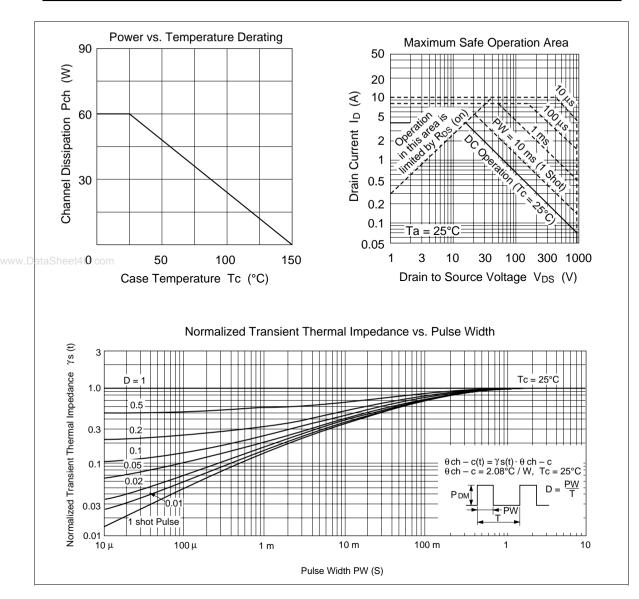
2. Value at Tc = 25 °C

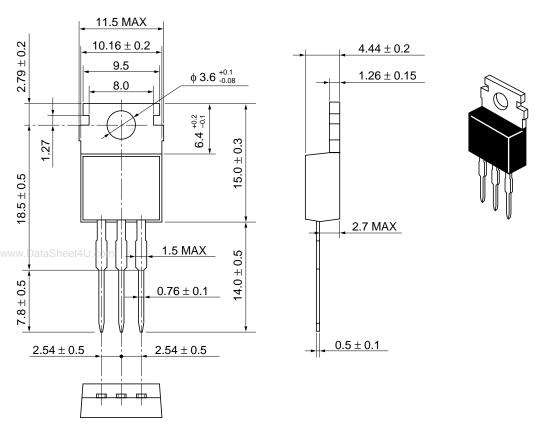
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{\rm (BR)DSS}$	900	—	—	V	$I_{\rm D}$ = 10 mA, $V_{\rm GS}$ = 0
Gate to source breakdown voltage	$V_{\rm (BR)GSS}$	±30	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_		±10	μA	$V_{GS} = \pm 25 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	250	μΑ	V _{DS} = 720 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	_	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$		3.0	4.0	Ω	$I_{\rm D} = 2 \text{ A}$ $V_{\rm GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	1.7	2.7	—	S	$I_{\rm D} = 2 \text{ A}$ $V_{\rm DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss		740	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	305		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	150	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	15	_	ns	I _D = 2 A
Rise time	t,		60	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	100	_	ns	R _L = 15 Ω
Fall time	t _f	_	80	_	ns	
Body to drain diode forward voltage	V_{DF}	—	0.9	—	V	$I_{F} = 4 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	—	800	—	ns	$I_F = 4 \text{ A}, V_{GS} = 0,$ $di_F / dt = 100 \text{ A} / \mu \text{s}$
voltage Body to drain diode reverse		_		_		$I_{\rm F} = 4 {\rm A}, {\rm V}_{\rm GS} = 0,$

Note 1. Pulse Test

See characteristic curves of 2SK1340





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JEDEC	Conforms		
EIAJ	Conforms		
Weight (reference value)	1.8 g		

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