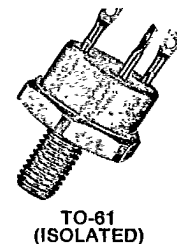


ISOLATED

10 AMP/NPN-PNP

Pirgo Silicon Planar Power Transistors

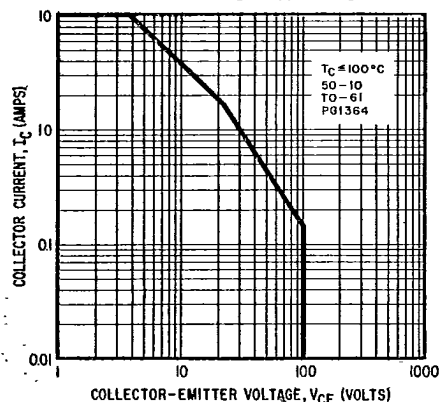
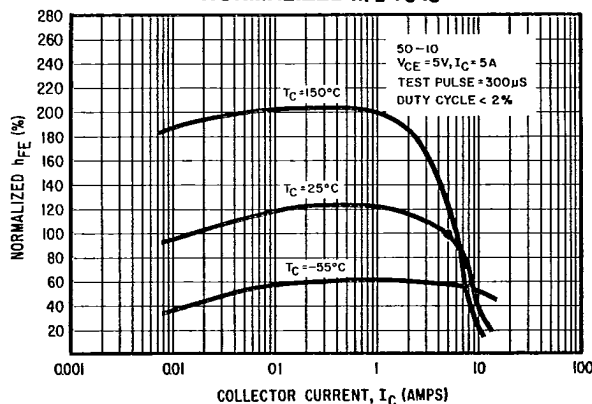
10 AMP
100 VOLT
40 WATT
60 MHz
TO-61
(ISOLATED)



- Linear h_{FE} from 50 mA to 10 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 60$ MHz (typical)
- High voltage, $BV_{CEO(sus)}$ to 100 volts

NPN Type	Package	BV_{CBO}	$BV_{CEO(sus)}$	BV_{EBO}	h_{FE}		h_{FE} @ I_{CA}	h_{FE}		$V_{CE(sat)}$ Max @ I_{CA}	C_{obMax} pf	P_{DW} @ 100°C	PNP Complement
					Min	Max		Min	@ I_{CA}				
PG1360	TO-61	80	50	8	20	60	5	10	10	2.5	10	350	PG2360
PG1361	TO-61	80	50	8	40	120	5	15	10	1.5	10	350	PG2361
PG1362	TO-61	120	70	8	20	60	5	10	10	2.5	10	350	PG2362
PG1363	TO-61	120	70	8	40	120	5	15	10	1.5	10	350	PG2363
PG1364	TO-61	120	100	8	20	60	5	10	10	2.5	10	350	PG2364
PG1365	TO-61	120	100	8	40	120	5	15	10	1.5	10	350	PG2365
PG1366	TO-61	120	100	8	100	300	5	20	10	1.5	10	350	PG2366
PG1367	TO-61	70	50	6	20	60	5			.6	5	350	PG2367
PG1368	TO-61	80	70	6	20	60	5			.6	5	350	PG2368
PG1369	TO-61	120	100	6	20	60	5			.6	5	350	PG2369
PG1370	TO-61	70	50	6	40	120	5			.6	5	350	PG2370
PG1371	TO-61	80	70	6	40	120	5			.6	5	350	PG2371
PG1372	TO-61	120	100	6	40	120	5			.6	5	350	PG2372
PG1373	TO-61	70	50	6	100	300	5			.6	5	350	PG2373
PG1374	TO-61	80	70	6	100	300	5			.6	5	350	PG2374
PG1375	TO-61	120	100	6	100	300	5			.6	5	350	PG2375
2N5317	TO-61	80	80	6	30	90	5	10	10	5.0	10	500	2N5316
2N5319	TO-61	100	100	6	30	90	5	10	10	5.0	10	500	2N5318

MAXIMUM SAFE OPERATING AREA

NORMALIZED h_{FE} Vs I_C 

Gold eutectic bonding
of dice to header

Ultra-sonic bonding.

Final assembly including environmentally
controlled encapsulation of devices.