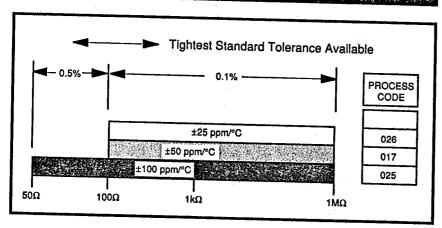
The CLA and CLB resistor arrays are the hybrid equivalent to the eight resistor common connection and isolated networks available in sips or dips. The resistors are spaced on 10 mil centers resulting in minimal space requirements.

These chips are manufactured using state-of-the-art thin-film techniques, are 100% electrically tested and visually inspected to MIL-STD-883.

- Eight equal value resistors on a 60 x 90 mil chip
- **Excellent TCR tracking**
- Resistance range 50Ω to $1 M\Omega$
- Resistor material tantalum nitride, self-passivating
- Oxidized silicon substrate for good power dissipation
- Low cost per resistor
- Shortened hybrid assembly times
- Reduced hybrid size

TCR VALUES AND TOLERANCES



ELECTRICAL CHARACTERISTICS

TCR tracking spread

Noise, MIL-STD-202, Method 308 $100\Omega - 250 \text{ k}\Omega$ $<100\Omega$ or $>251 k\Omega$

Moisture resistance, MIL-STD-202, Method 106

Stability, 1000 hr., +125 °C, 25 mw Absolute Ratio

Operating temperature range

Thermal shock, MIL-STD-202, Method 107, Test Condition F

High temperature exposure, +150 °C, 100 hr.

Dielectric voltage breakdown

Insulation resistance

Operating voltage

DC power rating at +70 °C, (derated to zero at +175 °C)

5 x rated power short-time overload, +25 °C, 5 seconds ±5 ppm/°C

-35 dB max. -20 dB max.

±0.5% max. △R/R

 $\pm 0.5\%$ max. $\Delta R/R$ ±0.1% max. ΔR/R

-55 °C to +125 °C

 $\pm 0.1\%$ max. $\Delta R/R$

±0.2% max. ΔR/R

400 V

 $10^{12}\Omega$ min.

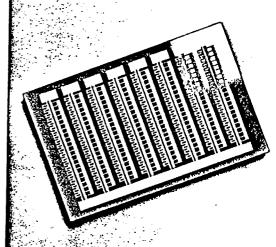
100 V max.

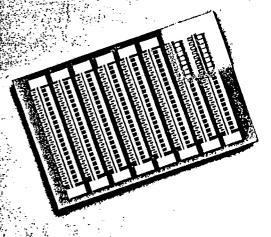
50 mw per resistor

±0.1% max. ΔR/R



EIGHT-RESISTOR ARRAYS





Semi 🗐 Films Division

P.O. Box 188 West Hurley, NY 12491 Tel. (914) 338-7714 Fax (914) 338-6329



MECHANICAL D

Chip size 60 ± 2 mil x 90 ± 2 mil (1.50 ±0.05 mm x 2.26 ±0.05 mm)

Chip thickness 8 ±3mil (0.203 ±0.08 mm)

Chip substrate material Oxidized silicon, 10 kÅ min. SiO₂

Tantalum nitride, (self-passivating) Resistor material

Bonding pads $4 \times 7 \text{ mil } (0.10 \times 0.178 \text{ mm})$

No. of top pads, CLA 16 9

Pad material

CLB

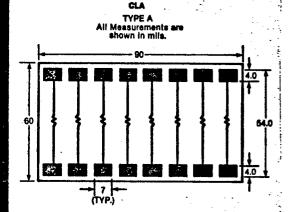
10 kÅ min. aluminum

Backing None, lapped semiconductor silicon

OPTIONS: Gold backing for eutectic die attach

These chips can be provided in 2 to 12 resistor arrays

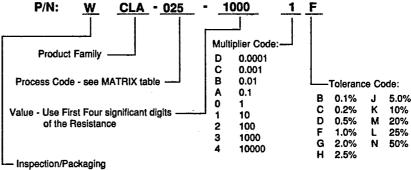
at proportionately changed sizes.



The CLA and CLB thin-film resistor arrays are designed for hybrid packages requiring up to eight resistors of the same resistance value and tolerance, as well as excellent TCR tracking. For such hybrids they afford great savings in cost and space.

NON BIRKED BY (HOW

Example: 100% visualled, $10k\Omega \pm 1\%$, ± 100 ppm TCR, CLA Format



Use - W for 100% visually inspected parts, per MIL-STD-883 X for sample, visually inspected loaded in matrix trays (4% AQL) Y for sample, visually inspected die loaded in vials (4% AQL)

