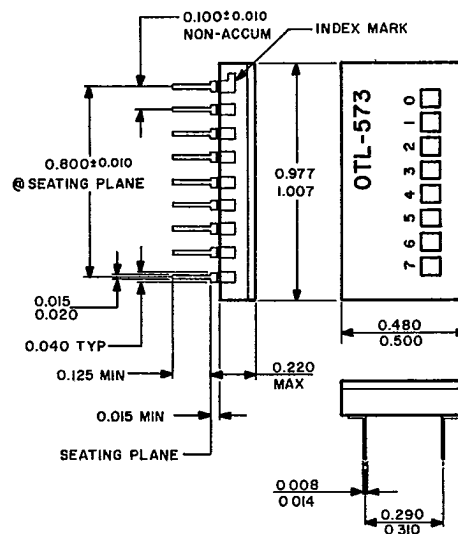
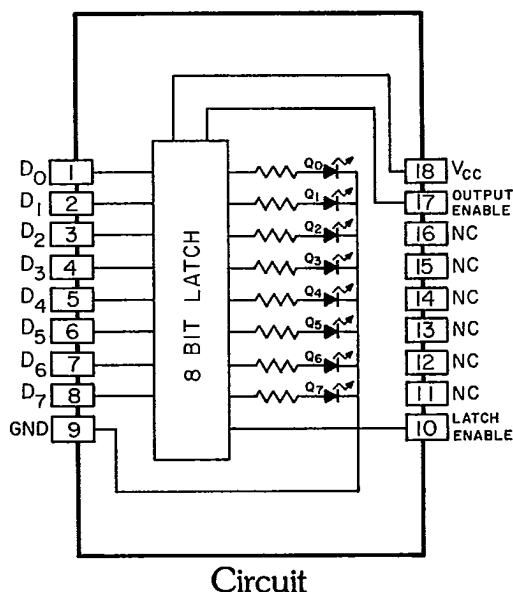


OPTOTEK**OTL-573****8 BIT DIGITAL STATUS INDICATOR**

An 8 element LED Display with integral drive electronics to provide direct indication of logic states.



- On board C-MOS OCTAL D-type latch can be used in "capture" mode or "transparent" mode to freeze or continuously monitor states.
- No external current limiting resistors or additional components required.
- Ideal for both troubleshooting applications and permanent PCB installations.
- Ceramic Dual-in-line packaging.
- Standard LED color is red.
- Green or yellow available on special order.

Truth Table			
Output Enable	Latch Enable	D _n	Q _n (LED)
L	H	H	ON
L	H	L	OFF
L	L	X	Previous State
H	X	X	OFF

H — High logic level
L — Low logic level
X — Don't care

8 BIT DIGITAL STATUS INDICATOR

Absolute Maximum Ratings

Voltage at any pin	-0.3V to $V_{CC} + 0.3V$
Operating Temperature Range	-40°C to 71°C
Storage Temperature Range	-40°C to 71°C
Package Dissipation (All LED's on)	500mW
Nominal V_{CC}	5.0V $\pm$ 0.5V
Absolute Maximum V_{CC}	6.0V
Lead Temperature (Soldering, 10 sec.)	300°C

DC Electrical Characteristic

Max/Min. limits apply across temperature range, unless otherwise noted.

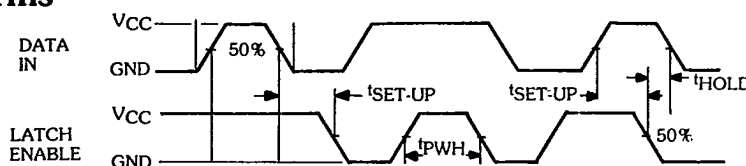
Parameter	Min.	Typ.	Max.	Units
$V_{IN(1)}$ Logical "1" Input Voltage	2.0			V
$V_{IN(0)}$ Logical "0" Input Voltage			0.8	V
$I_{IN(1)}$ Logical "1" Input Current			1.0	μA
$I_{IN(0)}$ Logical "0" Input Current			-1.0	μA
I_{CC} Supply Current (All LED's on)		40	75	mA

AC Electrical Characteristics

$T_A = 25^\circ C$, $C_L = 50pF$, $t_r = t_f = 6ns$, unless otherwise noted.

Parameter	Conditions	Typ.	Max.	Units
t_{SET-UP} Set-Up Time Data in to CLOCK/LATCH ENABLE	$V_{CC} = 5.0V$	10	25	ns
t_{HOLD} Hold Time DATA in to CLOCK/LATCH ENABLE	$V_{CC} = 5.0V$	10	25	ns
f_{MAX} LATCH ENABLE Frequency	$V_{CC} = 5.0V$		10	MHz
t_{PWH} LATCH ENABLE Pulse Width	$V_{CC} = 5.0V$	15	30	ns

Switching Time Waveforms



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