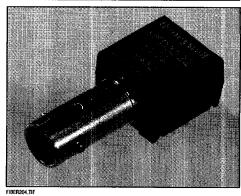
125 MHz PIN Plus Preamplified Analog Receiver

FEATURES

- Industry standard metal ST® barrel
- High-speed operation, Rise/Fall times are 3.5 ns typical
- Low pulse width distortion over a wide range of inputs because of 23 dB typical dynamic range
- Wide variety of cable options, operates with 50/125, 62.5/125, and 100/140 µm cables
- Popular Fiber DIP package
- The ST* fiber connector is electrically connected to the shield pins.



OUTLINE DIMENSIONS in inches (mm)

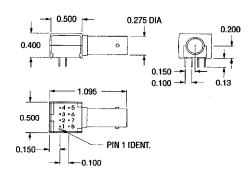
DESCRIPTION

The HFD3222 is designed for use in Ethernet/Token-Ring LAN applications such as Repeaters, Bridges, Hubs, Routers, Switches and Gateways. The inexpensive, high speed, analog fiber optic receiver is intended for use in local area networks (LANs) where data rates up to 125 Mbits/second are needed. The HFD3222 may be used as a low cost alternative to 1300 nm components. The hybrid bipolar fiber optic receiver contains a silicon PIN photodiode for high speed operation and a preamplifier integrated circuit for excellent noise immunity.

The HFD3222s preamplifier stage converts the current output of the PIN photodiode to a voltage and amplifies it. The output is a linear voltage that is proportional to the optical input over an input range of less than 1.0 µW to 175 µW peak (1.225 V typical output voltage swing), providing a dynamic operating range of 23 dB with very low pulse width distortion.

The HFD3222 is designed to operate on the ECL standard of -5.2 volts and has very good Power Supply Rejection Ratio (typically 20 dB at 10 MHz), making it highly immune to noise pickup. It can also be operated with a +5 Volt supply although some PSRR performance will be sacrificed at less than 1 MHz.

The receiver output is a proportional analog voltage, providing cost-effective design flexibility. The user can tailor the circuit design to the particular application, using inexpensive external components to perform the conversion to the needed logic levels. This allows for an optimized design, making maximum use of the power budget for a given data rate/transmission distance configuration.



FIBER052.DIM

Pinout

- 1. Shield*
- 5. Shield*
- Output
- 6. Vcc
- 3. V_{EE}
- 7. V_{EE} 8. Shield*
- * Shield pins are common and electrically connected to the metal ST* barrel. For optimum EMI shielding, these pins should be connected to chassis ground. Signal ground and chassis ground should be bridged with a high Q 0.01 μF capacitor at the fiber optic component.

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HFD3222

125 MHz PIN Plus Preamplified Analog Receiver

ELECTRO-OPTICAL CHARACTERISTICS (VEE = -5.2 V, Tc= 25°C unless otherwise stated)

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNITS | TEST CONDITIONS |
|---------------------------------|--------------------------------|------|-------------|------|-------|---|
| Responsivity | А | | | | mV/μW | f = 50MHz, P _{IN} = 100µW peak, |
| | | | | | | $\lambda = 850 \text{ nm}$, 62.5 μ m core fiber |
| T = 25°C | | 5.3 | 7.5 | 9.6 | | |
| -40 < T < +85°C | | 4.5 | | 11.5 | | |
| Input Power | P _{IN} (Peak) | | | | μW | f = 50 MHz, λ = 850 nm PWD = 2.5 ns |
| T = 25°C | | | | 175 | | 1 |
| -40 < T < +85°C | | | | 150 | | ì |
| DC Output Voltage (1) | Vooc | -4.5 | -3.6 | -3.0 | ٧ | P _{IN} < 0.1 µW |
| Power Supply Current | lcc | | 9 | 15 | mA | RLOAD = 0 |
| Rise/Fall Time | t _R /t _F | | | | en | f = 10MHz, P _{IN} = 100μW peak λ = 850 nm |
| T = 25°C | | | 3.6 | 4.5 | | K = 000 HH |
| 1 = 25 C -40 < T < +85°C | | | 3.6 | 6.3 | | |
| Pulse Width Distortion | PWD | | 0.2 | 1.5 | ns | f = 50MHz, P _{IN} = 150μW peak, |
| ruise Width Distortion | FWD | | Ų. <u>Z</u> | 1.5 | 113 | $\lambda = 850 \text{ nm}$ |
| Bandwidth | BW | | 125 | | MHz | $\lambda = 850 \text{ nm}$, R = 0.707 R max. |
| RMS Noise Output Voltage | VNO | | 120 | | mV | P _{IN} = 0 μW, 75 MHz, 3 pole |
| nivis Noise Output Voltage | VNO | | 0.52 | 0.58 | IIIV | Bessel filter on output |
| | | | 0.52 | 0.70 | | No filter on output |
| Output PSRR | | | 20 | 0.70 | dB | f = 10 MHz |
| Output Pann Output Overshoot | | | 10 | 13 | % | P _{IN} = 10 μW |
| Output Resistance | | | 20 | טו | ñ | f = 50 MHz |
| | D. | | 20 74 | 79 | nW | P _{IN} = 0 μW, 75 MHz, 3 pole |
| RMS Input Noise Power (2) | PNI | | 74 | 79 | 1100 | Bessel filter on output |
| | | | -41.3 | -41 | dBm | P _{IN} = 0 µW, 75 MHz, 3 pole |
| | P _{Ni} | | -41.3 | -41 | ubiii | Bessel filter on output |
| | | | | | | Dessei inter on output |

Notes

2. Photodiode has 600 µm diameter microlens for optical coupling.

ABSOLUTE MAXIMUM RATINGS

(Tcase = 25°C unless otherwise noted)

Storage temperature

-55 to +85°C

Operating temperature

-40 to +85°C

Lead solder temperature

260°C for 10 s

Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

RECOMMENDED OPERATING CONDITIONS

Operating temperature

-40 to +85°C

Supply voltage (Vcc - VEE)

-40 to +65 C

Optical signal input

1.0 to 125 μW

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^{1.} Quiescent output voltage (Vopc) is -3.6 V typical. Dynamic output voltage swing is below the quiescent output voltage (Vo= Vopc+ R x Pin).

HFD3222

125 MHz PIN Plus Preamplified Analog Receiver

| ORDER GUIDE | | | | | |
|--|-----------------|--|--|--|--|
| Description | Catalog Listing | | | | |
| 125 MHz PIN Plus Preamplifier Analog Receiver | HFD3222-002 | | | | |

CAUTION

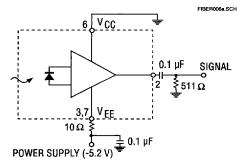
The inherent design of this component causes it to be sensitive to electrostatic discharge (ESD). To prevent ESD-induced damage and/or degradation to equipment, take normal ESD precautions when handling this product.

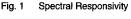


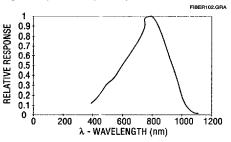
FIBER INTERFACE

Honeywell detectors are designed to interface with multimode fibers with sizes (core/cladding diameters) ranging from 50/125 to 200/230 microns. Honeywell performs final tests using 62.5/125 micron core fiber. The fiber chosen by the end user will depend upon a number of application issues (distance, link budget, cable attenuation, splice attenuation, and safety margin). The 50/125 and 62.5/125 micron fibers have the advantages of high bandwidth and low cost, making them ideal for higher bandwidth installations. The use of 100/140 and 200/230 micron core fibers results in greater power being coupled by the transmitter, making it easier to splice or connect in bulkhead areas. Optical cables can be purchased from a number of sources.

CIRCUIT DIAGRAM







SWITCHING WAVEFORM

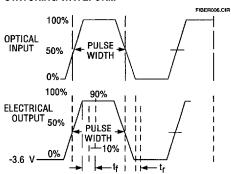
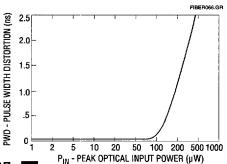


Fig. 2 Pulse Width Distortion vs Optical Input Power



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