

DATA SHEET

HM 65795

**64 K x 4 WITH SEPARATE I/O
HIGH SPEED CMOS SRAM**

FEATURES

- **FAST ACCESS TIME**
INDUSTRIAL/MILITARY : 35/45/55 ns (max)
COMMERCIAL : 25/35/45/55 ns (max)
- **LOW POWER CONSUMPTION**
ACTIVE : 385 mW (typ)
STANDBY : 100 mW (typ)
- **WIDE TEMPERATURE RANGE :**
- 55°C TO + 125°C
- **300 MILS WIDTH PACKAGE**
- **TTL COMPATIBLE INPUTS AND OUTPUTS**
- **ASYNCHRONOUS**
- **CAPABLE OF WITHSTANDING GREATER THAN 2000 V ELECTROSTATIC DISCHARGE**
- **SEPARATE INPUTS/OUTPUTS**
- **SINGLE 5 VOLT SUPPLY**

4

DESCRIPTION

The HM-65795 is a high speed CMOS static RAM organised as 65,536 x 4 bit. It is manufactured using MHS high performance CMOS technology.

Access times as fast as 25 ns are available with maximum power consumption of only 330 mW.

The HM-65795 features fully static operation requiring no external clocks or timing strobes. The automatic power-down feature reduces the power consumption by 71 % when the circuit is deselected.

Easy memory expansion is provided by an active low chip select (CS) and three state drivers.

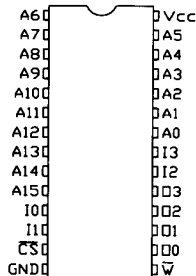
All inputs and outputs of the HM-65795 are TTL compatible and operate from single 5 V supply thus simplifying system design.

The HM-65795 is processed following the test methods of MIL STD 883C.

PACKAGES

Plastic 300 mils, 28 pins, DIL.
Ceramic 300 mils, 28 pins, DIL.
SO/SOJ 300 and 330 mils, 28 pins, DIL.
Tape and Reel Service

Pinout DIL 28 pins (top view)



LOGIC SYMBOL

