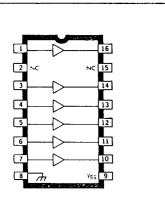
FLUORESCENT DISPLAY DRIVER



Dwg. No. A-9641A

ABSOLUTE MAXIMUM RATINGS at $T_A = +25$ °C

Supply Voltage, $V_{\rm BB}$ 85 V Input Voltage, $V_{\rm IN}$ 20 V Output Current, $I_{\rm OUT}$ -40 mA Operating Temperature Range,

T_A-55°C to +125°C

Caution: The high input impedance of this device makes it susceptible to static discharge damage' associated with handling and testing. Techniques similar to those used for handling MOS devices should be employed. Consisting of eight NPN Darlington output stages and associated common-emitter input stages, the UDS6118H interfaces between low-level logic and vacuum fluorescent displays. It is furnished in an 18-pin hermetic dual in-line side-brazed package. Reverse-bias burn-in and 100% high-reliability screening to MIL-STD-883, Class B, are standard.

Representative electrical characteristics (over an ambient temperature range of -20°C to +85°C) for the commercial type UDN6118A are shown in Section 3. Complete, detailed technical information on the UDS6118H is available on request.

FEATURES

- Digit or Segment Driver
- Low Input Current
- Integral Output Pull-Down Resistors
- High Output Breakdown Voltage
- Single or Split Supply Operation

Always order by complete part number: UDS6118H883.