

XR-68C681 XR-88C681

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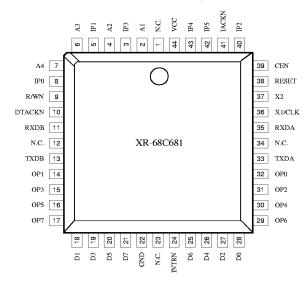
DUAL UNIVERSAL ASYNCHRONOUS RECEIVER AND TRANSMITTER

DESCRIPTION

The XR-68C681/88C681 is a Dual Universal Asynchronous Receiver and Transmitter with 3 bytes of receive FIFO. The XR-68C681/88C681 is the improved version of the Signetics SCC2681 Dual UART with additional features.

The XR-68C681/88C681 provides independent receive and transmit operating speeds that can be selected as one of the eighteen fixed baud rates, a 16 X clock derived from a programmable counter/timer, or an external 1X or 16X clock. The baud rate generator and counter/timer can operate directly from a crystal or from external clock input. The XR-68C681/88C681 provides a power-down mode in which the oscillator is stopped but the register contents are stored.

PLCC Package



Plastic-Dip Package

VCC A140 IP3 2 39 IP4 3 IP5 A2 38 IP1 IACKN 37 IP2 5 36 A3 A4 6 35 CSN IPO 7 34 RESETN R/WN 8 33 X2 DTACKN 9 X1/CLK 32 RXDB RXDA 10 31 TXDA TXDB 11 30 OP0 OP1 12 29 OP3 13 OP2 28 OP5 14 27 OP4 OP7 OP6 15 26 D1 16 25 DO D2 D3 17 24 D5 18 D4 23 D7 22 D6 GND 21 INTRN 20

FEATURES

- Pin to pin and functional compatible to SCC2681
- 3 bytes receive FIFO
- Full duplex asynchronous transmit receive operation
- Programmable character lengths (5, 6, 7, 8)
- Parity, framing, and over run error
- Programmable 16-bit timer/counter
- On-chip crystal oscillator
- TTL compatible inputs, outputs
- Single interrupt output with seven selectable interrupting conditions
- 18 fixed baud rates from 50Hz to 1M

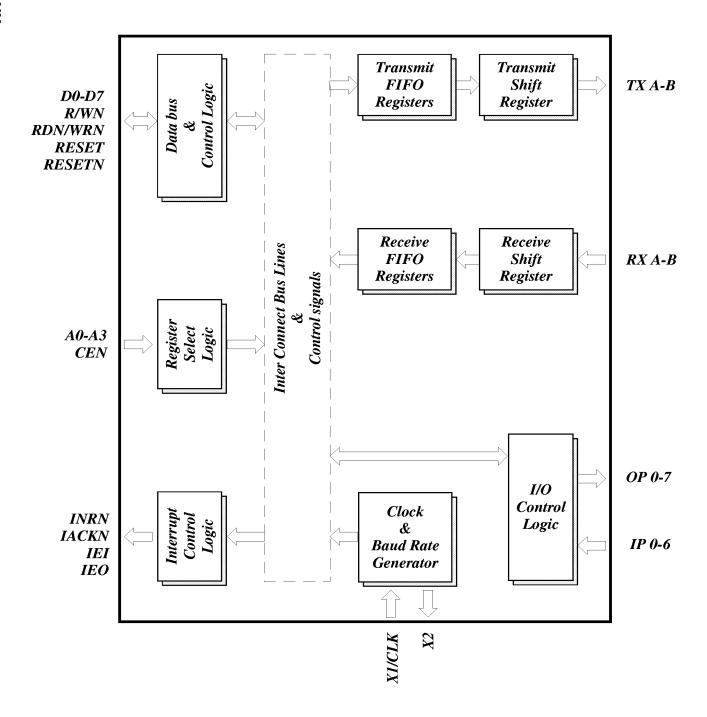
ORDERING INFORMATION

Part number	Package (Operating temperature
XR-88C681CP28	Plastic-Dip	0° C to + 70° C
XR-88C681CP40	Plastic-Dip	0° C to + 70° C
XR-88C681CJ44	PLCC	0° C to + 70° C
XR-68C681CP40	Plastic-Dip	0° C to + 70° C
XR-68C681CJ44	PLCC	0° C to + 70° C

Rev. 1.0 3-305

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XR-68C681/88C681 BLOCK DIAGRAM



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