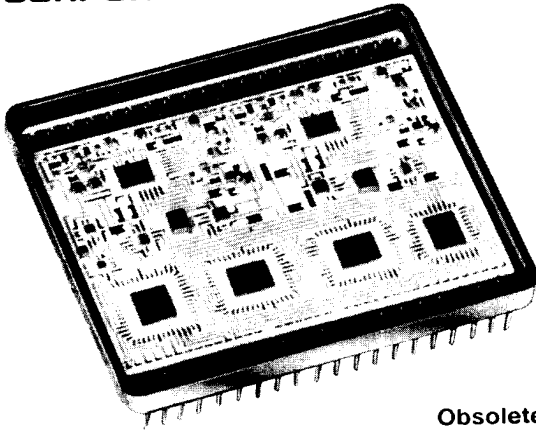




DUAL REDUNDANT MIL-STD-1553 RTU HYBRID

FEATURES

- *SMALL SIZE*
- *LOW POWER*
- *FULL COMPLIANCE WITH MIL-STD-1553B*
- *ALL MODE CODES*
- *WRAP AROUND TEST*
- *TIME OUT*
- *32 WORD FIFO*

Obsolete - for new designs see BUS-65127

DESCRIPTION

The BUS-65125 is a dual redundant MIL-STD-1553 Remote Terminal Unit (RTU) packaged in a small 1.9" x 2.1" hybrid. It is fully compliant with MIL-STD-1553B and features implementation of all mode codes, wrap-around test, and terminal fail-safe time out.

The BUS-65125 contains all of the functions required to provide a terminal interface between a dual redundant MIL-STD-1553 serial data bus and a subsystem tri-state data highway. Utilizing a high level of integration, the BUS-65125 has monolithic transceivers (two)

encoder/decoders (two), protocol sequencer, and 32 word FIFO.

The dual transceivers in the BUS-65125 are completely independent, including separate power and grounds, and feature low power dissipation, transmitter short circuit protection and low bit error rate (BER). The 32 word FIFO in the BUS-65125 minimizes subsystem overhead by allowing a full message to be transferred to the terminal without waiting for serial transmission, and by checking the validity of a full message before transfer to the subsystem.

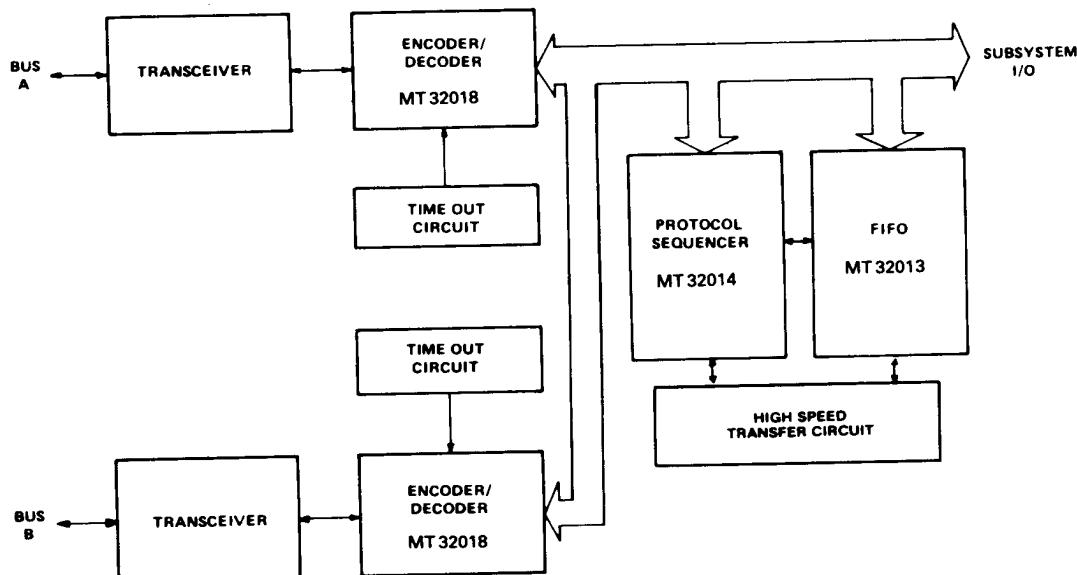


FIGURE 1. BLOCK DIAGRAM

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