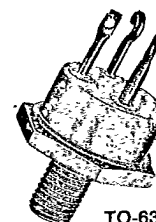


20 AMP/NPN**Pirgo Silicon Planar
Power Transistors**

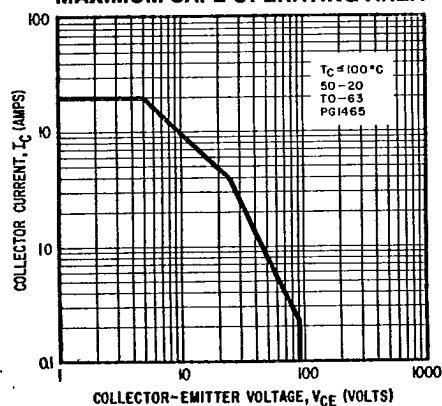
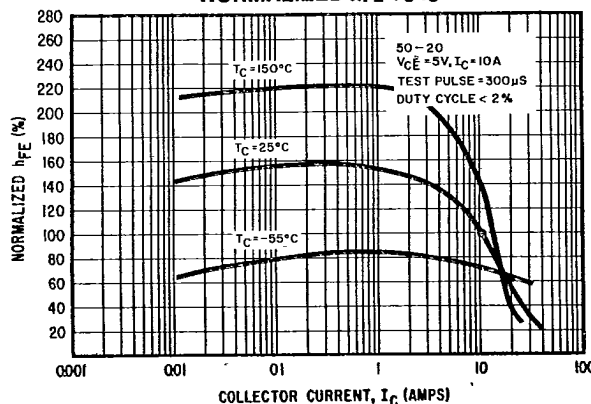
**20 AMP
90 VOLT
100 WATT
40 MHz
TO-63**



TO-63

- Linear h_{FE} from 50 mA to 20 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 40$ MHz (typical)
- High voltage, $BV_{CEO(sus)}$ to 90 volts

NPN Type	Package	BV_{CBO}	$BV_{CEO(sus)}$	BV_{EBO}	h_{FE}			h_{FE}		$V_{CE(sat)}$		C_{obMax} pf	P_{DW} @ 100°C
					Min	Max	@ $I_C A$	Min	@ $I_C A$	Max	@ $I_C A$		
2N3597	TO-63	60	30	8	40	120	10	10	20	1.5	20	700	100
2N3598	TO-63	80	50	8	40	120	10	10	20	1.5	20	700	100
2N3599	TO-63	100	70	8	40	120	10	10	20	1.5	20	700	100
PG1465	TO-63	100	90	8	40	120	10	10	20	1.5	20	700	100
PG1466	TO-63	60	30	8	20	60	10	5	20	2.0	20	700	100
PG1467	TO-63	80	50	8	20	60	10	5	20	2.0	20	700	100
PG1468	TO-63	100	70	8	20	60	10	5	20	2.0	20	700	100
PG1469	TO-63	100	90	8	20	60	10	5	20	2.0	20	700	100
PG1470	TO-63	60	30	8	100	300	10	15	20	1.5	20	700	100
PG1471	TO-63	80	50	8	100	300	10	15	20	1.5	20	700	100
PG1472	TO-63	100	70	8	100	300	10	15	20	1.5	20	700	100
PG1473	TO-63	100	90	8	100	300	10	15	20	1.5	20	700	100
PG1474	TO-63	60	30	6	20	60	10			.7	10	700	100
PG1475	TO-63	80	50	6	20	60	10			.7	10	700	100
PG1476	TO-63	100	70	6	20	60	10			.7	10	700	100
PG1477	TO-63	100	90	6	20	60	10			.7	10	700	100
PG1478	TO-63	60	30	6	40	120	10			.7	10	700	100
PG1479	TO-63	80	50	6	40	120	10			.7	10	700	100
PG1480	TO-63	100	70	6	40	120	10			.7	10	700	100
PG1481	TO-63	100	90	6	40	120	10			.7	10	700	100
PG1482	TO-63	60	30	6	100	300	10			.7	10	700	100
PG1483	TO-63	80	50	6	100	300	10			.7	10	700	100
PG1484	TO-63	100	70	6	100	300	10			.7	10	700	100
PG1485	TO-63	100	90	6	100	300	10			.7	10	700	100

MAXIMUM SAFE OPERATING AREA**NORMALIZED h_{FE} Vs I_C** 

PG -- 0110-16x